

# SD101A (1N6263)...SD101C

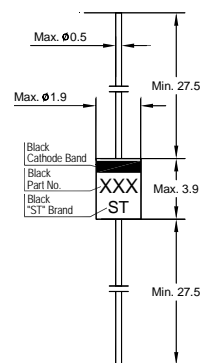
## SILICON SCHOTTKY BARRIER DIODES for general purpose applications

The SD101 Series is a metal on silicon Schottky barrier device which is protected by a PN junction guard ring.

The low forward voltage drop and fast switching make it ideal for protection of MOS devices, steering, biasing and coupling diodes for fast switching and low logic level applications.

The SD101A is equivalent to the 1N6263.

This diode is also available in MiniMELF case with type designation LL101A, B, C.



Glass Case DO-35  
Dimensions in mm

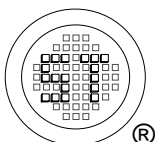
### Absolute Maximum Ratings ( $T_a = 25\text{ }^\circ\text{C}$ )

Parameter	Symbol	Value	Unit	
Peak Reverse Voltage	SD101A SD101B SD101C	$V_{RRM}$	60 50 40	V
Power Dissipation	$P_{tot}$	400 <sup>1)</sup>	mW	
Maximum Single Cycle Surge, 10 s Square wave	$I_{FSM}$	2	A	
Junction Temperature	$T_j$	200	$^\circ\text{C}$	
Storage Temperature Range	$T_s$	- 55 to + 200	$^\circ\text{C}$	

<sup>1)</sup> Valid provided the leads direct at the case are kept at ambient temperature.

### Characteristics at $T_a = 25\text{ }^\circ\text{C}$

Parameter	Symbol	Min.	Max.	Unit	
Reverse Breakdown Voltage at $I_R = 10\text{ }\mu\text{A}$	SD101A SD101B SD101C	$V_{(BR)R}$	60 50 40	V	
Forward Voltage at $I_F = 1\text{ mA}$	SD101A SD101B SD101C	$V_F$	- - -	0.41 0.4 0.39	V
at $I_F = 15\text{ mA}$	SD101A SD101B SD101C	$V_F$	- - -	1 0.95 0.9	V
Reverse Leakage Current at $V_R = 50\text{ V}$ at $V_R = 40\text{ V}$ at $V_R = 30\text{ V}$	SD101A SD101B SD101C	$I_R$	-	200	nA
Junction Capacitance at $V_R = 0\text{ V}$ , $f = 1\text{ MHz}$	SD101A SD101B SD101C	$C_{tot}$	- - -	2 2.1 2.2	pF
Reverse Recovery Time at $I_F = I_R = 5\text{ mA}$ , recover to $0.1 I_R$	$t_{rr}$	-	1	ns	



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ISO/TS 16949 : 2002  
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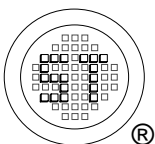
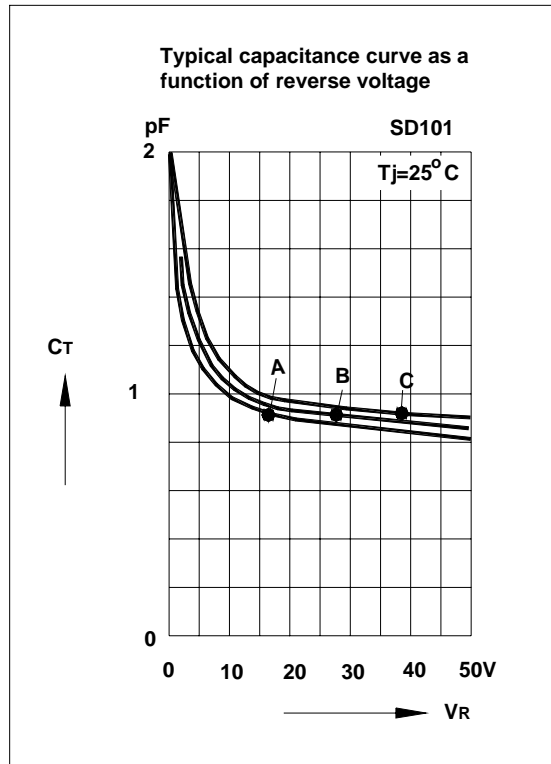
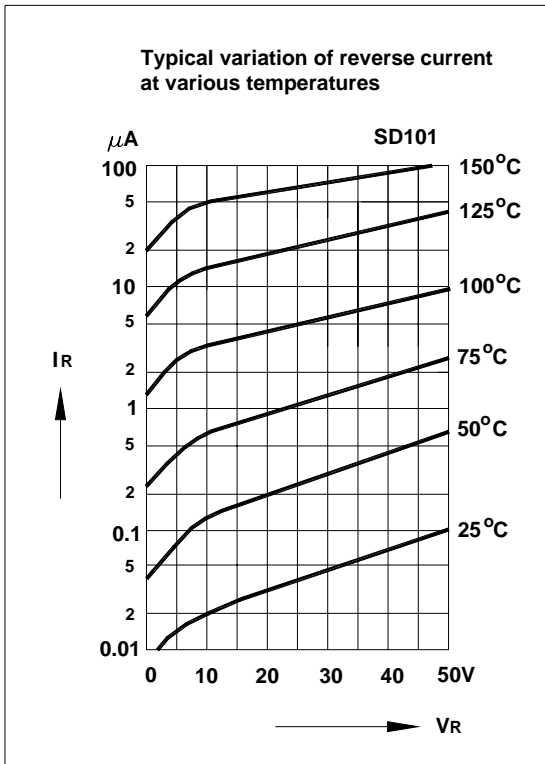
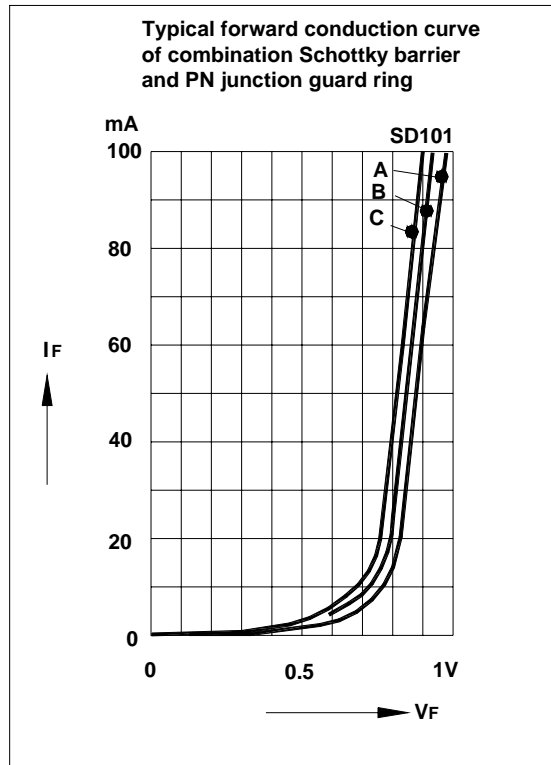
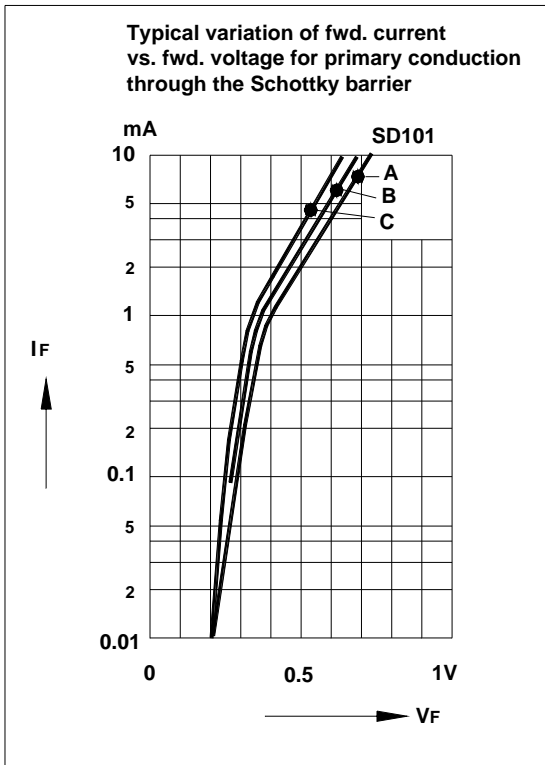
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Certificate No. 71116



ISO 9001:2000  
Certificate No. 0506098

Dated : 23/06/2007

# SD101A (1N6263)...SD101C



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