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SPECIFICATION

MODULE NO.: WF43QTIBEDBC0#

General Specifications

Item	Dimension	Unit
Size	4.3	inch
Dot Matrix	480 x RGBx272(TFT)	dots
Module dimension	106.7 x 83.98 x 8.76	mm
Active area	95.04 x 53.86	mm
Dot pitch	0.066 x 0.198	mm
LCD type	TFT, Normally White, Transmissive	
View Direction	12 o'clock	
Gray Scale Inversion Direction	6 o'clock	
Backlight Type	LED, Normally White	
Controller IC	SSD1963	
Interface	Digital 8080 family MPU 8bit/16bit	
With /Without TP	With CTP	
Surface	No Anti-Glare	

*Color tone slight changed by temperature and driving voltage.

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	℃
Storage Temperature	TST	-30	—	+80	℃

Electrical Characteristics

Operating conditions: (CON3.Pin1=GND, Pin2=VDD)

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For LCM	VDD	—	3.0	3.1	3.3	V
Supply Current For LCM	IDD	—	—	210	350	mA

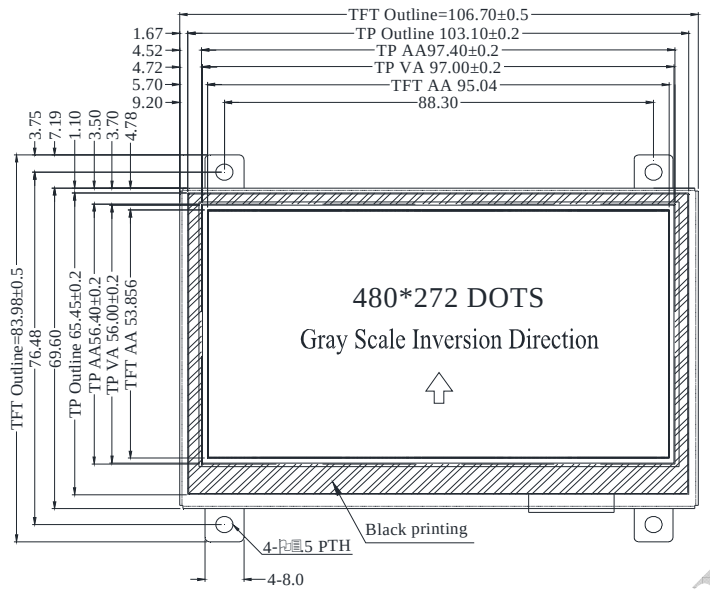
Backlight driving conditions (CON3.Pin33,34=VLED-, Pin35,36=VLED+)

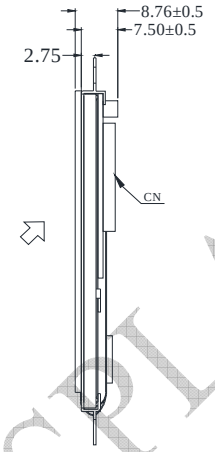
Parameter	Symbol	Min.	Typ.	Max.	Unit
Operation Current For LED Driver	VLED+=3.3V	270	-	405	mA
Power Consumption	VLED+=3.3V	891	-	1337	mW
Supply Voltage For LED Driver	VLED+	3.3	-	5	V
LED Life Time		-	50,000	-	Hr

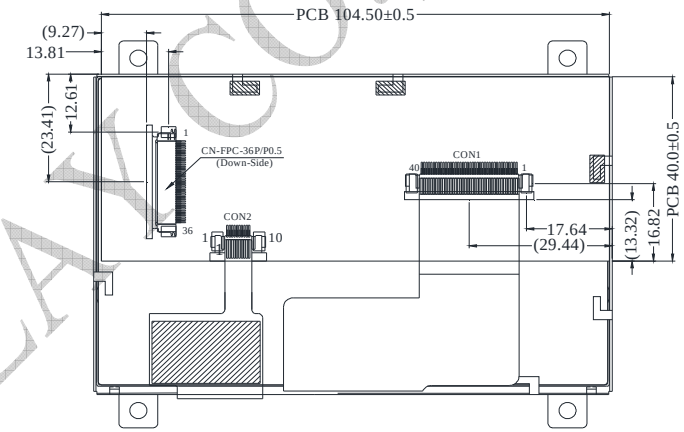
Interface

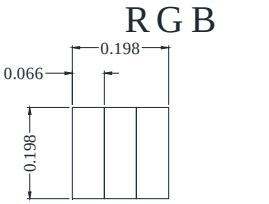
Pin	Symbol	Function
1	GND	System round pin of the IC. Connect to system ground.
2	VDD	Power Supply : +3.3V
3	BL_E	Backlight control signal , H: On \ L: Off
4	D/C	Data/Command select
5	WR	Write strobe signal
6	RD	Read strobe signal
7	D0	Data bus
8	D1	Data bus
9	D2	Data bus
10	D3	Data bus
11	D4	Data bus
12	D5	Data bus
13	D6	Data bus
14	D7	Data bus
15	D8	Data bus (When select 8bits mode, this pin is NC)
16	D9	Data bus (When select 8bits mode, this pin is NC)
17	D10	Data bus (When select 8bits mode, this pin is NC)
18	D11	Data bus (When select 8bits mode, this pin is NC)
19	D12	Data bus (When select 8bits mode, this pin is NC)
20	D13	Data bus (When select 8bits mode, this pin is NC)
21	D14	Data bus (When select 8bits mode, this pin is NC)
22	D15	Data bus (When select 8bits mode, this pin is NC)
23	NC	No connection
24	CTP_INT	External interrupt to the host
25	CS	Chip select
26	RESET	Hardware reset
27	DIP ON	Display control H: On \ L:Off
28	NC	No connection
29	CTP_SCL	Active low I2C clock input
30	CTP_SDA	Data input I2C data input and output
31	CTP_RST	External Reset, Low is active
32	CTP_WAKE	External interrupt from the host
33	VLED-	VLED- for B/L LED inverter (GND)
34	VLED-	VLED- for B/L LED inverter (GND)
35	VLED+	VLED+ for B/L LED inverter (+3.3V)
36	VLED+	VLED+ for B/L LED inverter (+3.3V)

Contour Drawing









SCALE 1:100

The non-specified tolerance of dimension is ±0.3mm.

PIN NO.	SYMBOL
1	GND
2	VDD
3	BL_E
4	D/C
5	WR
6	RD
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	DB8
16	DB9
17	DB10
18	DB11
19	DB12
20	DB13
21	DB14
22	DB15
23	NC
24	CTP_INT
25	CS
26	RESET
27	DISP_ON
28	NC
29	CTP_SCL
30	CTP_SDA
31	CTP_RST
32	CTP_WAKE
33	VLED-
34	VLED-
35	VLED+
36	VLED+