

1N5400 THRU 1N5408(FORM LEAD)

PLASTIC SILICON RECTIFIERS

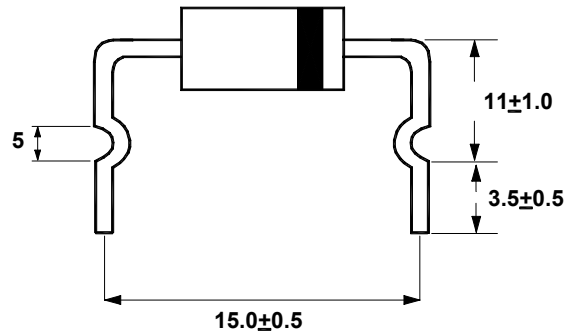
Reverse Voltage – 50 to 1000 Volts

Forward Current – 3.0 Ampere

DO-201AD(3A-5)

Features

- Low forward voltage
- High current capability
- Low leakage current
- High surge capability
- Low cost



Absolute Maximum Ratings and Characteristics

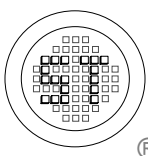
Ratings at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%.

	1N 5400 (3A-5)	1N 5401 (3A-5)	1N 5402 (3A-5)	1N 5403 (3A-5)	1N 5404 (3A-5)	1N 5405 (3A-5)	1N 5406 (3A-5)	1N 5407 (3A-5)	1N 5408 (3A-5)	Units	
*Maximum Recurrent Peak Reverse Voltage	50	100	200	300	400	500	600	800	1000	V	
*Maximum RMS Voltage	35	70	140	210	280	350	420	560	700	V	
*Maximum DC Blocking Voltage at T _A = 150 °C	50	100	200	300	400	500	600	800	1000	V	
*Maximum Average Forward Rectified Current 0.5", (12.5 mm) Lead Length at T _A = 75 °C	3									A	
*Peak Forward Surge Current 8.3ms single half sine-wave	150									A	
*Maximum Forward Voltage at 3A Peak	1									V	
*Maximum Reverse Current, at Rated DC Blocking Voltage					10						uA
					500						uA
*Maximum Full Load Reverse Current , Full Cycle Average, 0.5" , (12.5 mm) Lead Length T _A = 105 °C	500									uA	
Typical Junction Capacitance (Note 1)	50									pF	
*Storage Temperature Range T _A	-65 to +175									°C	
*Operating Temperature Range T _J	-65 to +170									°C	

Notes:

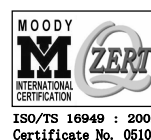
1. Measured at 1 MHz and applied reverse voltage of 4 V_{DC}.

* JEDEC Registered Value.



SEMTECH ELECTRONICS LTD.

(Subsidiary of Semtech International Holdings Limited, a company listed on the Hong Kong Stock Exchange, Stock Code: 724)



ISO/TS 16949 : 2002
Certificate No. 05103



ISO 14001
Certificate No. 7116



ISO 9001 : 2000
Certificate No. 5529-1999-00-002-001

Dated : 09/10/2003

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Fig. 1-TYPICAL FORWARD CHARACTERISTICS

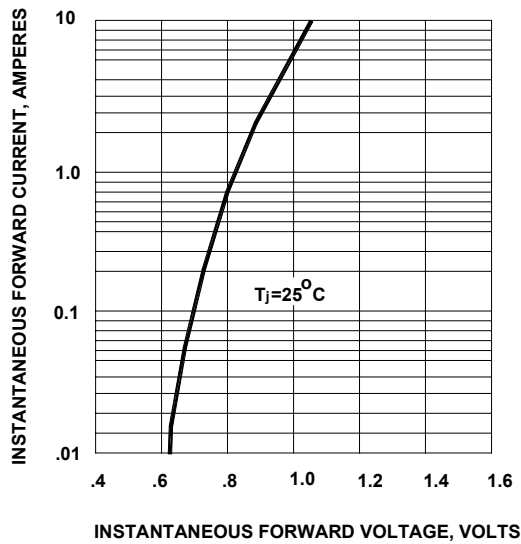


Fig. 2-PEAK FORWARD SURGE CURRENT

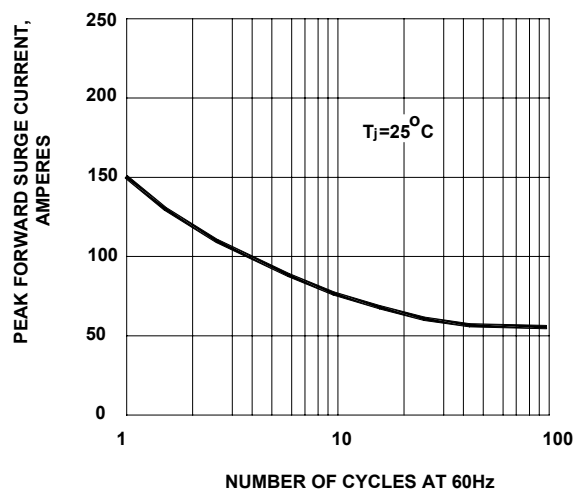


Fig. 3-FORWARD CURRENT DERATING CURVE

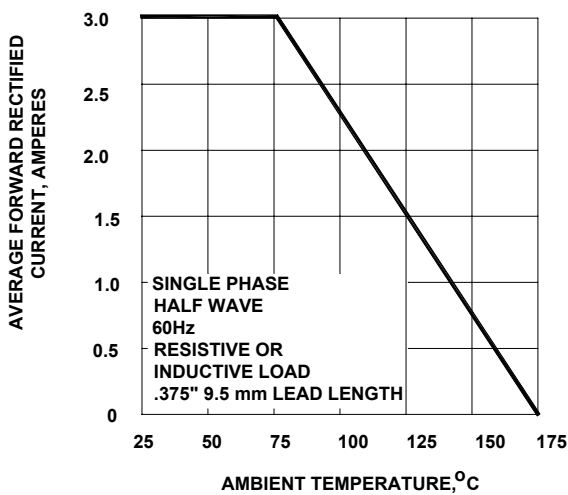
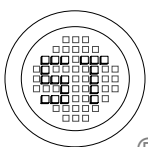
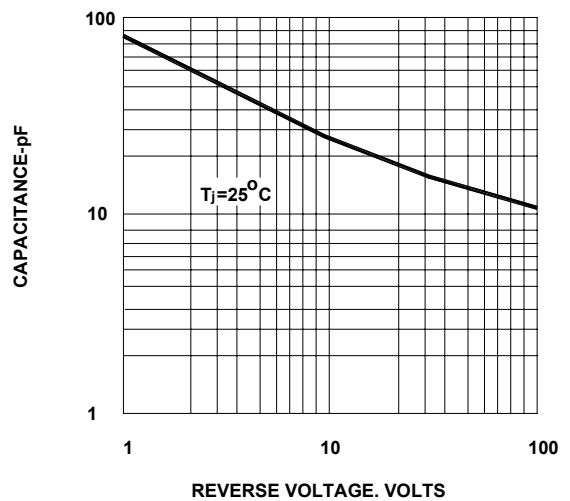
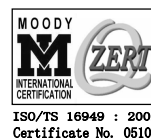


Fig. 4-TYPICAL JUNCTION CAPACITANCE



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