

### DC Input 16-Pin DMC-Isolator® Half Pitch Mini-Flat Phototransistor Optocoupler

#### **Features**

- High isolation 3750 VRMS
- Patented coplanar structure DMC-Isolator®
- Various CTR selection available
- DC input with transistor output
- Operating temperature range 55 °C to 125 °C
- External Creepage ≥ 5.0mm
- Distance Through Isolation ≥ 0.4mm
- Clearance Distance ≥ 5.0mm
- RoHS and REACH Compliance
- Halogen Free Compliance
- MSL class 1
- Regulatory Approvals
  - ✓ UL UL1577 (E364000)
  - ✓ VDE EN60747-5-5 (VDE0884-5)
  - ✓ CQC GB4943.1, GB8898 (19001231772)
  - ✓ IEC62368 (FI/41119)

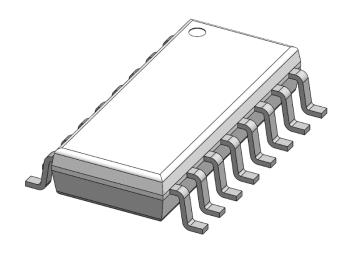
#### **Description**

The CTH247 series have four isolated channels, each channel contains a photo transistor optically coupled to an Infrared-emitting diode in a 16-lead half pitch Mini-Flat DMC-Isolator® package.

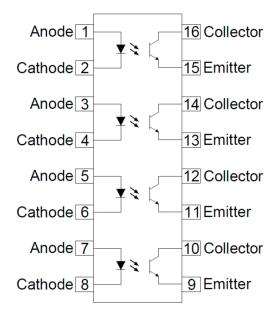
#### **Applications**

- DC-DC Converters
- Programmable controllers
- Telecommunication equipment
- Hybrid substrates that require high density mounting

#### **Package Outline**



#### **Schematic**





### DC Input 16-Pin DMC-Isolator® Half Pitch Mini-Flat Phototransistor Optocoupler

#### Absolute Maximum Ratings $\tau_A = 25$ °C, unless otherwise specified

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameters	Ratings	Units	Notes
Viso	Isolation voltage (AC, 1 minute, 40 ~ 60% R.H.)	3750	V <sub>RMS</sub>	
T <sub>OPR</sub>	Operating temperature	-55 ~ +125	°C	
T <sub>STG</sub>	Storage temperature	-55 ~ +150	°C	
T <sub>SOL</sub>	Soldering temperature (For 10 seconds)	260	°C	1
Ртот	Total power dissipation	200	mW	
Emitter		•	•	
I <sub>F</sub>	Forward current	50	mA	2
I <sub>F(TRANS)</sub>	Peak transient current (≤1µs P.W,300pps)	1	А	2
V <sub>R</sub>	Reverse voltage	6	V	2
P <sub>D</sub>	Power dissipation	70	mW	2
TJ	Junction temperature	125	°C	2
Detector		•	•	
Pc	Power dissipation	100	mW	2
B <sub>VCEO</sub>	Collector-Emitter Breakdown Voltage	80	V	2
Bveco	Emitter-Collector Breakdown Voltage	7	V	2
lc	Collector Current	50	mA	2

#### Notes

- 1. For reflow process
- 2. Each Channel



# DC Input 16-Pin DMC-Isolator® Half Pitch Mini-Flat Phototransistor Optocoupler

CTH247 Series

#### Electrical Characteristics $\tau_A = 25$ °C, unless otherwise specified

#### **Emitter Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
VF	Forward voltage	I <sub>F</sub> =10mA	-	1.24	1.4	V	
I <sub>R</sub>	Reverse Current	V <sub>R</sub> = 6V	-	-	5	μΑ	
Cin	Input Capacitance	f= 1MHz	-	10	30	pF	

#### **Detector Characteristics**

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
B <sub>VCEO</sub>	Collector-Emitter Breakdown	I <sub>C</sub> = 0.1mA	80	-	-	V	
Bveco	Emitter-Collector Breakdown	I <sub>E</sub> = 0.1mA	7	-	-	V	
ICEO	Collector-Emitter Dark Current	V <sub>CE</sub> = 20V, I <sub>F</sub> =0mA	-	-	100	nA	

#### **Transfer Characteristics**

Symbol	Paramete	rs	Test Conditions	Min	Тур	Max	Units	Notes
		CTH247		50	-	600		
CTD	Current Transfer	CTH247A		80	-	160	%	
CTR	Ratio	CTH247B	I <sub>F</sub> = 5mA, V <sub>CE</sub> = 5V	130	-	260	76	
		CTH247C		200	-	400		
\/	Collector-Emitter Saturation		I- 20m A I- 1m A		0.1	0.2	V	
VCE(SAT)	Voltage		I <sub>F</sub> = 20mA, I <sub>C</sub> = 1mA	-	0.1	0.2	V	
Rio	Isolation Resistance		V <sub>IO</sub> = 500V <sub>DC</sub>	5x10 <sup>10</sup>	-	-	Ω	
C <sub>IO</sub>	Isolation Capacitance		f= 1MHz	-	0.5	1	pF	

#### **Switching Characteristics**

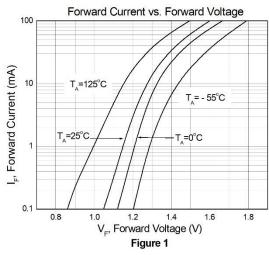
Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
t <sub>r</sub>	Rise Time	Io- 2m/ Vor- 2V B 1000	-	6	-	0	
t <sub>f</sub>	Fall Time	Ic= 2mA, V <sub>CE</sub> = 2V, R <sub>L</sub> = 100Ω	-	8	-	μS	

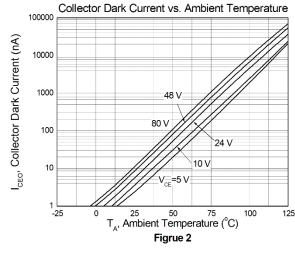


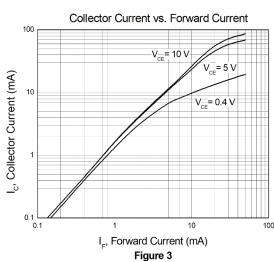


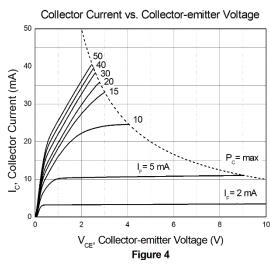
### Phototransistor Optocoupler

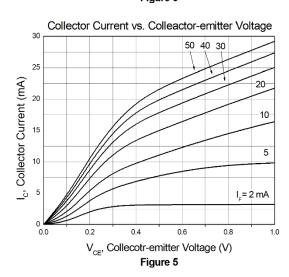
#### Typical Characteristic Curves T<sub>A</sub> = 25°C, unless otherwise specified

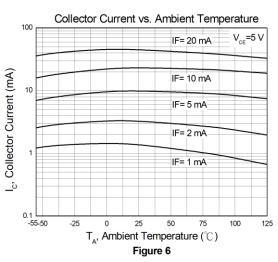








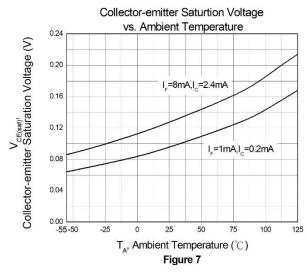


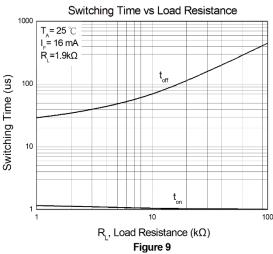


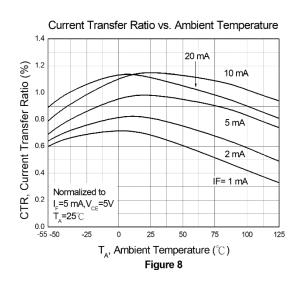


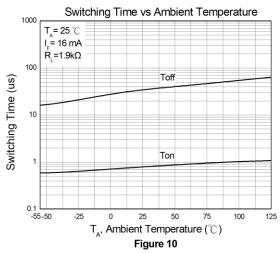
### DC Input 16-Pin DMC-Isolator® Half Pitch Mini-Flat Phototransistor Optocoupler

#### Typical Characteristic Curves $T_A = 25$ °C, unless otherwise specified (Continued)











## CTH247 Series DC Input 16-Pin DMC-Isolator® Half Pitch Mini-Flat

#### **Phototransistor Optocoupler**

#### **Test Circuit**

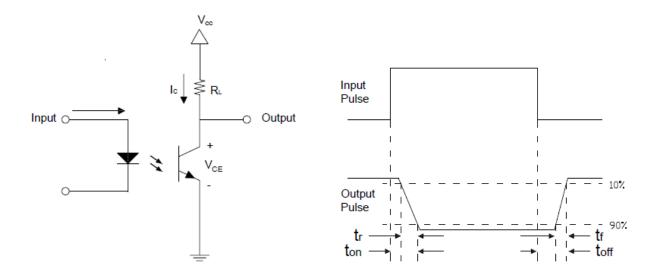
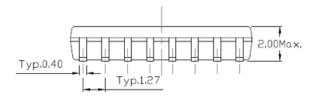


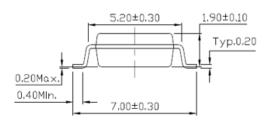
Figure 11: Switching Time Test Circuits

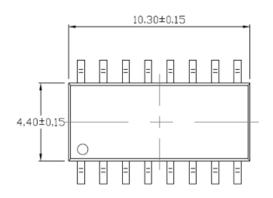


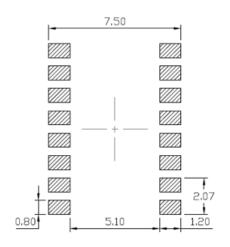
### DC Input 16-Pin DMC-Isolator® Half Pitch Mini-Flat Phototransistor Optocoupler

#### Package Dimension Dimensions in mm unless otherwise stated

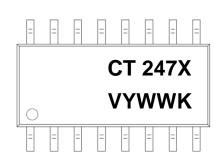








#### **Marking Information**



#### Note:

CT : Denotes "CT Micro"

247 : Part Number

X : CTR Rank Option(Blank, A, B or C)

V : VDE Safety Mark OptionY : One Digit Year CodeWW : Two Digit Work WeekK : Manufacturing Code



### CTH247 Series DC Input 16-Pin DMC-Isolator® Half Pitch Mini-Flat

#### **Phototransistor Optocoupler**

#### **Ordering Information**

#### CTH247X (V)(Z)

CT = Denotes "CT Micro"

H247 = Part Number

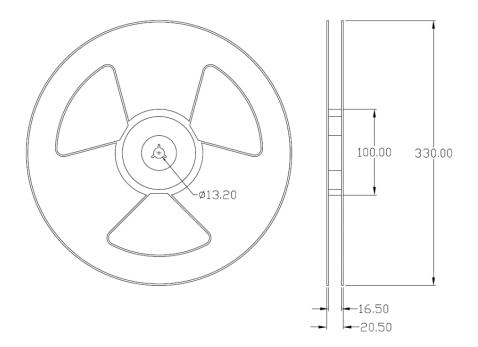
X = CTR Rank Option (Blank, A, B or C)V = VDE Safety Mark Option (Blank or V)

Z = Tape and Reel Option (T1 or T2)

Option	Option Description			
T1	Surface Mount Lead Forming – With Option 1 Taping	2000 Units/Reel		
T2	Surface Mount Lead Forming – With Option 1 Taping	2000 Units/Reel		

#### Reel Dimension All dimensions are in mm, unless otherwise stated

#### Option T1/T2



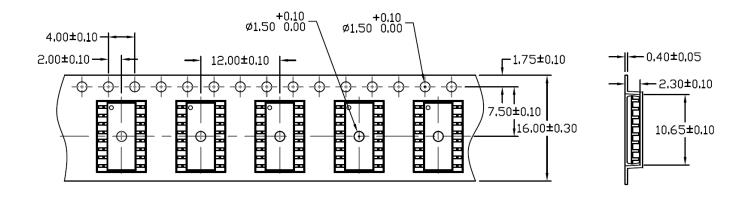


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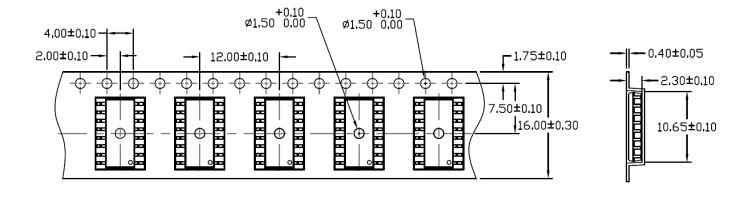
#### **Phototransistor Optocoupler**

#### Carrier Tape Specifications Dimensions in mm unless otherwise stated

#### **Option T1**



#### **Option T2**







### DC Input 16-Pin DMC-Isolator® Half Pitch Mini-Flat Phototransistor Optocoupler

#### Solderability spec (Follow the JEDEC standard JESD22-B102)

Reflow Soldering: Immersed surface, other than the end of pin as cut-surface, must be covered by solder.

Solder-Bath: More than 95% of the electrode must be covered with solder.

#### **Wave soldering (Follow the JEDEC standard JESD22-A111)**

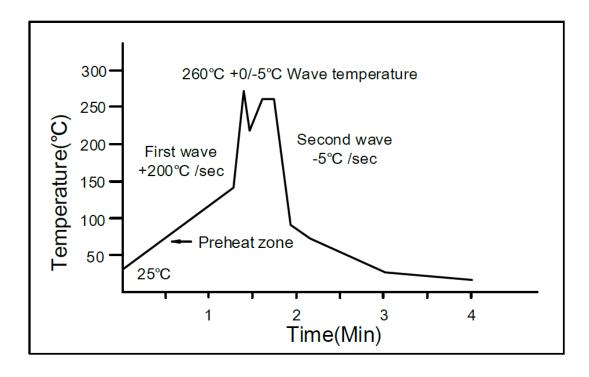
One time soldering is recommended within the condition of temperature.

Temperature: 260+0/-5°C.

Time: 10 sec.

Preheat temperature: 25 to 140°C.

Preheat time: 30 to 80 sec.



#### Iron soldering (Follow the standard MIL-STD 202G, Method 210F)

Allow single lead soldering in every single process.

One time soldering is recommended. Temperature: 350±10°C

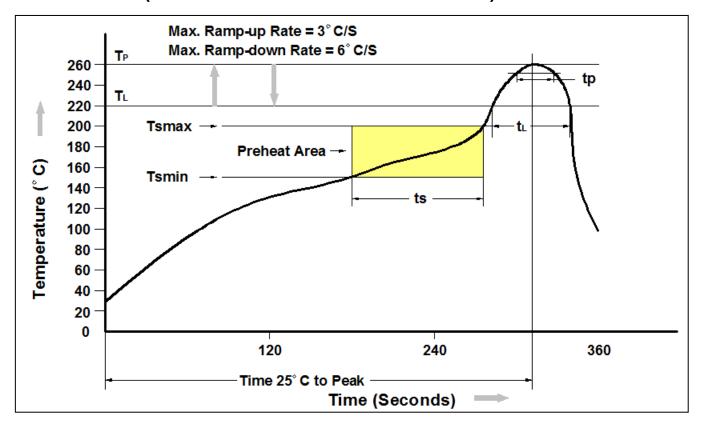
Time: 5 sec max.





### DC Input 16-Pin DMC-Isolator® Half Pitch Mini-Flat Phototransistor Optocoupler

#### Reflow Profile (Follow the JEDEC standard J-STD-020)



Profile Feature	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	150°C
Temperature Max. (Tsmax)	200°C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds
Ramp-up Rate (t∟ to t⊳)	3°C/second max.
Liquidous Temperature (T <sub>L</sub> )	217°C
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> )	60 – 150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (t <sub>P</sub> ) within 5°C of 260°C	30 seconds
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.



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