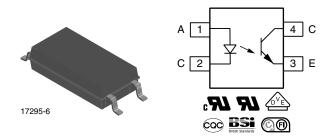
VOL618A

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Vishay Semiconductors

Optocoupler, Phototransistor Output, Low Input Current, 4 Pin LSOP, Long Creepage Mini-Flat Package



DESCRIPTION

The VOL618A has a GaAs infrared emitting diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 4 pin LSOP wide body package.

It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling device is designed for signal transmission between two electrically separated circuits.

FEATURES

Low profile package

- High collector emitter voltage, V_{CEO} = 80 V
- Isolation test voltage, 5000 V_{BMS}
- Isolation voltage V_{IORM} = 1050 V_{peak}
- Low coupling capacitance
- High common mode transient immunity
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

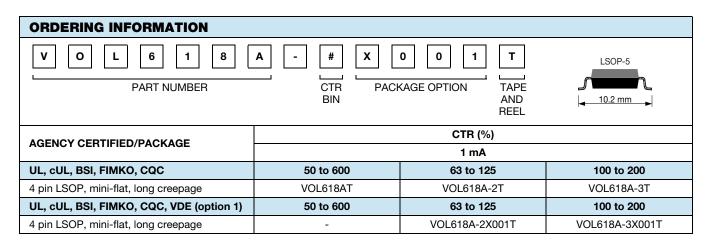
APPLICATIONS

- Telecom
- Industrial controls
- Battery powered equipment
- Office machines
- Programmable controllers

AGENCY APPROVALS

(All parts are certified under base model VOL618A)

- UL1577, file no. E76222
- cUL CSA 22.2 bulletin 5A, double protection
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- BSI: EN 60065:2002, EN 60950-1:2006
- FIMKO EN60950-1
- CQC: GB8898-2011, GB4943.1-2011



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Reverse voltage		V _R	6	V		
Power dissipation		P _{diss}	100	mW		
Forward current		I _F	60	mA		
Forward surge current	t _p < 10 μs	I _{FSM}	1.5	A		
Junction temperature		Тj	125	°C		
OUTPUT						
Collector emitter voltage		V _{CEO}	80	V		
Emitter collector voltage		V _{ECO}	7	V		
Collector current		Ι _C	50	mA		
	t _p /T = 0.5, t _p < 10 ms	Ι _C	100	mA		
Power dissipation		P _{diss}	150	mW		
Junction temperature		Тj	125	°C		
COUPLER						
Total power dissipation		P _{tot}	250	mW		
Storage temperature range		T _{stg}	-55 to +125	°C		
Ambient temperature range		T _{amb}	-55 to +110	°C		
Soldering temperature ⁽¹⁾	≤ 10 s	T _{sld}	260	°C		

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted devices.

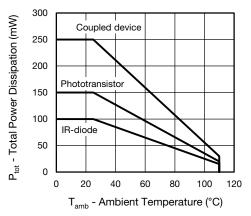


Fig. 1 - Total Power Dissipation vs. Ambient Temperature

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT	•			•	•	•	
Forward voltage	I _F = 5 mA		V _F	-	1.16	1.5	V
Capacitance	V _R = 0 V, f = 1 MHz		Co	-	45	-	pF
Reverse current	V _R = 6 V		I _R	-	-	100	μA
OUTPUT							
Collector emitter leakage current	V _{CE} = 10 V, I _F = 0 A		I _{CEO}	-	10	200	nA
Collector emitter capacitance	$V_{CE} = 5 V$, f = 1 MHz		C _{CE}	-	7	-	pF
COUPLER							
O III	$I_{\rm C} = 0.32 \text{ mA}, I_{\rm F} = 1 \text{ mA}$	VOL618A-2	V _{CEsat}	-	0.25	0.4	V
Collector emitter saturation voltage	$I_{\rm C} = 0.5 \text{ mA}, I_{\rm F} = 1 \text{ mA}$	VOL618A-3	V _{CEsat}	-	0.25	0.4	V
	I _C = 0.8 mA, I _F = 1 mA	VOL618A-4	V _{CEsat}	-	0.25	0.4	V
Coupling capacitance	f = 1 MHz		C _C	-	0.25		pF

Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

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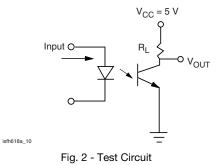
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CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I _C /I _F	I _F = 1 mA, V _{CE} = 5 V	VOL618A	CTR	50	-	600	%
		VOL618A-2	CTR	63	-	125	%
		VOL618A-3	CTR	100	-	200	%

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn on time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	t _{on}	-	6	-	μs
Rise time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	t _r	-	3.5	-	μs
Turn off time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	t _{off}	-	5.5	-	μs
Fall time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	t _f	-	5	-	μs



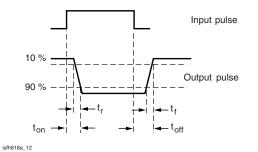


Fig. 3 - Test Circuit and Waveforms

SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Partial discharge test voltage - routine test	100 %, t _{test} = 1 s	V _{pd}	2	kV _{peak}		
Partial discharge test voltage -	t _{Tr} = 60 s, t _{test} = 10 s,	V _{IOTM}	8	kV _{peak}		
lot test (sample test)	(see figure 4)	V _{pd}	1.68	kV _{peak}		
Isolation test voltage between emitter and detector	t = 1 min	V _{ISO}	5000	V _{RMS}		
Insulation voltage		VIORM	1050	V _{peak}		
Insulation resistance	$V_{IO} = 500 V_{DC}, T_{amb} = 25 \text{ °C}$	R _{IO}	10 ¹²	Ω		
	$V_{IO} = 500 V_{DC}, T_{amb} = 100 \ ^{\circ}C$	R _{IO}	10 ¹¹	Ω		
	V _{IO} = 500 V _{DC} , T _{amb} = 150 °C (construction test only)	R _{IO}	10 ⁹	Ω		
Safety rating - maximum input current		I _{si}	130	mA		
Safety rating - maximum power dissipation		P _{SO}	265	mW		
Rated impulse voltage		V _{IOTM}	8	kV		
Safety rating - maximum ambient temperature		T _{si}	150	°C		
Comparative tracking index		CTI	275	mm		
Clearance distance			8	mm		
Creepage distance			8	mm		
Insulation distance (internal)			0.4	mm		

Note

• According to DIN EN 60747-5-5 (VDE 0884), § 7.4.3.8.2, (see figure 4). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.



300 $\begin{array}{l} Phototransistor \\ P_{SO} \ (mW) \end{array}$ 250 200 150 100 IR-diode 50 _{si} (mA) 0 125 150 0 25 50 75 100 T_{si} - Safety Temperature (°C) Fig. 4 - Derating Diagram

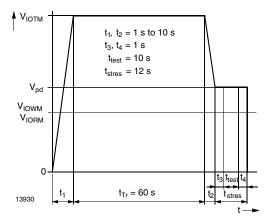
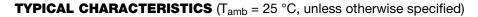


Fig. 5 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-5



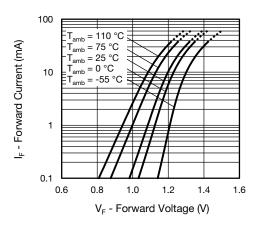


Fig. 6 - Forward Voltage vs. Forward Current

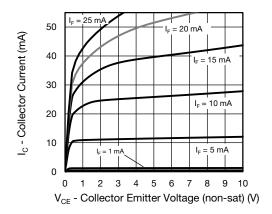


Fig. 7 - Collector Current vs. Collector Emitter Voltage (non-saturated)

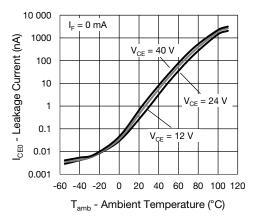


Fig. 8 - Collector Emitter Current vs. Ambient Temperature

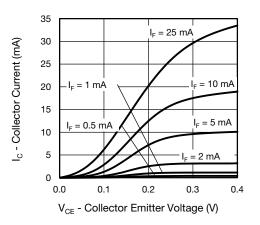
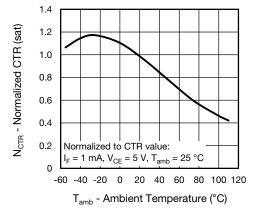


Fig. 9 - Collector Current vs. Collector Emitter Voltage (saturated)

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Fig. 10 - Normalized Current Transfer Ratio vs. Ambient Temperature (saturated)

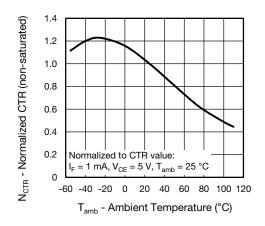


Fig. 11 - Normalized Current Transfer Ratio vs. Ambient Temperature (non-saturated)

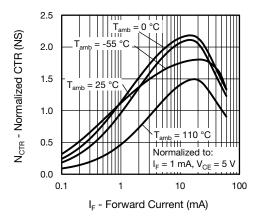


Fig. 12 - Current Transfer Ratio vs. Forward Current (saturated) Normalized to 1 mA at 25 $^\circ\text{C}$

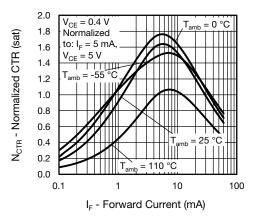


Fig. 13 - Current Transfer Ratio vs. Forward Current (non-saturated) Normalized to 1 mA at 25 °C

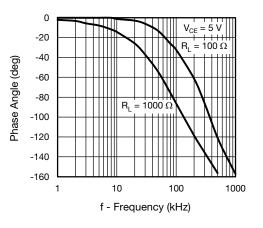


Fig. 14 - f_{CTR} vs. Phase Angle

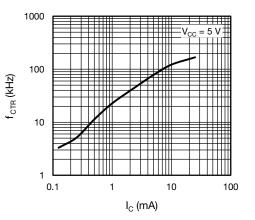
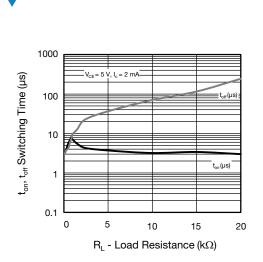


Fig. 15 - Frequency (-3 dB) vs. Collector Current

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Fig. 16 - Switching Time vs. Load Resistance

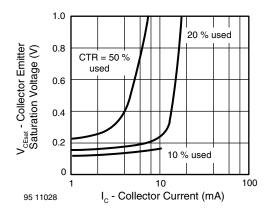


Fig. 17 - Collector Emitter Saturation Voltage vs. Collector Current

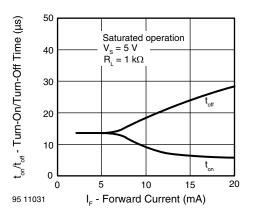


Fig. 18 - Turn-On/Turn-Off Time vs. Forward Current

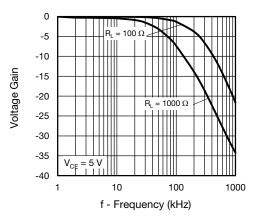
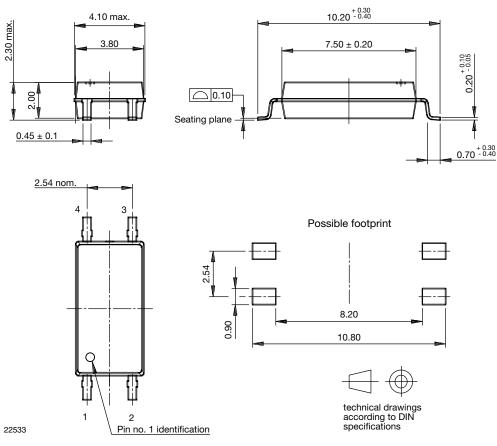


Fig. 19 - Voltage Gain vs. Cut-off Frequency

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PACKAGE DIMENSIONS (in millimeters)

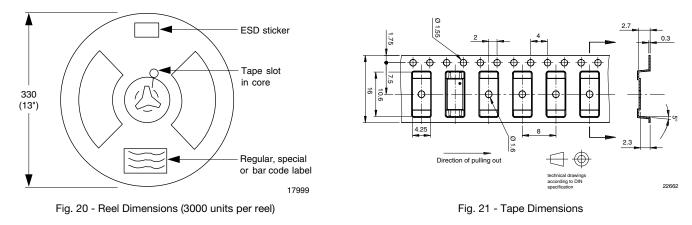


PACKAGE MARKING (example of VOL618A-3X001T)

Notes

- Only option 1 is reflected in the package marking with the characters "X1".
- Tape and reel suffix (T) is not part of the package marking.

TAPE AND REEL DIMENSIONS (in millimeters)



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VOL618A



Vishay Semiconductors

SOLDER PROFILE

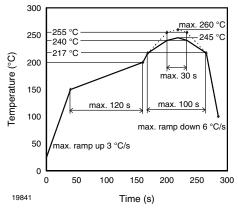


Fig. 22 - Lead (Pb)-free Reflow Solder Profile according to J-STD-020

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited Conditions: $T_{amb} < 30$ °C, RH < 85 % Moisture sensitivity level 1, according to J-STD-020



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