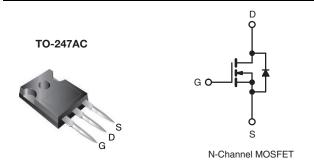


Vishay Siliconix

# **Power MOSFET**

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	600			
R <sub>DS(on)</sub> (Ω)	V <sub>GS</sub> = 10 V	0.21		
Q <sub>g</sub> (Max.) (nC)	180	180		
Q <sub>gs</sub> (nC)	61	61		
Q <sub>gd</sub> (nC)	85	85		
Configuration	Sing	Single		



#### **FEATURES**

· Superfast body diode eliminates the need for external diodes in ZVS applications



• Lower gate charge results in simpler drive requirements

- Enhanced dV/dt capabilities offer improved ruggedness
- · Higher gate voltage threshold offers improved noise immunity
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### Note

This datasheet provides information about parts that are RoHS-compliant and / or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details.

#### **APPLICATIONS**

- Zero voltage switching (SMPS)
- Telecom and server power supplies
- Uninterruptible power supplies
- Motor control applications

ORDERING INFORMATION		
Package	TO-247AC	
Lead (Pb)-free	IRFP26N60LPbF	
	SiHFP26N60L-E3	
SnPb	IRFP26N60L	
	SiHFP26N60L	

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER			SYMBOL	LIMIT	UNIT
Drain-Source Voltage			V <sub>DS</sub>	600	V
Gate-Source Voltage			$V_{GS}$	± 30	7 v
Continuous Drain Current	V at 10 V	T <sub>C</sub> = 25 °C		26	
Continuous Drain Current	V <sub>GS</sub> at 10 V	Γ <sub>C</sub> = 100 °C	l <sub>D</sub>	17	A
Pulsed Drain Current <sup>a</sup>			I <sub>DM</sub>	100	
Linear Derating Factor				3.8	W/°C
Single Pulse Avalanche Energy b			E <sub>AS</sub>	570	mJ
Repetitive Avalanche Current <sup>a</sup>			$I_{AR}$	26	Α
Repetitive Avalanche Energy <sup>a</sup>			E <sub>AR</sub>	47	mJ
Maximum Power Dissipation $T_C = 25  ^{\circ}C$		°C	P <sub>D</sub>	470	W
Peak Diode Recovery dV/dt <sup>c</sup>			dV/dt	21	V/ns
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	
Soldering Recommendations (Peak Temperature) <sup>d</sup> for 10 s			300		
Mounting Torque	6 22 or M2	oorou		10	lbf ⋅ in
Mounting Torque 6-32 or M3 screw			1.1	N · m	

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11). b. Starting T<sub>J</sub> = 25 °C, L = 1.7 mH, R<sub>g</sub> = 25  $\Omega$ , I<sub>AS</sub> = 26 A, dV/dt = 21 V/ns (see fig. 12). c. I<sub>SD</sub>  $\leq$  26 A, dI/dt  $\leq$  480 A/µs, V<sub>DD</sub>  $\leq$  V<sub>DS</sub>, T<sub>J</sub>  $\leq$  150 °C.

- d. 1.6 mm from case.



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THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R <sub>thJA</sub>	-	40	
Case-to-Sink, Flat, Greased Surface	R <sub>thCS</sub>	0.24	-	°C/W
Maximum Junction-to-Case (Drain)	R <sub>thJC</sub>	-	0.27	

PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> =	= 0 V, I <sub>D</sub> = 250 μA	600	-	-	V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	e to 25 °C, I <sub>D</sub> = 1 mA	-	0.33	-	V/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	· V <sub>GS</sub> , I <sub>D</sub> = 250 μA	3.0	-	5.0	V
Gate-Source Leakage	I <sub>GSS</sub>	,	V <sub>GS</sub> = ± 30 V	-	-	± 100	nA
Zava Cata Valtaga Dvais Cuvvant		V <sub>DS</sub> =	= 600 V, V <sub>GS</sub> = 0 V	-	-	50	μΑ
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 480 V	', V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C	-	-	2.0	mA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 10 A <sup>b</sup>	-	0.21	0.25	Ω
Forward Transconductance	9 <sub>fs</sub>	$V_{DS}$	= 50 V, I <sub>D</sub> = 16 A	13	-	-	S
Dynamic							
Input Capacitance	C <sub>iss</sub>		$V_{GS} = 0 V$ ,	-	5020	-	
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 25 V, V <sub>DS</sub> = 25 V, f = 1.0 MHz, see fig. 5		-	450	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>			-	34	-	
Effective Output Capacitance	Coss eff.			-	230	-	Pi
Effective Output Capacitance (Energy related)	C <sub>oss</sub> eff. (ER)	$V_{GS} = 0 \text{ V}$ $V_{DS} = 0 \text{ V to } 480 \text{ V}^{c}$	-	170	-		
Total Gate Charge	Qg			-	-	180	
Gate-Source Charge	Q <sub>gs</sub>	$V_{GS} = 10 \text{ V}$ $I_D = 26 \text{ A}, V_{DS} = 480 \text{ see fig. 7 and } 15^{10} \text{ sec. } 7 \text{ \text$		-	-	61	nC
Gate-Drain Charge	$Q_{gd}$		occ ng. 7 and 10	-	-	85	
Turn-On Delay Time	t <sub>d(on)</sub>			-	31	-	
Rise Time	t <sub>r</sub>	$V_{DD} = 300 \text{ V}, I_{D} = 26 \text{ A},$ $R_{a} = 4.3 \Omega, V_{GS} = 10 \text{ V}$		-	110	-	]
Turn-Off Delay Time	t <sub>d(off)</sub>	n <sub>g</sub> =	ig. 11a and 11b b	-	47	-	ns
Fall Time	t <sub>f</sub>			-	42	-	
<b>Drain-Source Body Diode Characteristic</b>	cs						
Continuous Source-Drain Diode Current	I <sub>S</sub>	MOSFET sym showing the		-	-	26	A
Pulsed Diode Forward Current <sup>a</sup>	I <sub>SM</sub>	integral revers p - n junction	~ <del>~</del>	-	-	100	
Body Diode Voltage	$V_{SD}$	T <sub>J</sub> = 25 °C	$I_{S} = 26 \text{ A}, V_{GS} = 0 \text{ V}^{\text{b}}$	-	-	1.5	V
Dada Diada Damara Daga Tiran		$T_J = 25 ^{\circ}\text{C}, I_F = 26 \text{A}$ $T_J = 125 ^{\circ}\text{C}, \text{dl/dt} = 100 \text{A/}\mu\text{s}^{\text{b}}$		-	170	250	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			-	210	320	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		°C, I <sub>F</sub> = 26 A, V <sub>GS</sub> = 0 V b	-	670 1050	1000 1570	nC
Reverse Recovery Current	I <sub>RRM</sub>	10 120	T <sub>.1</sub> = 25 °C	-	7.3	11	Α
Forward Turn-On Time	t <sub>on</sub>	Late de la de	rn-on time is negligible (turr	l on in day			1

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width  $\leq 300~\mu s;$  duty cycle  $\leq 2~\%.$
- c.  $C_{oss}$  eff. is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 % to 80 %  $V_{DS}$ .  $C_{oss}$  eff. (ER) is a fixed capacitance that stores the same energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 % to 80 %  $V_{DS}$ .

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### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

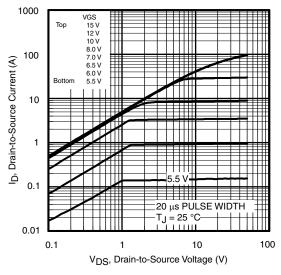


Fig. 1 - Typical Output Characteristics

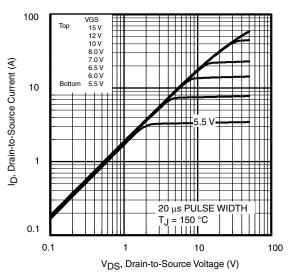


Fig. 2 - Typical Output Characteristics

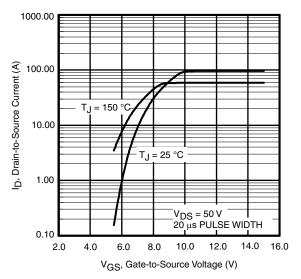


Fig. 3 - Typical Transfer Characteristics

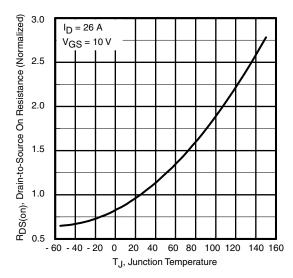


Fig. 4 - Normalized On-Resistance vs. Temperature



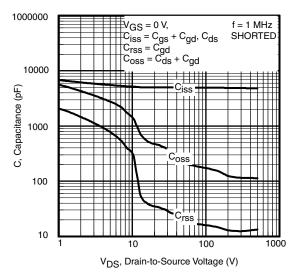


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

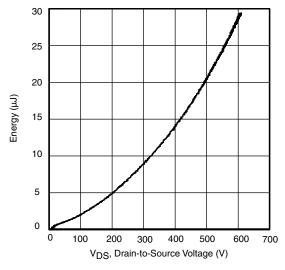


Fig. 6 - Typical Output Capacitance Stored Energy vs.V<sub>DS</sub>

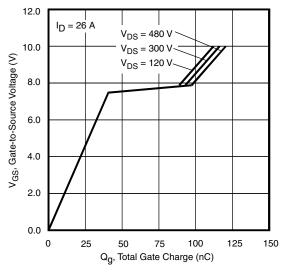


Fig. 7 - Typical Gate Charge vs. Gate-to-Source Voltage

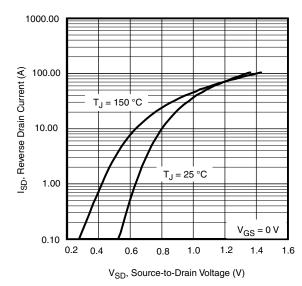


Fig. 8 - Typical Source-Drain Diode Forward Voltage



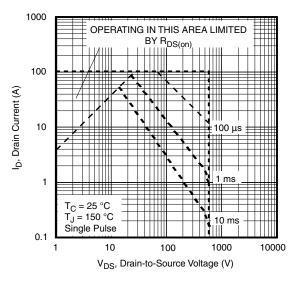


Fig. 9 - Maximum Safe Operating Area

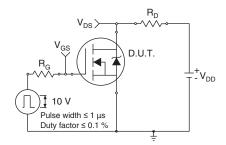


Fig. 11a - Switching Time Test Circuit

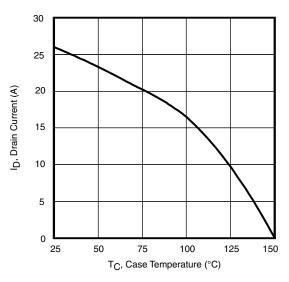


Fig. 10 - Maximum Drain Current vs. Case Temperature

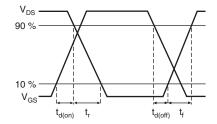


Fig. 11b - Switching Time Waveforms

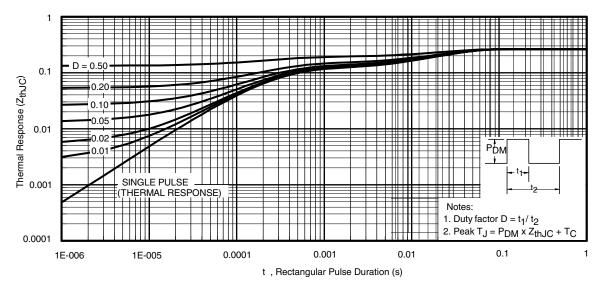


Fig. 12 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

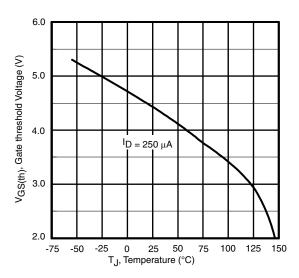


Fig. 13 - Threshold Voltage vs. Temperature

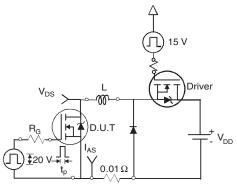


Fig. 14a - Unclamped Inductive Test Circuit

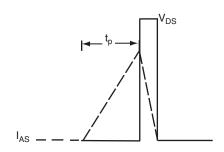


Fig. 14b - Unclamped Inductive Waveforms

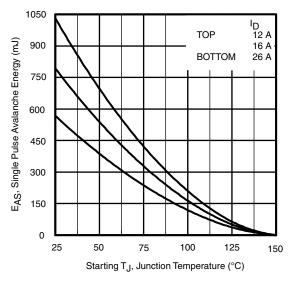


Fig. 14c - Maximum Avalanche Energy vs. Drain Current

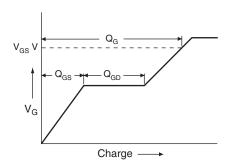


Fig. 15a - Basic Gate Charge Waveform

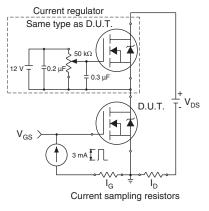
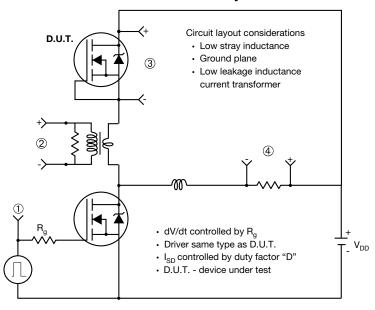


Fig. 15b - Gate Charge Test Circuit



#### Peak Diode Recovery dV/dt Test Circuit



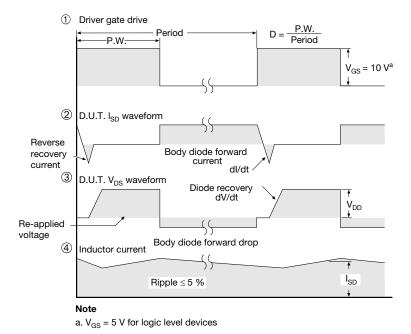


Fig. 16 - For N-Channel

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# **TO-247AC (High Voltage)**

#### **VERSION 1: FACILITY CODE = 9**







Section C--C,D--D,E--E

	MILLIN	IETERS	
DIM.	MIN.	MAX.	NOTES
Α	4.83	5.21	
A1	2.29	2.55	
A2	1.50	2.49	
b	1.12	1.33	
b1	1.12	1.28	
b2	1.91	2.39	6
b3	1.91	2.34	
b4	2.87	3.22	6, 8
b5	2.87	3.18	
С	0.55	0.69	6
c1	0.55	0.65	
D	20.40	20.70	4

	MILLIMETERS		
DIM.	MIN.	MAX.	NOTES
D1	16.25	16.85	5
D2	0.56	0.76	
E	15.50	15.87	4
E1	13.46	14.16	5
E2	4.52	5.49	3
е	5.44	BSC	
L	14.90	15.40	
L1	3.96	4.16	6
ØΡ	3.56	3.65	7
Ø P1	7.19 ref.		
Q	5.31	5.69	
S	5.54	5.74	

- (1) Package reference: JEDEC® TO247, variation AC
- (2) All dimensions are in mm
- (3) Slot required, notch may be rounded
- (4) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm per side. These dimensions are measured at the outermost extremes of the plastic body
- (5) Thermal pad contour optional with dimensions D1 and E1
- (6) Lead finish uncontrolled in L1
- (7) Ø P to have a maximum draft angle of 1.5° to the top of the part with a maximum hole diameter of 3.91 mm
- (8) Dimension b2 and b4 does not include dambar protrusion. Allowable dambar protrusion shall be 0.1 mm total in excess of b2 and b4 dimension at maximum material condition

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#### **VERSION 2: FACILITY CODE = Y**



	MILLIM	IETERS	
DIM.	MIN.	MAX.	NOTES
Α	4.58	5.31	
A1	2.21	2.59	
A2	1.17	2.49	
b	0.99	1.40	
b1	0.99	1.35	
b2	1.53	2.39	
b3	1.65	2.37	
b4	2.42	3.43	
b5	2.59	3.38	
С	0.38	0.86	
c1	0.38	0.76	
D	19.71	20.82	
D1	13.08	-	

	MILLIN		
DIM.	MIN.	MAX.	NOTES
D2	0.51	1.30	
E	15.29	15.87	
E1	13.72	-	
е	5.46	BSC	
Øk	0.2	0.254	
L	14.20	16.25	
L1	3.71	4.29	
ØР	3.51	3.66	
Ø P1	-	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51 BSC		

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c

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#### **VERSION 3: FACILITY CODE = N**



	MILLIMETERS		
DIM.	MIN.	MAX.	
Α	4.65	5.31	
A1	2.21	2.59	
A2	1.17	1.37	
b	0.99	1.40	
b1	0.99	1.35	
b2	1.65	2.39	
b3	1.65	2.34	
b4	2.59	3.43	
b5	2.59	3.38	
С	0.38	0.89	
c1	0.38	0.84	
D	19.71	20.70	
D1	13.08	-	

	MILLIMETERS		
DIM.	MIN.	MAX.	
D2	0.51	1.35	
E	15.29	15.87	
E1	13.46	-	
е	5.46	BSC	
k	0.254		
L	14.20	16.10	
L1	3.71	4.29	
N	7.62	BSC	
Р	3.56	3.66	
P1	=	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51 BSC		

ECN: E20-0545-Rev. F, 19-Oct-2020

DWG: 5971

- <sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")



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