Vishay Siliconix

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N-Channel 30 V (D-S) MOSFET

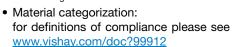


Marking code: P4

PRODUCT SUMMARY					
V _{DS} (V)	30				
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 10 \text{ V}$	0.060				
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 4.5 \text{ V}$	0.075				
Q _g typ. (nC)	2.1				
I _D (A) ^a	3.6				
Configuration	Single				

FEATURES

- TrenchFET® power MOSFET
- 100 % R_g tested

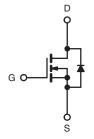




FREE

APPLICATIONS

• DC/DC converter



N-Channel MOSFET

ORDERING INFORMATION				
Package	SOT-23			
Lead (Pb)-free and halogen-free	Si2304DDS-T1-GE3			

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	30	V	
Gate-source voltage		V_{GS}	± 20	V	
Continuous drain current (T _J = 150 °C)	T _C = 25 °C		3.6 ^a		
	T _C = 70 °C	Τ , Γ	3.3		
	T _A = 25 °C	l _D	3.3		
	T _A = 70 °C	Ţ [2.7	Α	
Pulsed drain current		I _{DM}	15		
Continuous source-drain diode current	T _C = 25 °C		1.4		
	T _A = 25 °C	ls l	0.9 b, c		
Maximum power dissipation	T _C = 25 °C		1.7		
	T _C = 70 °C	T 5 [1.1	14/	
	T _A = 25 °C	P _D	1.1 ^{b, c}	W	
	T _A = 70 °C	†	0.7 b, c		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C	
Soldering recommendations (peak temperature) d, e			260	-0	

THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT		
Maximum junction-to-ambient b, d	t ≤ 5 s	R_{thJA}	90	115	°C/W	
Maximum junction-to-foot (drain)	Steady state	R_{thJF}	60	75	C/ VV	

Notes

- a. Package limited
- b. Surface mounted on 1" x 1" FR4 board
- c. t = 5 s
- d. Maximum under steady state conditions is 130 $^{\circ}\text{C/W}$



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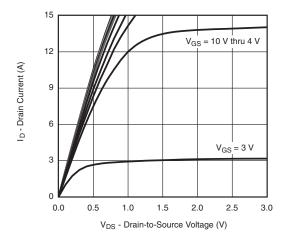
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static			<u>'</u>				
Drain-source breakdown voltage	V_{DS}	V _{GS} = 0 V, I _D = 250 μA	30	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	1 050 A	-	31	-		
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA	-	-5	-	mV/°C	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1.2	-	2.2	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
7		V _{DS} = 30 V, V _{GS} = 0 V	-	-	1	— uA	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C	-	-	10		
On-state drain current a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	10	-	-	Α	
D		$V_{GS} = 10 \text{ V}, I_D = 3.2 \text{ A}$	-	0.049	0.060	_	
Drain-source on-state resistance a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 2.8 \text{ A}$	-	0.061	0.075	Ω	
Forward transconductance a	9 _{fs}	$V_{DS} = 15 \text{ V}, I_D = 4.8 \text{ A}$	-	11	-	S	
Dynamic ^b			1	1			
Input capacitance	C _{iss}		-	235	-		
Output capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	45	-	pF	
Reverse transfer capacitance	C _{rss}		-	17	-	1	
Total gate charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.4 \text{ A}$	-	4.5	6.7	nC	
		· · · · · · · · · · · · · · · ·	-	2.1	3.2		
Gate-source charge	Q _{qs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 3.4 \text{ A}$	-	0.85	-		
Gate-drain charge	Q _{ad}			0.65	-		
Gate resistance	R _q	f = 1 MHz	0.8	4.4	8.8	Ω	
Turn-on delay time	t _{d(on)}		-	12	20		
Rise time	t _r	$V_{DD} = 15 \text{ V}, R_L = 5.6 \Omega,$	-	50	75	1	
Turn-off delay time	t _{d(off)}	$I_D \cong 2.7 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$	-	12	20		
Fall time	t _f		-	22	35		
Turn-on delay time	t _{d(on)}		-	5	10	ns	
Rise time	t _r	$V_{DD} = 15 \text{ V}, R_L = 5.6 \Omega,$	-	12	20	1	
Turn-off delay time	t _{d(off)}	$I_D \cong 2.7 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	10	15	1	
Fall time	t _f		-	5	10	1	
Drain-Source Body Diode Characteristi	cs		1	1			
Continuous source-drain diode current	Is	T _C = 25 °C	-	-	1.4		
Pulse diode forward current	I _{SM}		-	-	15	Α	
Body diode voltage	V _{SD}	$I_S = 2.7 \text{ A}, V_{GS} = 0 \text{ V}$	-	0.8	1.2	V	
Body diode reverse recovery time	t _{rr}		-	10	20	ns	
Body diode reverse recovery charge	Q _{rr}	$I_F = 2.7 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s},$	-	5	10	nC	
Reverse recovery fall time	t _a	$T_J = 25 ^{\circ}C$	-	6	-		
Reverse recovery rise time	t _b		_	4	-	ns	

Notes

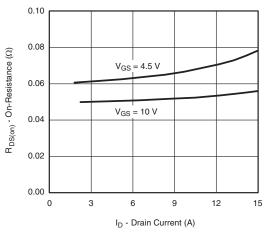
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

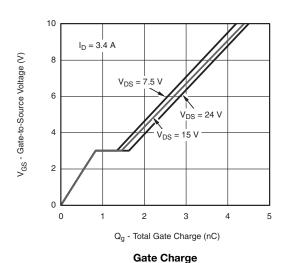


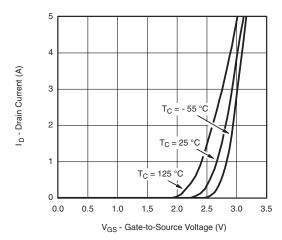


Output Characteristics

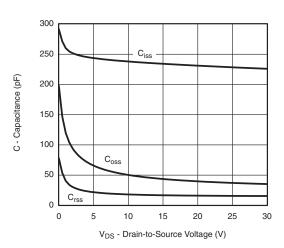


On-Resistance vs. Drain Current and Gate Voltage

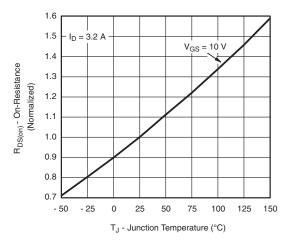




Transfer Characteristics

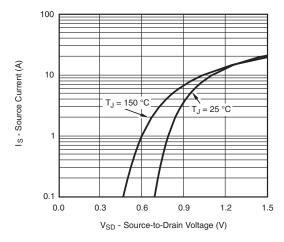


Capacitance

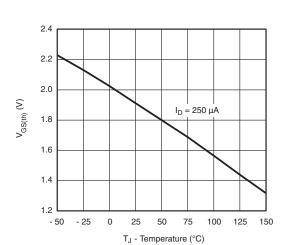


On-Resistance vs. Junction Temperature

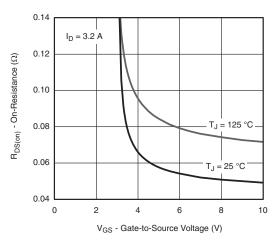




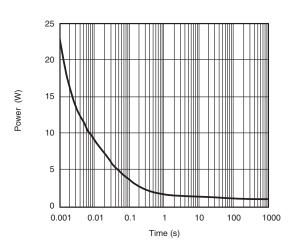
Source-Drain Diode Forward Voltage



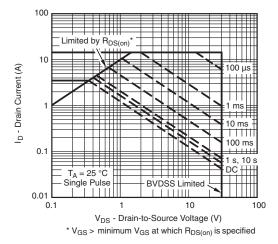
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

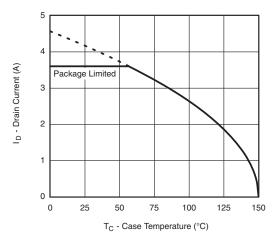


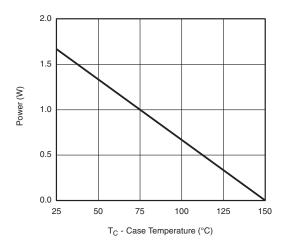
Single Pulse Power



Safe Operating Area, Junction-to-Ambient







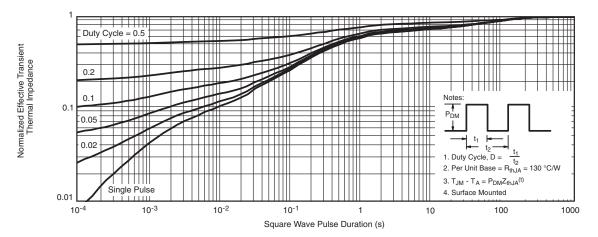
Current Derating ^a

Power Derating

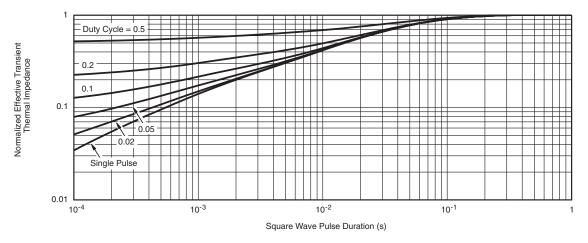
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?65175.

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SOT-23 (TO-236): 3-LEAD







Dim	MILLI	METERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.074	0.0748 Ref	
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
FCN: S-03946-Rev K 09-	lul-01	•			

ECN: S-03946-Rev. K, 09-Jul-01

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Document Number: 71196 www.vishay.com 09-Jul-01



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

APPLICATION NOTE



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