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Vishay Siliconix

P-Channel 80 V (D-S) MOSFET

SOT-23 (TO-236) D 3 1 G T-20 View

Marking code: G6

PRODUCT SUMMARY					
V _{DS} (V)	-80				
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 10 \text{ V}$	0.164				
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 4.5 \text{ V}$	0.242				
Q _g typ. (nC)	6.7				
I _D (A) ^{a, e}	-3.0				
Configuration	Single				

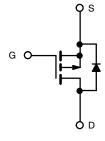
FEATURES

- TrenchFET® Gen IV p-channel power MOSFET
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



APPLICATIONS

- · Load switch
- Circuit protection
- · Motor drive control



P-Channel MOSFET

ORDERING INFORMATION			
Package	SOT-23		
Lead (Pb)-free and halogen-free	SI2387DS-T1-GE3		

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V _{DS}	-80	V
Gate-source voltage		V _{GS}	± 20	V
Continuous drain current (T _J = 150 °C)	T _C = 25 °C		-3.0 ^e	
	T _C = 70 °C		-2.4	
	T _A = 25 °C	I _D	-2.1 ^{b, c}	
	T _A = 70 °C		-1.7 ^{b, c}	А
Pulsed drain current (t = 100 μs)		I _{DM}	-10	
Continuous source-drain diode current	T _C = 25 °C	,	-2.1	
	T _A = 25 °C	I _S	-1.1 ^{b, c}	
Maximum power dissipation	T _C = 25 °C		2.5	
	T _C = 70 °C		1.6	W
	T _A = 25 °C	P _D	1.3 ^{b, c}	
	T _A = 70 °C		0.8 b, c	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C

THERMAL RESISTANCE RATINGS							
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT			
Maximum junction-to-ambient ^b	t ≤ 5 s	R _{thJA}	75	100	°C/W		
Maximum junction-to-case (drain)	Steady state	R_{thJF}	40	50] C/W		

Notes

- a. Based on T_C = 25 °C
- b. Surface mounted on 1" x 1" FR4 board
- c. t = 5 s
- d. Maximum under steady state conditions is 166 °C/W
- e. Package limited



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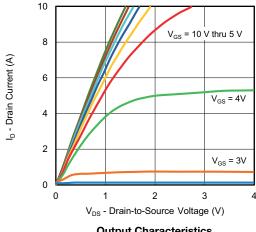
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static				•	•		
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-80	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	I _D = -250 μA	-	-24.7	-		
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA	-	4.7	-	mV/°C	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	-1.5	-	-2.5	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20$	-	-	100	nA	
	I _{DSS}	V _{DS} = -80 V, V _{GS} = 0 V	-	-	-1	— uA	
Zero gate voltage drain current		V _{DS} = -80 V, V _{GS} = 0 V, T _J = 70 °C	-	-	-15		
On-state drain current ^a	I _{D(on)}	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	-10	-	-	Α	
Duein accuracy on state weststands 2	Б	V _{GS} = -10 V, I _D = -2.1 A	-	0.137	0.164		
Drain-source on-state resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -1.7 \text{ A}$	-	0.202	0.242	Ω	
Forward transconductance ^a	9 _{fs}	V _{DS} = -15 V, I _D = -2.1 A	-	5	-	S	
Dynamic ^b							
Input capacitance	C _{iss}		-	395	-		
Output capacitance	C _{oss}	$V_{DS} = -40 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	155	-	pF	
Reverse transfer capacitance	C _{rss}		-	7	-	1	
Total gate charge	Qg	$V_{DS} = -40 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -2.1 \text{ A}$	-	6.8	10.2	nC	
			-	3.2	4.8		
Gate-source charge	Q _{gs}	$V_{DS} = -40 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = 2.1 \text{ A}$	-	1.8	-		
Gate-drain charge	Q_{gd}		-	0.85	-		
Gate resistance	R_g	f = 1 MHz	0.1	5.5	11	Ω	
Turn-on delay time	t _{d(on)}		-	12	24		
Rise time	t _r	$V_{DD} = -40 \text{ V}, R_L = 23.5 \Omega, I_D \cong -1.7 \text{ A},$	-	5	10		
Turn-off delay time	t _{d(off)}	$V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$	-	22	44	1	
Fall time	t _f		-	13	26	1	
Turn-on delay time	t _{d(on)}			25	50	ns	
Rise time	t _r	$V_{DD} = -40 \text{ V}, R_L = 23.5 \Omega, I_D \cong -1.7 \text{ A},$	-	20	40		
Turn-off delay time	t _{d(off)}	$V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		22	44		
Fall time	t _f			15	30		
Drain-Source Body Diode Characteristi	cs						
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	-2.1	А	
Pulse diode forward current	I _{SM}		-	-	-10	1 ~	
Body diode voltage	V_{SD}	I _S = -1.7 A, V _{GS} = 0 V	-	-0.8	-1.2	V	
Body diode reverse recovery time	t _{rr}		-	18	36	ns	
Body diode reverse recovery charge	Q_{rr}	I _F = -4.8 A, di/dt = 100 A/μs,	-	15	30	nC	
Reverse recovery fall time	ta	T _J = 25 °C	-	13	-		
Reverse recovery rise time	t _b		-	5	-	ns	

Notes

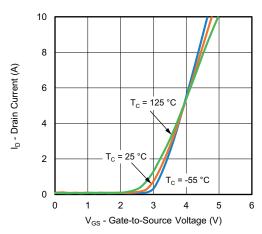
- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

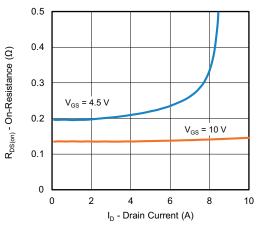




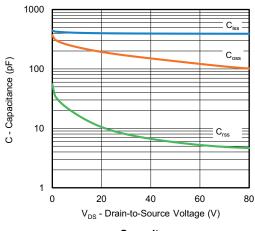
Output Characteristics



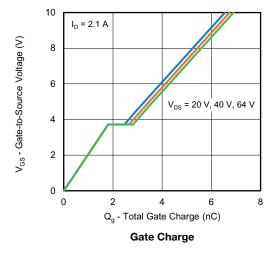
Transfer Characteristics

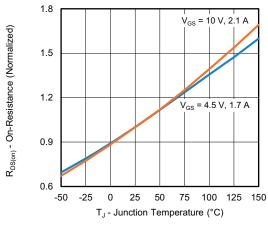


On-Resistance vs. Drain Current and Gate Voltage



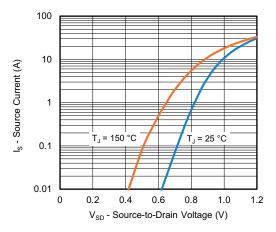
Capacitance



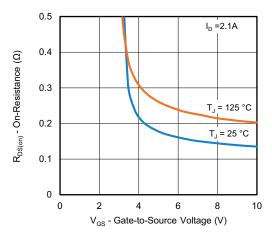


On-Resistance vs. Junction Temperature

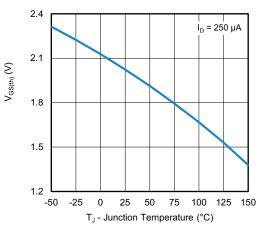




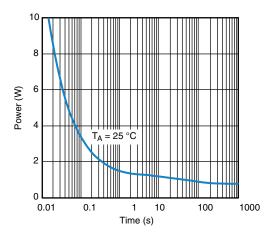
Source-Drain Diode Forward Voltage



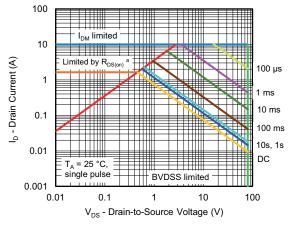
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power, Junction-to-Ambient

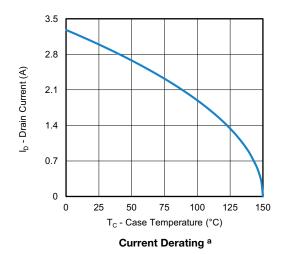


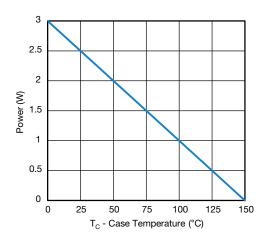
Safe Operating Area, Junction-to-Ambient

Note

a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified





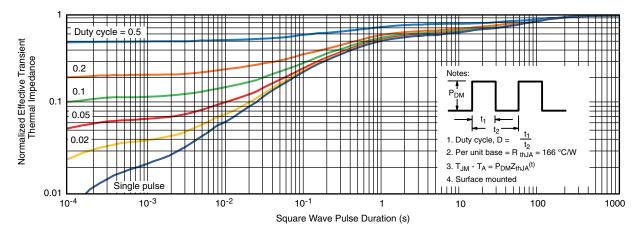


Power, Junction-to-Case

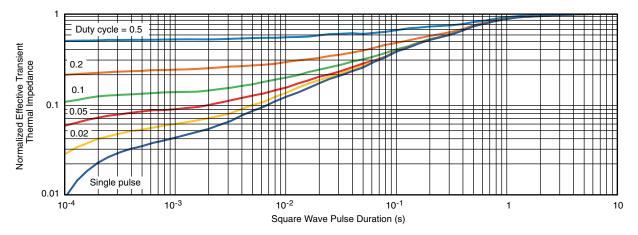
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?63025.

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SOT-23 (TO-236): 3-LEAD







Dim	MILLI	METERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
FCN: S-03946-Rev K 09-	lul-01	•			

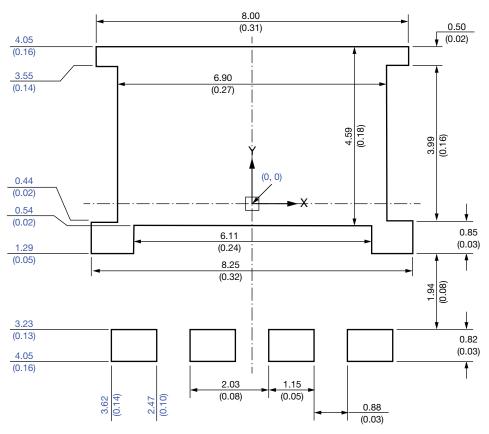
ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



Recommended Minimum PADs for PowerPAK® 8 x 8L Single



Dimensions in millimeters (inches)

Note

• Linear dimensions are in black, the same information is provided in ordinate dimensions which are in blue.



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