

Vishay Siliconix

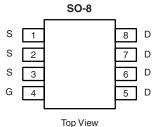
N-Channel 30-V MOSFET

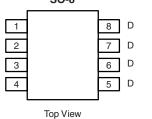
PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)			
30	0.007 at V _{GS} = 10 V	16			
	0.0085 at V_{GS} = 4.5 V	14			

FEATURES

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET[®] Power MOSFETs
- 100 % R_g Tested







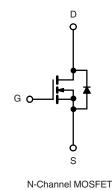


ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted Parameter Symbol 10 s **Steady State** Unit Drain-Source Voltage V_{DS} 30 ٧ \overline{V}_{GS} Gate-Source Voltage ± 20 T_A = 25 °C 16 12 Continuous Drain Current (T_{.1} = 150 °C)^a I_D T_A = 70 °C 13 9 А **Pulsed Drain Current** I_{DM} ± 50 I_S 2.7 1.40 Continuous Source Current (Diode Conduction)^a $T_A = 25 \overline{^{\circ}C}$ 3.0 1.6 P_D w Maximum Power Dissipation^a $T_A = 70 \degree C$ 2.0 1.0 T_J, T_{stg} °C Operating Junction and Storage Temperature Range - 55 to 150

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	- R _{thJA}	34	41	
Maximum Junction-to-Ambient (MOSFET) ^a	Steady State		68	80	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	16	21	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



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MOSFET SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.		Max.	Unit	
Static	•			•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.0		3.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	1	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		1		μA	
	DSS	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 70 ^{\circ}\text{C}$	$0 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 70 ^{\circ}\text{C}$		5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5$ V, V_{GS} = 10 V	30			А	
Drain-Source On-State Resistance ^a	P	V _{GS} = 10 V, I _D = 16 A		0.0057	0.007	Ω	
	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 14 A	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 14 \text{ A}$ 0.0068		0.0085	<u>0</u>	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 16 A		65		S	
Diode Forward Voltage ^a	V _{SD}	I _S = 2.7 A, V _{GS} = 0 V		0.74	1.1	V	
Dynamic ^b							
Input Capacitance	C _{iss}			3230			
Output Capacitance	C _{oss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		585		pF	
Reverse Transfer Capacitance	C _{rss}			255			
Total Gate Charge	Qg			21	25		
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_{D} = 16 A		9.5		nC	
Gate-Drain Charge	Q _{gd}			6.5			
Gate Resistance	R _g		0.4	0.9	1.4	Ω	
Turn-On Delay Time	t _{d(on)}			16	25		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		10	20	ns	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong \text{1}$ A, $\text{V}_\text{GEN}=\text{10}$ V, $\text{R}_\text{g}=\text{6}~\Omega$		57	90		
Fall Time	t _f			15	25		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 2.7 A, dl/dt = 100 A/μs		40	60		

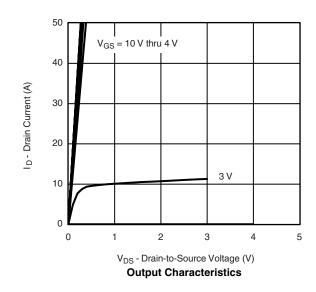
Notes:

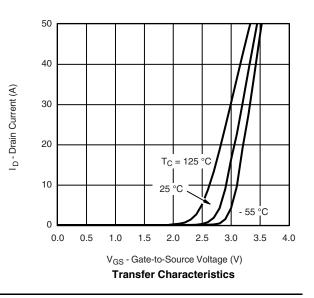
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





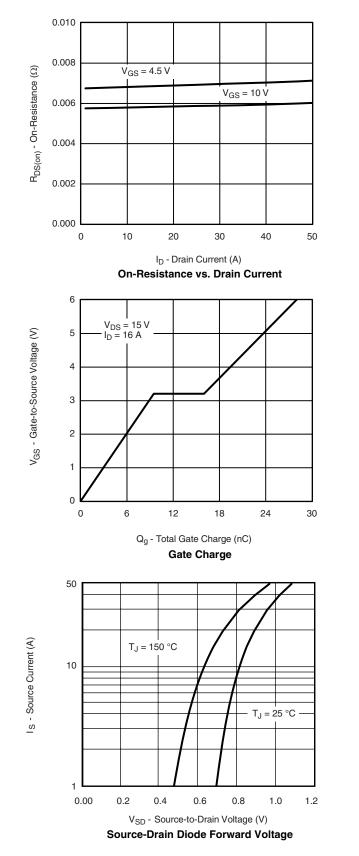


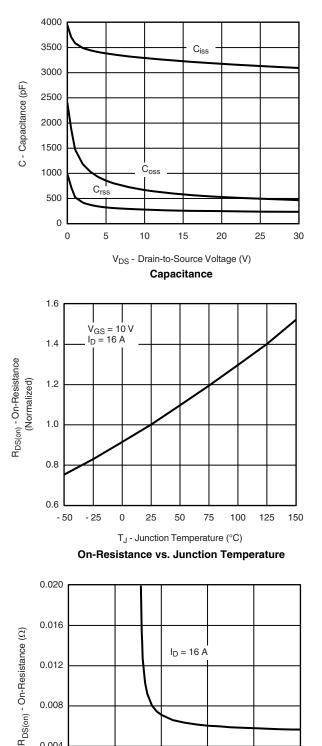


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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

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0.000 0 2 4 6 8 10 V_{GS} - Gate-to-Source Voltage (V) **On-Resistance vs. Gate-to-Source Voltage**

0.008

0.004

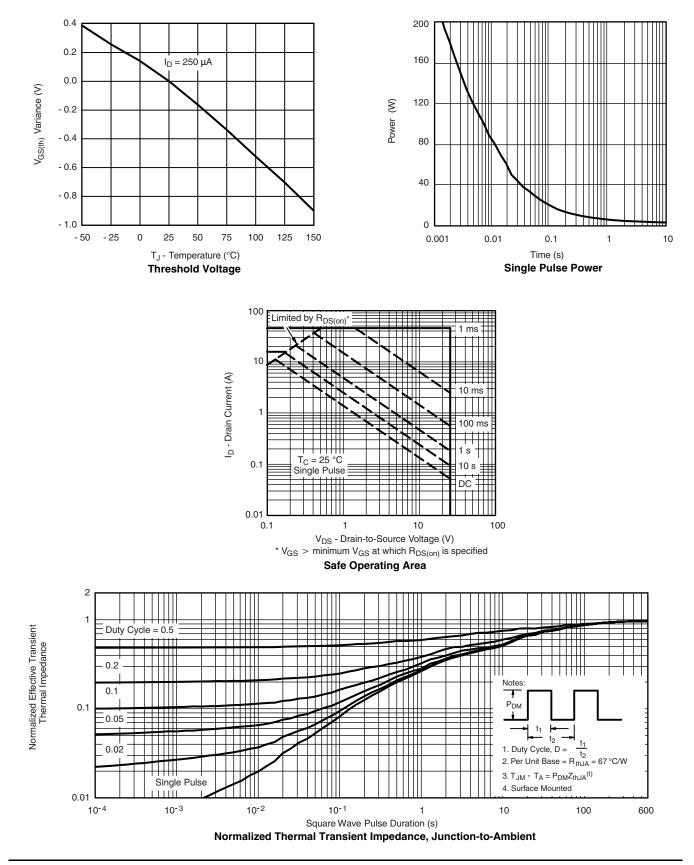
Document Number: 73058 S09-0540-Rev. C, 06-Apr-09

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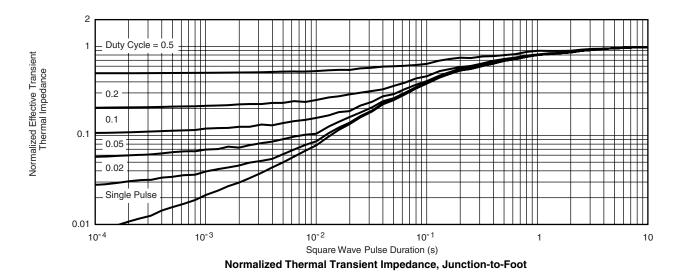
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?73058.



Package Information

Vishay Siliconix

SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





	MILLIM	IETERS	INC	HES		
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

Application Note 826

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RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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