

Vishay Siliconix

# N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
30	0.011 at V <sub>GS</sub> = 10 V	12		
	0.016 at V <sub>GS</sub> = 4.5 V	9.8		



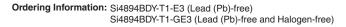
- Halogen-free According to IEC 61249-2-21
  Available
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested



FREE Available

N-Channel MOSFET

_	SO-8	_
S 1		8 D
S 2		7 D
S 3		6 D
G 4		5 D
	Top View	



ABSOLUTE MAXIMUM RATINGS	T <sub>A</sub> = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	30		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		v
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	12	8.9	
	T <sub>A</sub> = 70 °C		9.5	7.1	
Pulsed Drain Current		I <sub>DM</sub>	40		А
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	2.3	1.3	
Single Pulse Avalanche Current		I <sub>AS</sub>	20		
Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	20		mJ
	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	2.5	1.4	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		1.6	0.9	vv
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 s	- R <sub>thJA</sub>	43	50		
Maximum Junction-to-Amplent~	Steady State		73	90	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	19	25		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

# Si4894BDY

## Vishay Siliconix



Parameter	Symbol	Test Conditions	Тур.	Max.	Unit	
Static	· · ·		·	·	·	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		3.0	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		± 100	nA	
	1	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1	μA
Zero Gate Voltage Drain Current	DSS	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5$ V, $V_{GS}$ = 10 V	30			А
	Р	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 12 A		0.009	0.011	Ω
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 9.8 \text{ A}$		0.013	0.016	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 12 A		32		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = 2.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.76	1.1	V
Dynamic <sup>b</sup>	1		1			
Input Capacitance	C <sub>iss</sub>			1580		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		295		
Reverse Transfer Capacitance	C <sub>rss</sub>			140		
Total Gata Charge	0	$V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 12 \text{ A}$		13.2	20	- nC
Total Gate Charge	Qg			25.4	38	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 12 \text{ A}$		5.3		nc
Gate-Drain Charge	Q <sub>gd</sub>			4.3		
Gate Resistance	R <sub>g</sub>		0.9	1.8	2.7	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			13	20	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		10	15	
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong \text{1}$ A, $\text{V}_\text{GEN}$ = 10 V, $\text{R}_\text{g}$ = 6 $\Omega$		33	50	ns
Fall Time	t <sub>f</sub>			10	15	
Source-Drain Reverse Recovery Time	se Recovery Time t <sub>rr</sub> I <sub>F</sub> = 2.3 A, dl/dt = 100 A/µs			25	40	

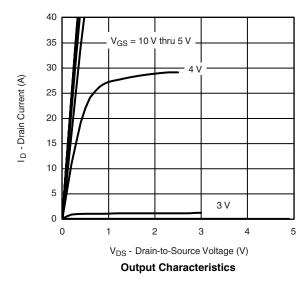
Notes:

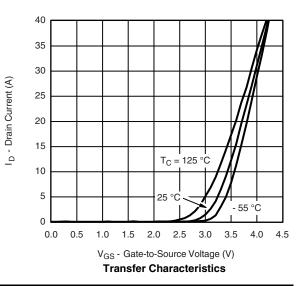
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

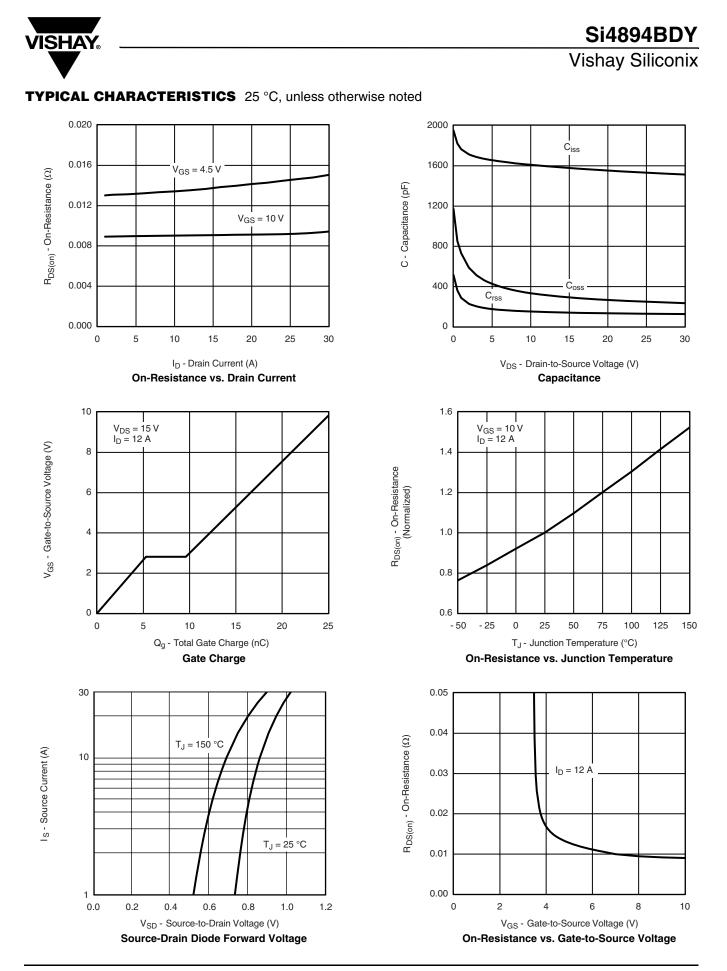
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







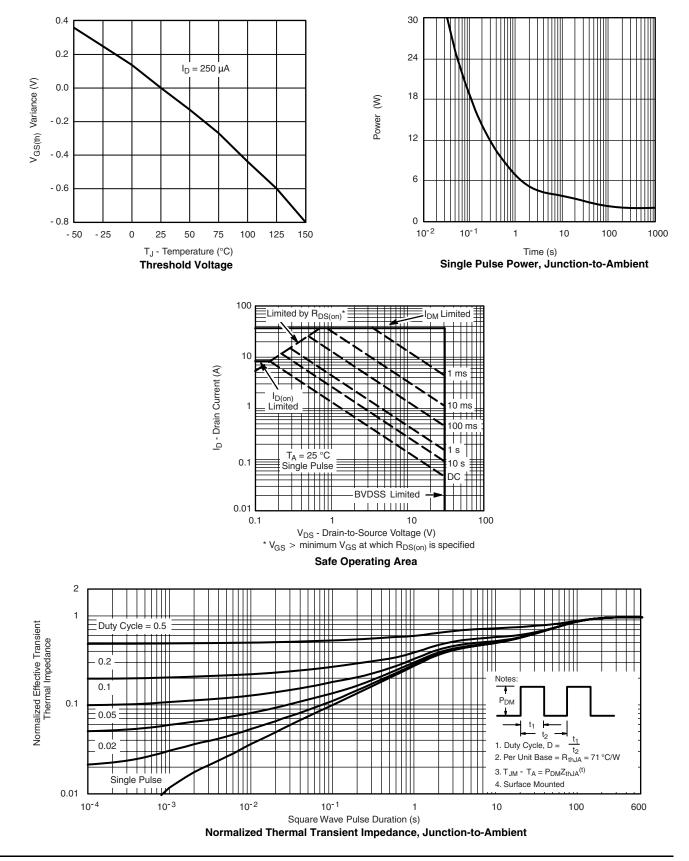
Document Number: 72993 S09-0540-Rev. D, 06-Apr-09

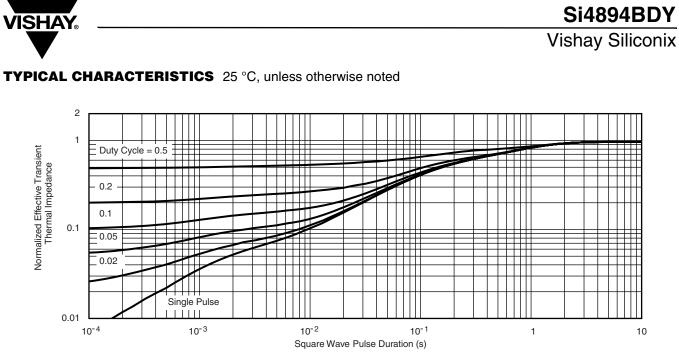
# Si4894BDY

### **Vishay Siliconix**

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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?72993">www.vishay.com/ppg?72993</a>.



# Package Information

Vishay Siliconix

# SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





	MILLIM	IETERS	INC	HES		
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

# **Application Note 826**

Vishay Siliconix



**RECOMMENDED MINIMUM PADS FOR SO-8** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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