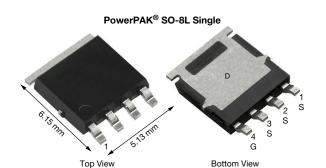
Vishay Siliconix

N-Channel 80 V (D-S) MOSFET



PRODUCT SUMMARY							
V _{DS} (V)	80						
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 10 \text{ V}$	0.0062						
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 7.5 \text{ V}$	0.0065						
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 4.5 \text{ V}$	0.0095						
Q _g typ. (nC)	24						
I _D (A) ^{a, g}	60						
Configuration	Single						

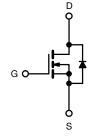
FEATURES

- TrenchFET® power MOSFET
- 100 % R_g and UIS tested
- Capable of operating with 5 V gate drive
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



APPLICATIONS

- DC/DC primary side switch
- · Synchronous rectification
- · High current switching



N-Channel MOSFET

ORDERING INFORMATION	
Package	PowerPAK SO-8L
Lead (Pb)-free and halogen-free	SiJ482DP-T1-GE3

PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	80	V	
Gate-source voltage		V _{GS}	± 20		
	T _C = 25 °C		60 ^g		
Continuous drain current (T. – 150 °C)	T _C = 70 °C	1_	60 ^g		
Continuous drain current (T _J = 150 °C)	T _A = 25 °C	I _D	21.1 ^{b, c}		
	T _A = 70 °C		16.9 ^{b, c}		
Pulsed drain current (t = 300 μs)	I _{DM}	100	Α		
Continuous source-drain diode current	T _C = 25 °C		60 ^g		
	T _A = 25 °C	I _S	4.5 b, c		
Single pulse avalanche current		I _{AS}	30		
Single pulse avalanche energy	L = 0.1 mH	E _{AS}	45	mJ	
	T _C = 25 °C		69.4		
Maximum power dissipation	T _C = 70 °C		44.4	w	
	T _A = 25 °C	P _D	5 b, c		
	T _A = 70 °C		3.2 b, c		
Operating junction and storage temperature ra	T _J , T _{stg}	-55 to +150	°C		
Soldering recommendations (peak temperature	e) ^{d, e}	, and the second	260		

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient b, f	t ≤ 10 s	R _{thJA}	20	25	°C/W
Maximum junction-to-case (drain)	Steady state	R_{thJC}	1.3	1.8	C/VV

Notes

- a. Based on T_C = 25 °C b. Surface mounted on 1" x 1" FR4 board
- See solder profile (www.vishay.com/doc?73257). The PowerPAK SO-8L is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection
- Rework conditions: manual soldering with a soldering iron is not recommended for leadless components
- Maximum under steady state conditions is 65 °C/W
- Package limited

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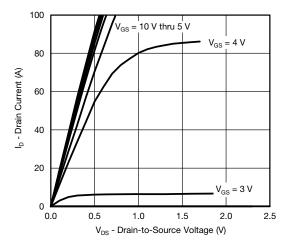
SPECIFICATIONS (T _J = 25 °C, t	unless otherv	wise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	80	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$. OFO A	-	36	-) //00	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA	-	-5.7	-	mV/°C	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.5	-	2.7	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
Zava gata valtaga duain avuunnt		V _{DS} = 80 V, V _{GS} = 0 V		1			
Zero gate voltage drain current	IDSS	V _{DS} = 80 V, V _{GS} = 0 V, T _J = 55 °C	-	-	10	μA	
On-state drain current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30	-	-	Α	
		V _{GS} = 10 V, I _D = 20 A	-	0.0051	0.0062		
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = 7.5 V, I _D = 15 A	-	0.0054	0.0065	Ω	
		$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$	-	0.0068	0.0095		
Forward transconductance a	9 _{fs}	V _{DS} = 10 V, I _D = 20 A	-	68	-	S	
Dynamic ^b							
Input capacitance	C _{iss}		-	2425	-		
Output capacitance	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	1180	-	pF			
Reverse transfer capacitance	C _{rss}		-	100	-	-	
Total gate charge	Qg	$V_{DS} = 40 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	-	47	71		
		$V_{DS} = 40 \text{ V}, V_{GS} = 7.5 \text{ V}, I_D = 10 \text{ A}$	-	36.5	55		
			-	24	36	nC	
Gate-source charge	Q_{gs}	$V_{DS} = 40 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$	-	6.6	-		
Gate-drain charge	Q_{gd}		-	10.2	-		
Output charge	Q _{oss}	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}$	-	69	105		
Gate resistance	R_g	f = 1 MHz	0.4	1.1	2.2	Ω	
Turn-on delay time	t _{d(on)}		-	14	28		
Rise time	t _r	$V_{DD} = 40 \text{ V}, R_L = 4 \Omega$	-	11	22		
Turn-off delay time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	36	72		
Fall time	t _f		-	9	18	1	
Turn-on delay time	t _{d(on)}		-	16	32	ns	
Rise time	t _r	$V_{DD} = 40 \text{ V}, R_L = 4 \Omega$	-	13	26		
Turn-off delay time	t _{d(off)}	$I_D\cong 10$ A, $V_{GEN}=7.5$ V, $R_g=1$ Ω	-	35	70		
Fall time	t _f			11	22		
Drain-Source Body Diode Characteristi	cs						
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	60	^	
Pulse diode forward current ^a	I _{SM}		-	-	100	A	
Body diode voltage	V _{SD}	I _S = 4 A	-	0.73	1.1	V	
Body diode reverse recovery time	t _{rr}		-	46	90	ns	
Body diode reverse recovery charge	Q _{rr}	$I_F = 10 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s},$	-	44	86	nC	
Reverse recovery fall time	t _a	T _J = 25 °C	-	21	-	n -	
Reverse recovery rise time	t _b		-	25	-	ns	

Notes

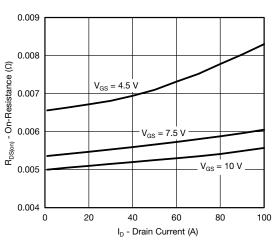
- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

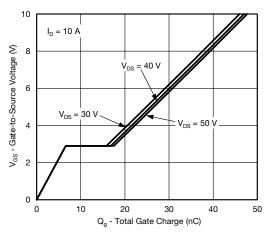




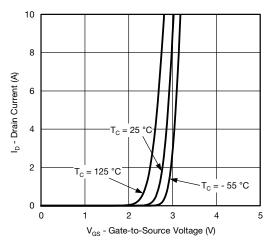
Output Characteristics



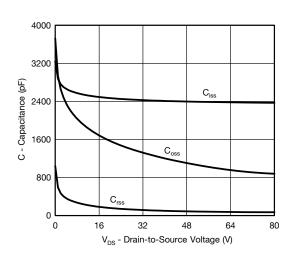
On-Resistance vs. Drain Current and Gate Voltage



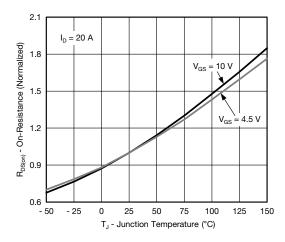
Gate Charge



Transfer Characteristics

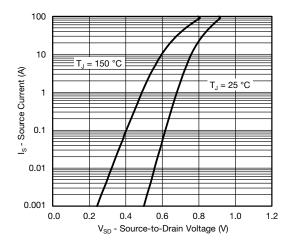


Capacitance

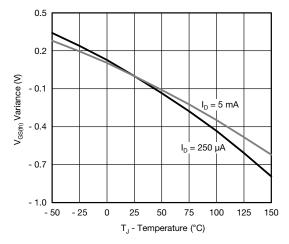


On-Resistance vs. Junction Temperature

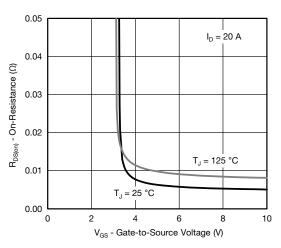




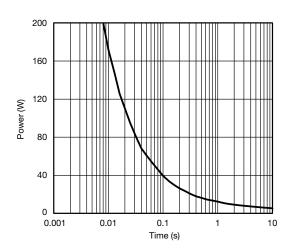
Source-Drain Diode Forward Voltage



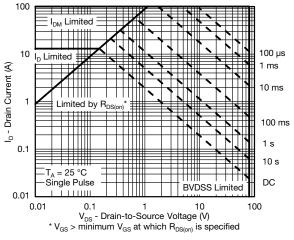
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

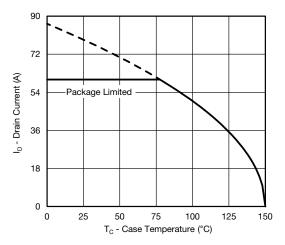


Single Pulse Power, Junction-to-Ambient

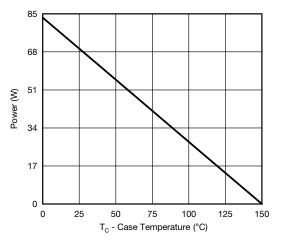


Safe Operating Area, Junction-to-Ambient

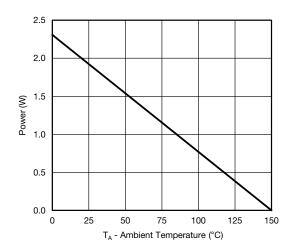




Current Derating a





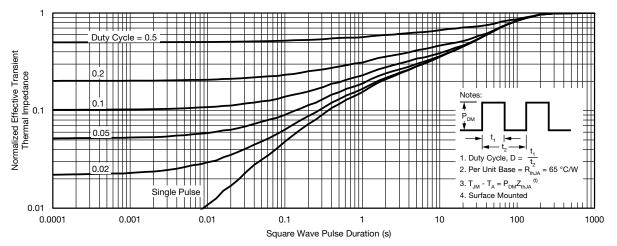


Power, Junction-to-Ambient

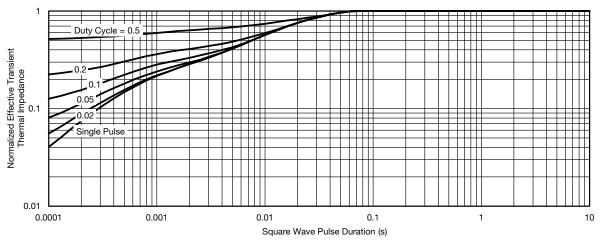
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient

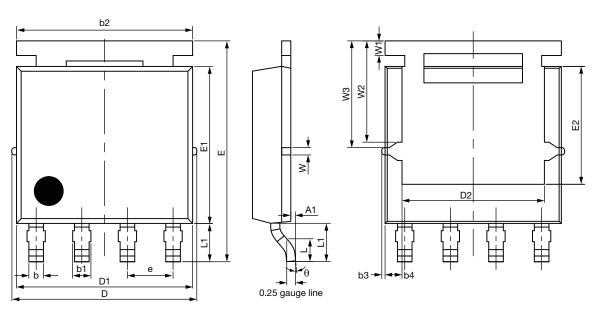


Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?63728.

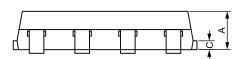


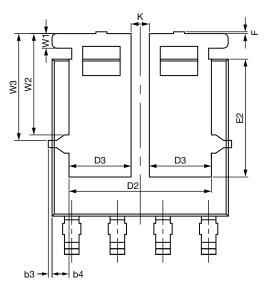
PowerPAK® SO-8L Case Outline 1



Topside view

Backside view (single)





Backside view (dual)



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DIM	MILLIMETERS			INCHES			
DIM.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
Α	1.00	1.07	1.14	0.039	0.042	0.045	
A1	0.00	-	0.127	0.00	-	0.005	
b	0.33	0.41	0.48	0.013	0.016	0.019	
b1	0.44	0.51	0.58	0.017	0.020	0.023	
b2	4.80	4.90	5.00	0.189	0.193	0.197	
b3		0.094	•		0.004		
b4		0.47			0.019		
С	0.20	0.25	0.30	0.008	0.010	0.012	
D	5.00	5.13	5.25	0.197	0.202	0.207	
D1	4.80	4.90	5.00	0.189	0.193	0.197	
D2	3.86	3.96	4.06	0.152	0.156	0.160	
D3	1.63	1.73	1.83	0.064	0.068	0.072	
е		1.27 BSC	•	0.050 BSC			
E	6.05	6.15	6.25	0.238	0.238 0.242 0		
E1	4.27	4.37	4.47	0.168	0.172	0.176	
E2	3.18	3.28	3.38	0.125	0.129	0.133	
F	-	-	0.15	-	-	0.006	
L	0.62	0.72	0.82	0.024	0.028	0.032	
L1	0.92	1.07	1.22	0.036	0.042	0.048	
K		0.51		0.020			
W	0.23			0.009			
W1	0.41			0.016			
W2	2.82			0.111			
W3		2.96			0.117		
θ	0°	-	10°	0°	-	10°	

ECN: S19-0643-Rev. E, 05-Aug-2019

DWG: 5976

Note

• Millimeters will gover



RECOMMENDED MINIMUM PAD FOR PowerPAK® SO-8L SINGLE



Recommended Minimum Pads Dimensions in mm (inches)



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