

Vishay Siliconix

Automotive P-Channel 30 V (D-S) 175 °C MOSFET

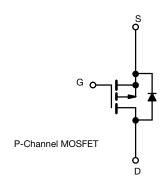
PRODUCT SUMMARY					
V _{DS} (V)	-30				
$R_{DS(on)} (\Omega)$ at $V_{GS} = -10 V$	0.175				
$R_{DS(on)} (\Omega)$ at $V_{GS} = -4.5 V$	0.300				
I _D (A)	-3				
Configuration	Single				
Package	SC-70				



FEATURES

- TrenchFET[®] power MOSFET
- AEC-Q101 qualified ^c
- 100 % $R_{\rm q}$ and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





Marking Code: 9E

ABSOLUTE MAXIMUM RATINGS ($T_C = 25 \text{ °C}$, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V _{DS}	-30	V			
Gate-Source Voltage	V _{GS}	± 20	v			
Continuous Drain Current	T _C = 25 °C	I-	-3			
Continuous Drain Gunent	T _C = 125 °C	I _D	-1.8			
Continuous Source Current (Diode Conduction)	I _S	-3.7	А			
Pulsed Drain Current ^a	I _{DM}	-12				
Single Pulse Avalanche Current L = 0.1 mH		I _{AS}	-6			
Single Pulse Avalanche Energy		E _{AS}	1.8	mJ		
Maximum Power Dissipation ^a	T _C = 25 °C	PD	3	W		
	T _C = 125 °C	۲D	1	vv		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 175	°C			

THERMAL RESISTANCE RATINGS							
PARAMETER		SYMBOL	LIMIT	UNIT			
Junction-to-Ambient	PCB Mount ^b	R _{thJA}	130	°C/W			
Junction-to-Foot (Drain)		R _{thJF}	50	0/10			

Notes

- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- b. When mounted on 1" square PCB (FR4 material).
- c. Parametric verification ongoing.

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SPECIFICATIONS ($T_C = 25 \degree C$, PARAMETER	SYMBOL		MIN.	TYP.	MAX.	UNIT	
Static	OTHEOL	1 120	T CONDITIONS			10703	0.111
Drain-Source Breakdown Voltage	V _{DS}	Voo -	= 0 V, I _D = -250 μA	-30	-	-	
Gate-Source Threshold Voltage	V _{GS(th)}		V _{GS} , I _D = -250 μA	-1	-1.5	-2	V
Gate-Source Leakage	I _{GSS}	_	$V_{GS}, I_D = 200 \mu A$ 0 V, V _{GS} = ± 20 V	-	-	± 100	nA
	1655	V _{GS} = 0 V				-1	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$ $V_{GS} = 0 V$	V _{DS} = -30 V, T _J = 125 °C	-	-	-50	μA
Zero Gale Voltage Drain Gurrent	USS	$V_{GS} = 0 V$ $V_{GS} = 0 V$	V _{DS} = -30 V, T _J = 125 °C V _{DS} = -30 V, T _J = 175 °C	_	-	-150	μΛ
On-State Drain Current ^a	- I	$V_{GS} = 0.V$ $V_{GS} = -10 V$	$V_{DS} = -50 \text{ V}, \text{ 1J} = 175 \text{ C}$ $V_{DS} \le -5 \text{ V}$	-5	-	-130	А
On-State Drain Current	I _{D(on)}	$V_{GS} = -10 V$ $V_{GS} = -10 V$	$V_{\rm DS} \le -3$ V $I_{\rm D} = -2$ A	-5	0.125	- 0.175	~
			$I_D = -2 \text{ A}$ $I_D = -2 \text{ A}, T_J = 125 \text{ °C}$	_	-	0.175	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -10 V$ $V_{GS} = -10 V$		-	-	0.232	
		$V_{GS} = -10 V$ $V_{GS} = -4.5 V$	$I_D = -2 R$, $I_J = 173 C$ $I_D = -1.6 A$	-	0.230	0.294	
Forward Transconductance ^b	g _{fs}	$V_{GS} = -4.5 V$ $I_D = -1.6 A$ $V_{DS} = -10 V, I_D = -2 A$		-	3	-	S
Dynamic ^b	= -10 V, ID = -2 A	-	5	-	3		
Input Capacitance	C _{iss}			_	164	205	
Output Capacitance	C _{iss} C _{oss}	V _{GS} = 0 V	V _{DS} = -25 V, f = 1 MHz	-	44	55	pF
Reverse Transfer Capacitance		$v_{GS} = 0 v$	$v_{DS} = -25 v, i = 1 ivinz$	-	28	35	
Total Gate Charge ^c	C _{rss}			-	4.2	6.5	
	Q _g	V _{GS} = -4.5 V	V _{DS} = -15 V, I _D = -2.2 A	-	4.2	-	nC
Gate-Source Charge ^c Gate-Drain Charge ^c	Q _{gs}	$v_{GS} = -4.5 v$	$v_{DS} = -15 v, I_D = -2.2 A$	-	0.7	-	
Gate Resistance	Q _{gd}				12.5	- 18.5	Ω
	R _g		f = 1 MHz		5	8	52
Turn-On Delay Time ^c Rise Time ^c	t _{d(on)}	- ,,			5 8	8 12	- ns
	t _r	$\label{eq:VDD} \begin{array}{l} V_{DD} = -15 \ V, \ R_L = 15 \ \Omega \\ I_D \cong -1 \ A, \ V_{GEN} = -10 \ V, \ R_g = 1 \ \Omega \end{array}$		-	-	12	
Turn-Off Delay Time ^c	t _{d(off)}			-	11 8	17	
Fall Time ° t _f - 8 12 Source-Drain Diode Ratings and Characteristics ^b							
Source-Drain Diode Ratings and Chara Pulsed Current ^a						10	
	I _{SM}			-	-	-12	A
Forward Voltage	V _{SD}	I _F =	-1.2 A, V _{GS} = 0 V	-	-0.85	-1.2	V

Notes

a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

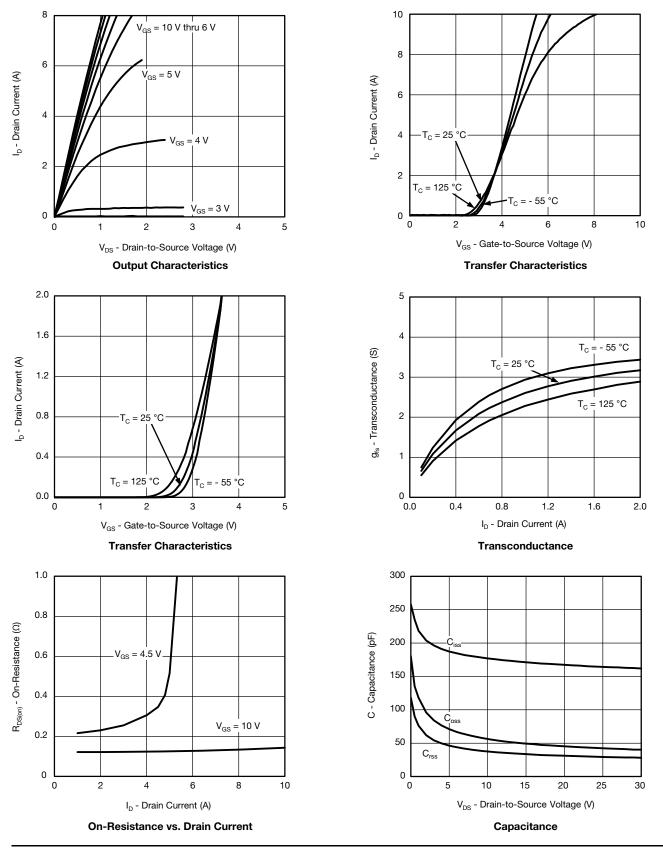
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



S15-2880 Rev. C, 14-Dec-15

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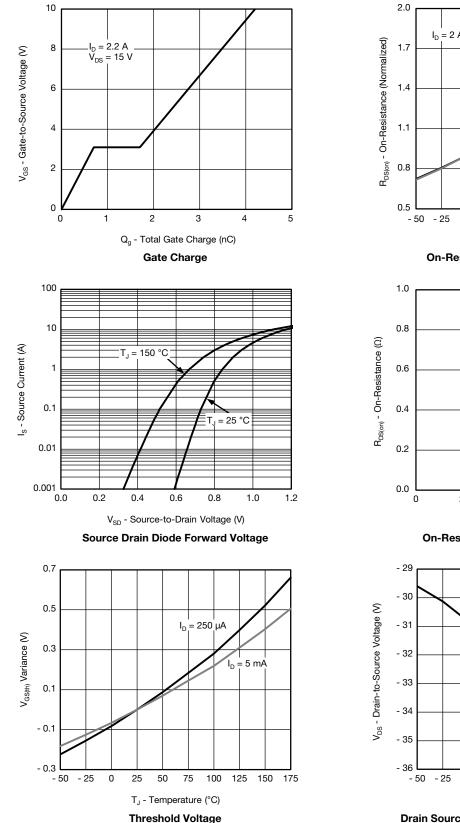
Document Number: 67048

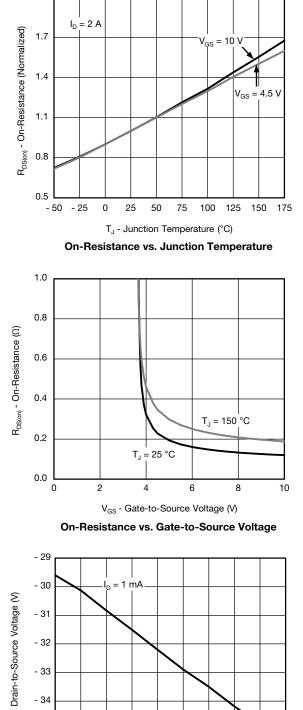
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TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)





0 25 50 75 100 125 150 175 T_J - Junction Temperature (°C) Drain Source Breakdown vs. Junction Temperature 4

S15-2880 Rev. C, 14-Dec-15

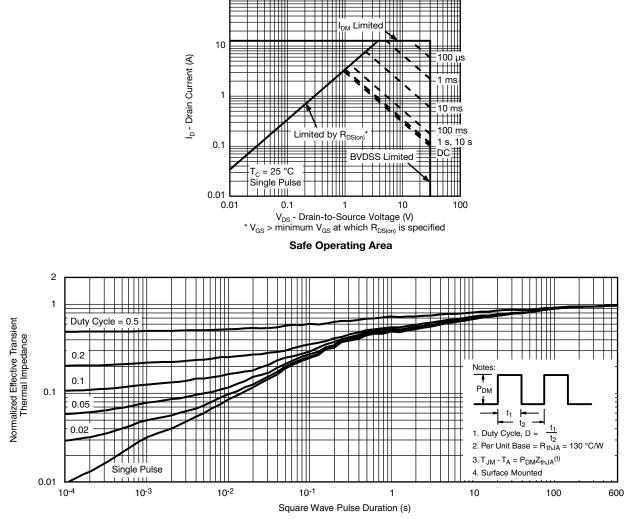
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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)

100



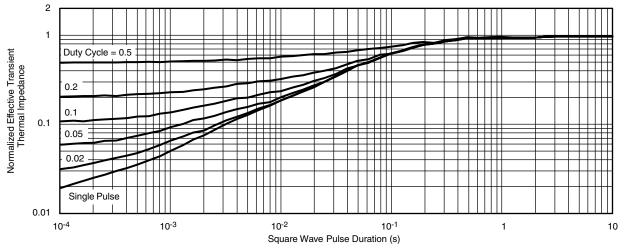
Normalized Thermal Transient Impedance, Junction-to-Ambient



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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

• The characteristics shown in the two graphs

S15-2880 Rev. C, 14-Dec-15

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg?67048</u>.

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REVISION HISTORY^a

REVISION	DATE	DESCRIPTION OF CHANGE			
С	03-Dec-15	Changed R _g minimum			

Note

a. As of April 2014



Package Information Vishay Siliconix

SC-70: 6-LEADS





	MIL	LIMET	ERS	INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.90	-	1.10	0.035	-	0.043
A ₁	-	-	0.10	-	-	0.004
A ₂	0.80	- 1.00 0.031 -		0.039		
b	0.15	-	0.30	0.006	-	0.012
С	0.10	-	0.25	0.004	-	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
Е	1.80	2.10	2.10 2.40		0.083	0.094
E ₁	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65BSC			0.026BSC		
e ₁	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
٩	7°Nom			7°Nom		
ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5550						

Application Note 826

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RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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