

Vishay Siliconix

# Automotive Dual N-Channel 20 V (D-S) 175 °C MOSFET



Marking Code: 8S

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	20				
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 4.5 \text{ V}$	0.350				
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 2.5 \text{ V}$	0.600				
I <sub>D</sub> (A) per leg	0.84				
Configuration	Dual				
Package	SC-70				

#### **FEATURES**

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R<sub>q</sub> tested
- Typical ESD protection: 800 V
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





ROHS COMPLIANT HALOGEN FREE

G <sub>1</sub> D <sub>1</sub>	G <sub>2</sub>
<b>O</b>	<b>Ó</b>
S <sub>1</sub>	S <sub>2</sub>

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> :	= 25 °C, unles	s otherwise noted	d)		
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage	V <sub>DS</sub>	20	W		
Gate-source voltage	V <sub>GS</sub>	± 12	V		
Continuous drain current a	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	0.84		
Continuous drain current 4	T <sub>C</sub> = 125 °C		0.49		
Continuous source current (diode conduction) a	I <sub>S</sub>	0.54	Α		
Pulsed drain current <sup>b</sup>		I <sub>DM</sub>		3	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	3.3		
Single Pulse Avalanche Energy	L=U.I MH	E <sub>AV</sub>	0.54	mJ	
Maximum power dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	D	1.5	W	
	T <sub>C</sub> = 125 °C	$P_{D}$	0.5	VV	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stq</sub>	-55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-ambient	PCB mount <sup>c</sup>	$R_{thJA}$	460	°C/W
Junction-to-foot (drain)		$R_{thJF}$	350	C/VV

#### **Notes**

- a. Package limited
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %
- c. When mounted on 1" square PCB (FR4 material)



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static		_						
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0$ , $I_D = 250 \mu A$		20	-	-	V	
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$		1	1.5		
Onto anima lankana		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 3 \text{ V}$		-	-	± 1	μΑ	
Gate-source leakage	ate-source leakage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$		-	± 10	mA	
		$V_{GS} = 0 V$	V <sub>DS</sub> = 20 V	-	-	1		
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = 20 V, T <sub>J</sub> = 125 °C	-	-	50	μΑ	
		$V_{GS} = 0 V$	V <sub>DS</sub> = 20 V, T <sub>J</sub> = 175 °C	-	-	150		
On-state drain current a	I <sub>D(on)</sub>	$V_{GS} = 4.5 \text{ V}$	$V_{DS} \ge 5 V$	0.4	-	-	Α	
Drain-source on-state resistance <sup>a</sup>		$V_{GS} = 4.5 \text{ V}$	I <sub>D</sub> = 0.4 A	-	0.200	0.350	Ω	
	Б	V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 0.4 A, T <sub>J</sub> = 125 °C	-	-	0.507		
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 0.4 A, T <sub>J</sub> = 175°C	-	-	0.600		
		V <sub>GS</sub> = 2.5 V	I <sub>D</sub> = 0.4 A	-	0.250	0.600		
Dynamic <sup>b</sup>								
Input capacitance	C <sub>iss</sub>			-	50	-	pF	
Output capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	$V_{DS} = 10 \text{ V}, f = 1 \text{ MHz}$	-	21	-		
Reverse transfer capacitance	C <sub>rss</sub>			-	10	-		
Total gate charge c	$Q_g$			-	0.7	1.2		
Gate-source charge c	$Q_{gs}$	$V_{GS} = 4.5 \text{ V}$	$V_{DS} = 10 \text{ V}, I_{D} = 1.2 \text{ A}$	-	0.2	-	nC	
Gate-drain charge c	$Q_{gd}$			-	0.2	-		
Gate resistance d	$R_g$	f = 1 MHz		4.5	9	13.7	Ω	
Turn-on delay time <sup>c</sup>	t <sub>d(on)</sub>			-	10	15		
Rise time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 10 \text{ V}, \text{ R}_{L} = 20 \Omega$ $I_{D} \cong 0.5 \text{ A}, \text{ V}_{GEN} = 4.5 \text{ V}, \text{ R}_{g} = 1 \Omega$		-	12	22		
Turn-off delay time c	t <sub>d(off)</sub>			-	15	21	ns	
Fall time °	t <sub>f</sub>			-	6	10		
Source-Drain Diode Ratings and Char	acteristics <sup>b</sup>	•						
Pulsed current <sup>a</sup>	I <sub>SM</sub>			-	-	3	Α	
	$V_{SD}$	I <sub>F</sub> = 0.5 A, V <sub>GS</sub> = 0		_	0.8	1.2	V	

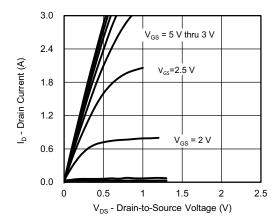
#### Notes

- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature
- d. Gate is obscured by ESD network series resistance and cannot be tested directly

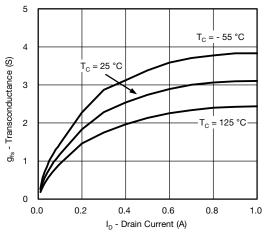
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



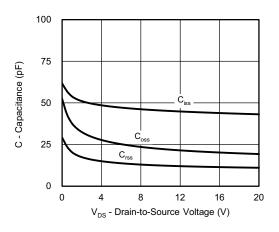
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



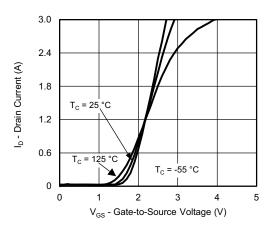
#### **Output Characteristics**



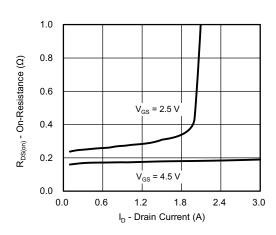
Transconductance



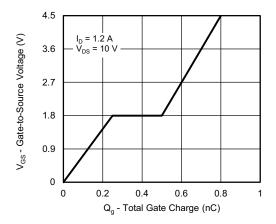
Capacitance



**Transfer Characteristics** 



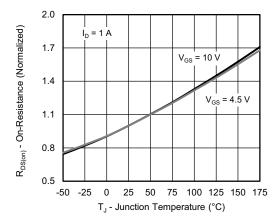
On-Resistance vs. Drain Current



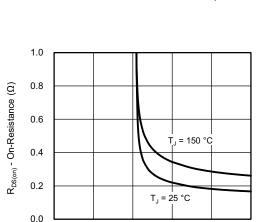
**Gate Charge** 



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



On-Resistance vs. Junction Temperature



2

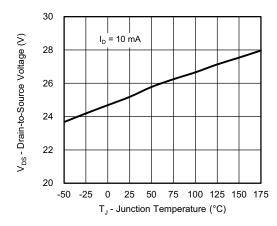
0

On-Resistance vs. Gate-to-Source Voltage

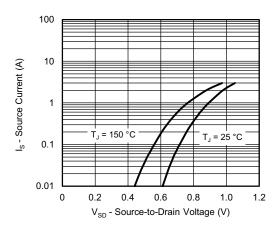
V<sub>GS</sub> - Gate-to-Source Voltage (V)

3

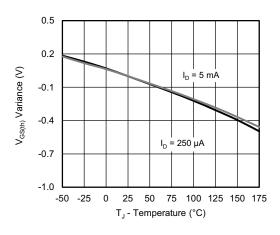
5



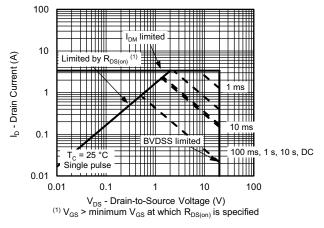
Drain Source Breakdown vs. Junction Temperature



**Source Drain Diode Forward Voltage** 



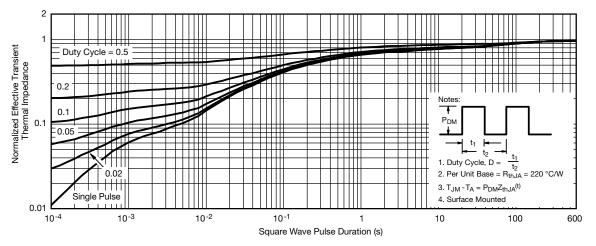
Threshold Voltage



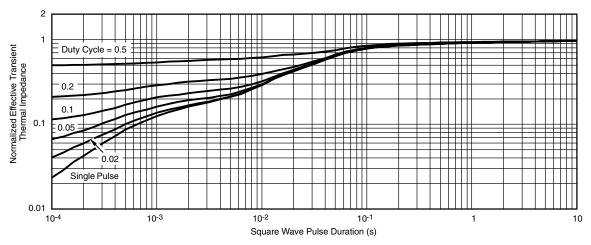
Safe Operating Area



## THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

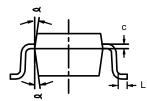
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## SC-70: 6-LEADS





	MILLIMETERS			INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.90	-	1.10	0.035	_	0.043
A <sub>1</sub>	-	-	0.10	-	-	0.004
A <sub>2</sub>	0.80	-	1.00	0.031	_	0.039
b	0.15	-	0.30	0.006	-	0.012
С	0.10	-	0.25	0.004	-	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
E	1.80	2.10	2.40	0.071	0.083	0.094
E <sub>1</sub>	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65BSC				0.026BSC	;
e <sub>1</sub>	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
9	7°Nom				7°Nom	
ECN: S-03946—Rev. B, 09-Jul-01						

DWG: 5550



### **RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)

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