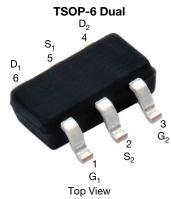
## SQ3987EV

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**Vishay Siliconix** 

# Automotive Dual P-Channel 30 V (D-S) 175 °C MOSFET



Marking code: 8X

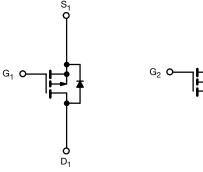
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	-30				
$R_{DS(on)} (\Omega)$ at $V_{GS} = -10 V$	-0.110				
$R_{DS(on)} (\Omega)$ at $V_{GS} = -4.5 V$	-0.185				
I <sub>D</sub> (A)	-2.75				
Configuration	Dual				
Package	TSOP-6				

#### FEATURES

- TrenchFET<sup>®</sup> power MOSFET
- AEC-Q101 qualified
- 100 % R<sub>g</sub> and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



RoHS COMPLIANT HALOGEN FREE



P-Channel MOSFET

D<sub>2</sub> P-Channel MOSFET

 $S_2$ 

		otherwise noted)		
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-source voltage	V <sub>DS</sub>	-30	v	
Gate-source voltage	V <sub>GS</sub>	± 20	- v	
Continuous drain current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>C</sub> = 25 °C		-3	
	T <sub>C</sub> = 125 °C		-1.74	
Pulsed drain current	I <sub>DM</sub>	-11	— A	
Continuous source current (diode conduction) <sup>a</sup>	I <sub>S</sub>	-2.1	7	
Maximum neuror dissinction a	T <sub>C</sub> = 25 °C	D	1.67	W
Maximum power dissipation <sup>a</sup>	T <sub>C</sub> = 125 °C	P <sub>D</sub>	0.56	
Unclamped inductive surge UIS		I <sub>AV</sub>	-5	A
Operating junction and storage temperature ran	ge	T <sub>J</sub> , T <sub>sta</sub>	-55 to +175	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Maximum junction-to-ambient <sup>a</sup>	Steady state	R <sub>thJA</sub>	150	°C/W	
Maximum junction-to-foot (drain)	Steady state	R <sub>thJF</sub>	90	0/10	

#### Note

a. Surface mounted on 1" x 1" FR4 board

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SPECIFICATIONS (T <sub>J</sub>	= 25°C, ur	nless otherwise	noted)					
PARAMETER	SYMBOL	T	MIN.	TYP.	MAX.	UNIT		
Static								
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \ \mu A$		-1.5	-	-2.5	V	
Gate-body leakage	I <sub>GSS</sub>	V <sub>DS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	± 100	nA	
Zero gate voltage drain	I <sub>DSS</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = -30 V	-	-	-1		
current		$V_{GS} = 0 V$	$V_{DS}$ = -30 V, $T_{J}$ = 175 °C	-	-	-50	μA	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = -10 V	$V_{DS} \le -5 V$	-4	-	-	А	
Drain-source on-state	Б	$V_{GS} = -10 V$	I <sub>D</sub> = -1.5 A	-	0.085	0.133		
resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5 V	I <sub>D</sub> = -2 A	I <sub>D</sub> = -2 A - 0.13		0.185	Ω	
Forward transconductance a	g <sub>fs</sub>	V	<sub>DS</sub> = -5 V, I <sub>D</sub> = -1 A	-	4.2	-	S	
Diode forward voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub>	= -0.5 A, V <sub>GS</sub> = 0 V	-	-0.83	-1.10	V	
Dynamic <sup>b</sup>								
Input capacitance	C <sub>iss</sub>			-	456	570	pF	
Output capacitance	Coss	$V_{GS} = 0 V$	V <sub>DS</sub> = -15 V	-	85	106		
Reverse capacitance	C <sub>rss</sub>			-	59	74		
Total gate charge	Qg	V <sub>GS</sub> = -10 V	V <sub>DS</sub> = -15 V, I <sub>D</sub> = -3 A	-	9.7	12.2	nC	
Gate-source charge	Q <sub>gs</sub>			-	1.3	-		
Gate-drain charge	Q <sub>gd</sub>			-	2	-		
Gate resistance	Rg	f = 1 MHz		9	-	24	Ω	
Turn-on delay time	t <sub>d(on)</sub>				6.6	8.3		
Rise time	t <sub>r</sub>	$\label{eq:VDD} \begin{array}{l} V_{DD}=\text{-10 V}, \ R_L=\text{10 }\Omega, \\ I_D\cong\text{-1 A}, \ V_{GEN}=\text{-10 V}, \ R_g=\text{1}\ \Omega \end{array}$		-	2.4	3	ns	
Turn-off delay time	t <sub>d(off)</sub>			-	18.4	23		
Fall time	t <sub>f</sub>			-	2.2	2.8		
Source-Drain Diode Ratings	and Charac	teristic <sup>b</sup>		•				
Pulsed current	I <sub>SM</sub>			-	-	-11	А	
Forward voltage	V <sub>SD</sub>	١ <sub>F</sub>	= 0.5 A, $V_{GS}$ = 0 V	-	-0.83	-1.1	V	
Reverse recovery fall time	t <sub>a</sub>			-	9.1	-	ns	
Reverse recovery rise time	t <sub>b</sub>	$V_{DD}$ = -24 V, $I_{FM}$ = -1.5 A, di/dt = 100 A/µs, R = 160 $\Omega,$ L = 1 mH, pulse W = 2 µs		-	4.8	-	ns	
Body diode reverse recovery time	t <sub>rr</sub>			-	14	28	ns	
Body diode reverse recovery charge	Q <sub>rr</sub>			-	9	18	μC	
Body diode peak reverse recovery current	I <sub>RM(REC)</sub>			-	-1.4	-	А	

#### Notes

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %

b. Guaranteed by design, not subject to production testing

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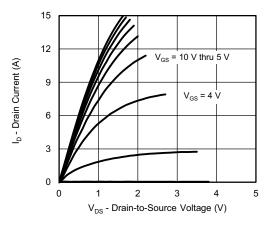
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

2

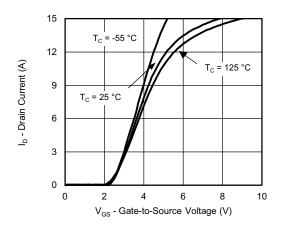


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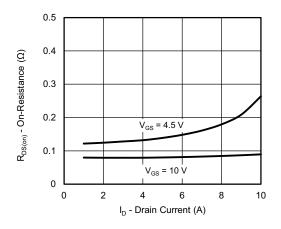
#### TYPICAL CHARACTERISTICS (25 °C unless otherwise noted)



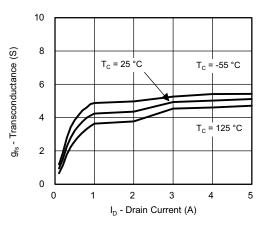
**Output Characteristics** 



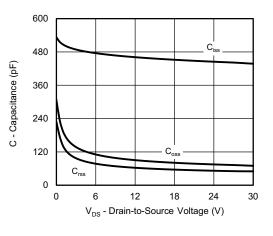
**Transfer Characteristics** 



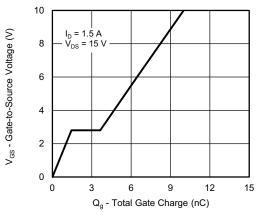
**On-Resistance vs. Drain Current** 



Transconductance







Gate Charge

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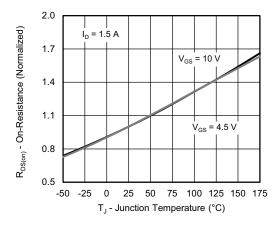
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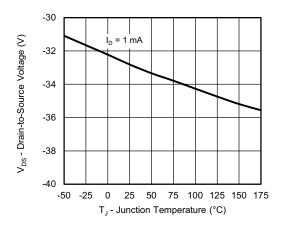
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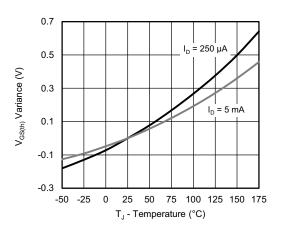
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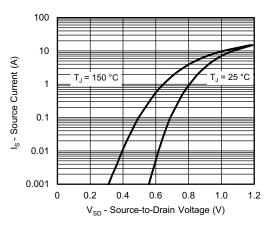
**On-Resistance vs. Junction Temperature** 



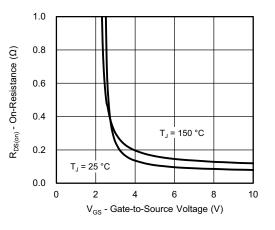
Drain Source Breakdown vs. Junction Temperature



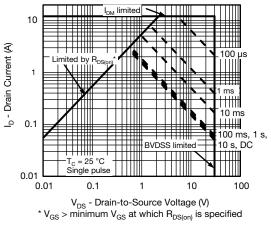
**Threshold Voltage** 



Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



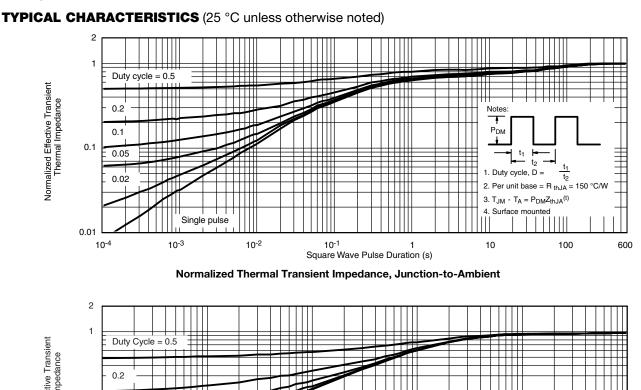
Safe Operating Area, Junction-to-Case

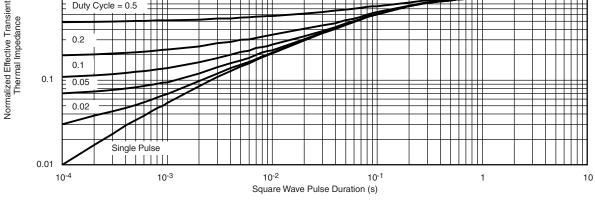
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Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?75315.

**SQ3987EV** 

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Package Information

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TSOP: 5/6-LEAD JEDEC Part Number: MO-193C









6-LEAD TSOP



	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
<b>A</b> <sub>1</sub>	0.01	-	0.10	0.0004	-	0.004	
A <sub>2</sub>	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
Е	2.70	2.85	2.98	0.106	0.112	0.117	
E <sub>1</sub>	1.55	1.65	1.70	0.061	0.065	0.067	
е		0.95 BSC		0.0374 BSC			
<b>e</b> <sub>1</sub>	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L <sub>1</sub>	0.60 Ref			0.024 Ref			
L <sub>2</sub>	0.25 BSC				0.010 BSC		
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
$\theta_1$	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

# **Application Note 826**

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**RECOMMENDED MINIMUM PADS FOR TSOP-6** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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