## SQP50P03-07

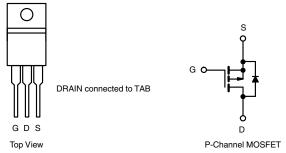


Vishay Siliconix

# Automotive P-Channel 30 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	- 30				
$R_{DS(on)}(\Omega)$ at $V_{GS}$ = - 10 V	0.0070				
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 V$	0.0110				
I <sub>D</sub> (A)	- 50				
Configuration	Single				

#### TO-220AB



#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Package with Low Thermal Resistance
- 100 %  $R_{\rm q}$  and UIS Tested
- AEC-Q101 Qualified<sup>d</sup>
- Compliant to RoHS Directive 2002/95/EC



ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free and Halogen-free	SQP50P03-07-GE3

ABSOLUTE MAXIMUM RATINGS (	T <sub>C</sub> = 25 °C, unles	s otherwise noted	)	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V <sub>DS</sub>	- 30	v
Gate-Source Voltage		V <sub>GS</sub>	± 20	v
Continuous Drain Current <sup>a</sup>	T <sub>C</sub> = 25 °C	1	- 50	
Continuous Drain Current-	T <sub>C</sub> = 125 °C	- I <sub>D</sub>	- 50	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 50	А
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	- 200	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	- 50	
Single Pulse Avalanche Energy		E <sub>AS</sub>	125	mJ
Mauianum Dauran Diasia ati sab	T <sub>C</sub> = 25 °C	;	150	w
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 125 °C	P <sub>D</sub>	50	VV V
Operating Junction and Storage Temperature Ra	ange	T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount <sup>c</sup>	R <sub>thJA</sub>	62	°C/W
Junction-to-Case (Drain)		R <sub>thJC</sub>	1	0/10

#### Notes

- a. Package limited.
- b. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.



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PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT		
Static					•			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> :	V <sub>GS</sub> = 0, I <sub>D</sub> = - 250 μA		-	-	v	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 1.5	- 2.0	- 2.5	v	
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =	0 V, $V_{GS} = \pm 20 V$	-	-	± 100	nA	
		$V_{GS} = 0 V$	V <sub>DS</sub> = - 30 V	-	-	- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 V$	$V_{DS}$ = - 30 V, T <sub>J</sub> = 125 °C	-	-	- 50	μA	
		$V_{GS} = 0 V$	$V_{DS}$ = - 30 V, T <sub>J</sub> = 175 °C	-	-	- 250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = - 10 V	$V_{DS} \le$ - 5 V	- 80	-	-	Α	
		V <sub>GS</sub> = - 10 V	I <sub>D</sub> = - 30 A	-	0.0050	0.0070	Ω	
Drain Source On State Desistence?		V <sub>GS</sub> = - 10 V	I <sub>D</sub> = - 30 A, T <sub>J</sub> = 125 °C	-	-	0.0102		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V	I <sub>D</sub> = - 30 A, T <sub>J</sub> = 175 °C	-	-	0.0118		
		V <sub>GS</sub> = - 4.5 V	I <sub>D</sub> = - 20 A	-	0.0089	0.0110		
Forward Transconductanceb	9 <sub>fs</sub>	V <sub>DS</sub> =	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 30 A		62	-	S	
Dynamic <sup>b</sup>		-						
Input Capacitance	C <sub>iss</sub>			-	4304	5380		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	$V_{GS} = 0 V$ $V_{DS} = -25 V$ , f = 1 MHz	-	764	955	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			-	680	850		
Total Gate Charge <sup>c</sup>	Qg			-	103.5	155		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = - 10 V	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 75 A	-	14.3	-	nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	26.9	-		
Gate Resistance	Rg	f = 1 MHz		1.42	2.85	4.28	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			-	11	17		
Rise Time <sup>c</sup>	t <sub>r</sub>	$\begin{array}{l} V_{\text{DD}}=\text{-}~15~\text{V},~R_{\text{L}}=0.2~\Omega\\ I_{\text{D}}\cong\text{-}~75~\text{A},~V_{\text{GEN}}=\text{-}~10~\text{V},~R_{g}=1~\Omega \end{array}$		-	10	15	ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	63	95		
Fall Time <sup>c</sup>	t <sub>f</sub>			-	26	39		
Source-Drain Diode Ratings and Char	acteristics <sup>b</sup>	•						
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	- 200	Α	
Forward Voltage	V <sub>SD</sub>	$I_{\rm F} = -45$ A, $V_{\rm GS} = 0$		-	- 0.9	- 1.5	V	

Notes

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

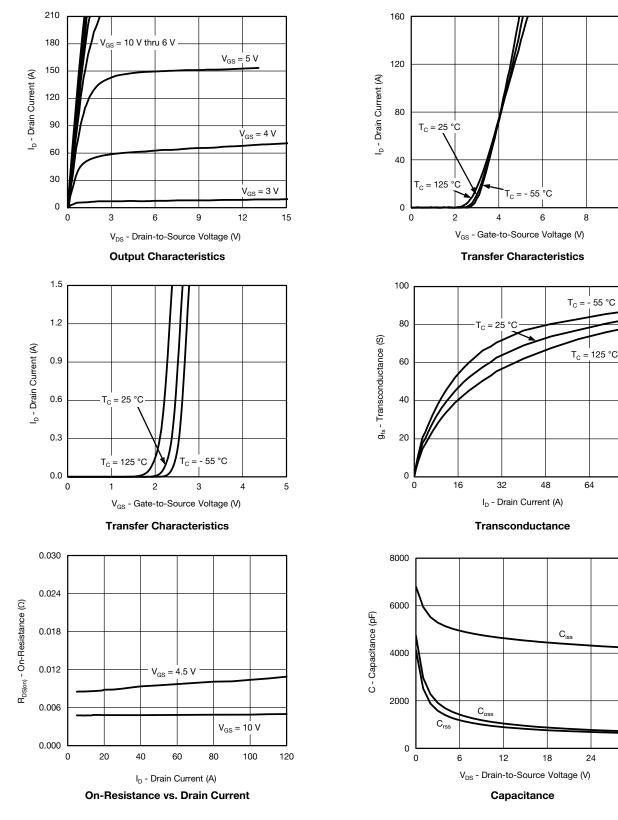
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)

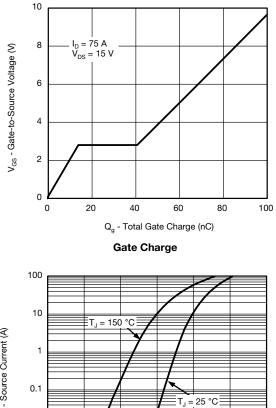


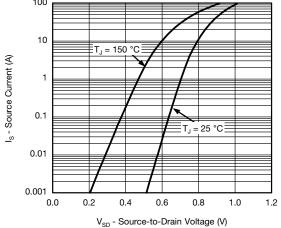
S11-1879-Rev. A, 03-Oct-11

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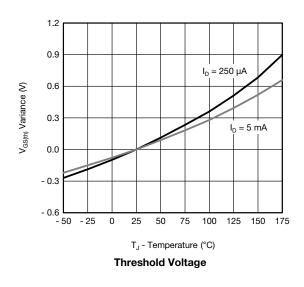


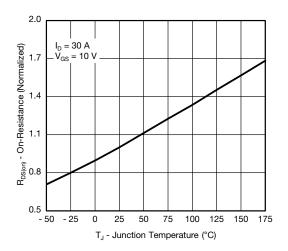
### TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



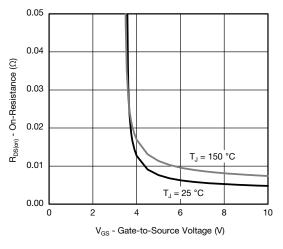


Source Drain Diode Forward Voltage

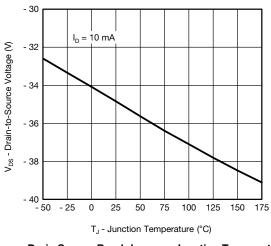




**On-Resistance vs. Junction Temperature** 



**On-Resistance vs. Gate-to-Source Voltage** 



Drain Source Breakdown vs. Junction Temperature

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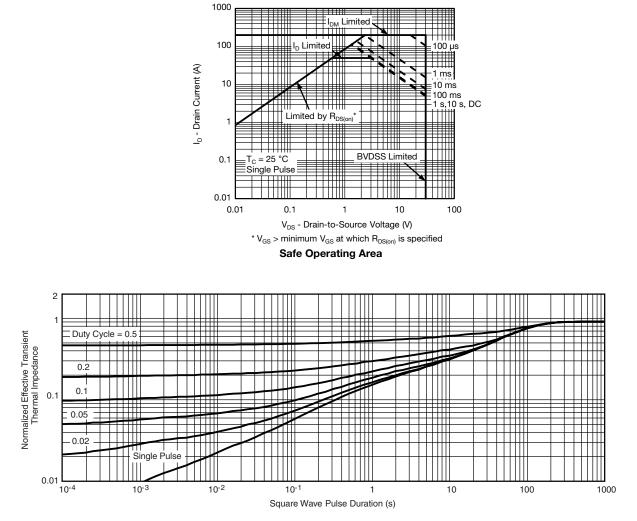
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#### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)

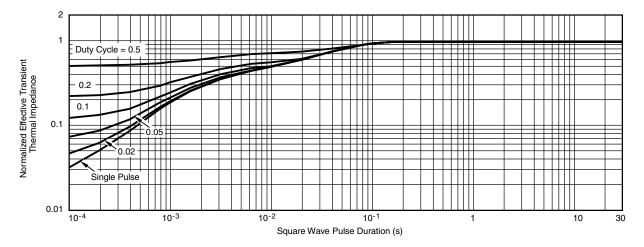


Normalized Thermal Transient Impedance, Junction-to-Ambient

S11-1879-Rev. A, 03-Oct-11



### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



#### Normalized Thermal Transient Impedance, Junction-to-Case

- Note
- The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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# **TO-220AB**



	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
E	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØР	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
	0413-Rev. P,		0.102	0.118

Note

 $^{\star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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