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Vishay Siliconix

# Automotive N-Channel 200 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V <sub>DS</sub> (V)	200			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0153			
I <sub>D</sub> (A)	78.5			
Configuration	Single			
Package	TO-220			

#### **FEATURES**

- TrenchFET® power MOSFET
- Package with low thermal resistance
- AEC-Q101 qualified
- 100 % R<sub>a</sub> and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



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N-Channel MOSFET	S

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage		V <sub>DS</sub>	200	V	
Gate-source voltage		$V_{GS}$	± 20	V	
Continuous drain current	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	78.5		
	T <sub>C</sub> = 125 °C		45		
Continuous source current (diode conducti	Is	120	Α		
Pulsed drain current <sup>b</sup>		I <sub>DM</sub>		170	
Single pulse avalanche current	L = 0.1 mH	I <sub>AS</sub>	64		
Single pulse avalanche energy	L = 0.1 mH	E <sub>AS</sub>	205	mJ	
Maximum power dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	D	250	W	
	T <sub>C</sub> = 125 °C	$P_{D}$	83	VV	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount c	$R_{thJA}$	40	°C/W	
Junction-to-case (drain)		$R_{thJC}$	0.6	C/VV	

#### Notes

- a. Package limited.
- b. Pulse test; pulse width  $\leq 300 \,\mu\text{s}$ , duty cycle  $\leq 2 \,\%$ .
- c. When mounted on 1" square PCB (FR4 material).



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<b>SPECIFICATIONS</b> ( $T_C = 25  ^{\circ}C$ , PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static						1	
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		200	-	_	Ī
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$		3.0	3.5	V
Gate-source leakage	I <sub>GSS</sub>	_	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA
-		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 200 V	-	-	1	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 200 V, T <sub>J</sub> = 125 °C	=	-	50	μΑ
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 200 V, T <sub>J</sub> = 175 °C	-	-	600	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	V <sub>DS</sub> ≥ 5 V	40	-	-	Α
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 20 A	-	0.0127	0.0153	
Drain-source on-state resistance a	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C	-	-	0.0310	Ω
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C	-	-	0.0404	1
Forward transconductance b	9 <sub>fs</sub>	V <sub>DS</sub>	= 15 V, I <sub>D</sub> = 20 A	-	54	-	S
Dynamic <sup>b</sup>							
Input capacitance	C <sub>iss</sub>		V <sub>DS</sub> = 25 V, f = 1 MHz	-	3200	4200	pF
Output capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$		-	1300	1750	
Reverse transfer capacitance	C <sub>rss</sub>	1		-	80	110	
Total gate charge <sup>c</sup>	Qg			-	55	85	
Gate-source charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	$V_{GS} = 10 \text{ V}$ $V_{DS} = 100 \text{ V}, I_D = 9 \text{ A}$		14	-	nC
Gate-drain charge <sup>c</sup>	Q <sub>gd</sub>	1		=	16.5	-	
Gate Resistance	$R_g$		f = 1 MHz		2.92	4.40	Ω
Turn-on delay time <sup>c</sup>	t <sub>d(on)</sub>			-	17	30	
Rise time <sup>c</sup>	t <sub>r</sub>		100 V, $R_L$ = 11.1 $Ω$	=	8	15	1 _
Turn-off delay time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 9 A$ ,	$I_D \cong 9 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		39	60	ns
Fall time <sup>c</sup>	t <sub>f</sub>				16	30	
Source-Drain Diode Ratings and Chara	acteristics <sup>b</sup>						
Pulsed current <sup>a</sup>	I <sub>SM</sub>				-	170	Α
Forward voltage	$V_{SD}$	I <sub>F</sub> = 20 A, V <sub>GS</sub> = 0 V		-	0.82	1.5	V
Body diode reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 10 A, di/dt = 100 A/μs		-	129	260	ns
Body diode reverse recovery charge	Q <sub>rr</sub>			=	685	1400	nC
Reverse recovery fall time	t <sub>a</sub>			=	106	-	ns
Reverse recovery rise time	t <sub>b</sub>			=	26	-	
Body diode peak reverse recovery current	I <sub>RM(REC)</sub>			-	-11	-	Α

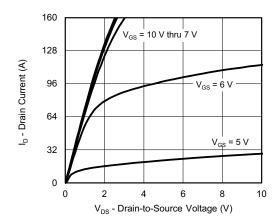
### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

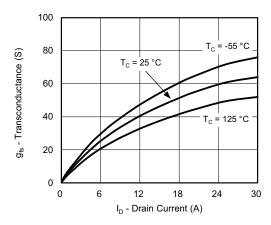
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



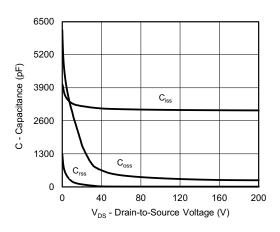
### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



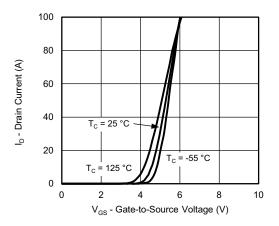
#### **Output Characteristics**



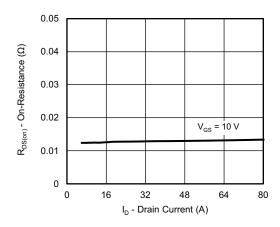
Transconductance



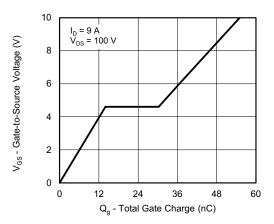
Capacitance



**Transfer Characteristics** 



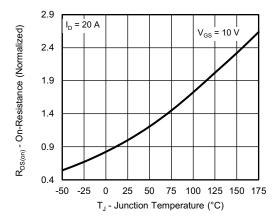
On-Resistance vs. Drain Current



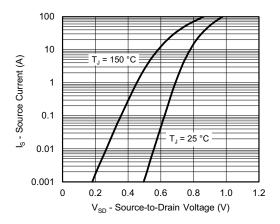
**Gate Charge** 



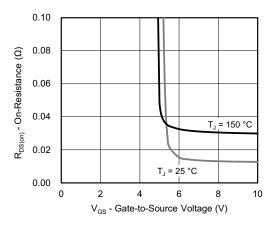
### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



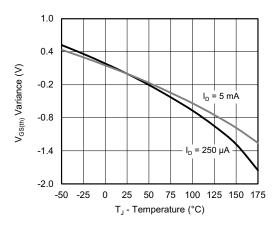
On-Resistance vs. Junction Temperature



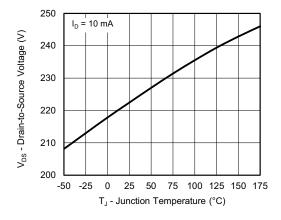
**Source Drain Diode Forward Voltage** 



On-Resistance vs. Gate-to-Source Voltage



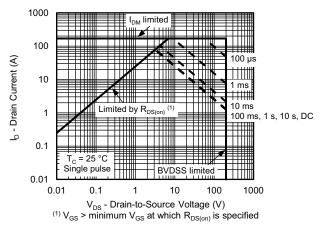
**Threshold Voltage** 



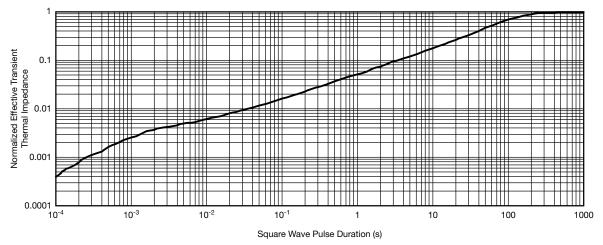
**Drain Source Breakdown vs. Junction Temperature** 



### **THERMAL RATINGS** ( $T_A = 25$ °C, unless otherwise noted)



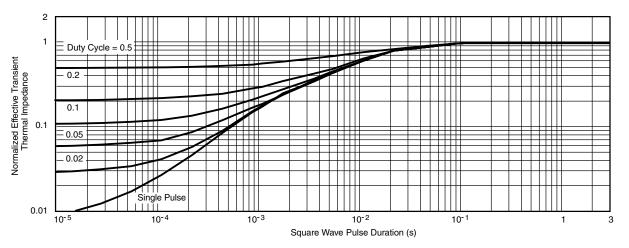
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

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### THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction to Case (25 °C)

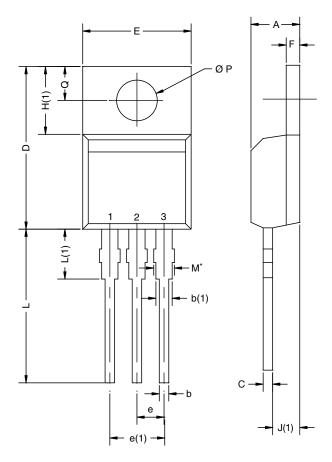
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg276843">www.vishay.com/ppg276843</a>



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## **TO-220AB**



	D2

	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØΡ	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: T14-0413-Rev. P, 16-Jun-14 DWG: 5471				

#### Note

 $<sup>^{\</sup>star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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