

Vishay Siliconix

N-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)	Q _g (Typ.)		
60	0.006 at V_{GS} = 10 V	90 ^d	78.5		

FEATURES

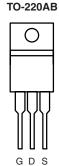
- TrenchFET[®] Power MOSFET
- 175 °C Junction Temperature ٠
- 100 % R_g and UIS Tested •
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

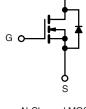
- Power Supply
- Secondary Synchronous Rectification

D

Industrial



Top View Ordering Information: SUP90N06-6m0P-E3 (Lead (Pb)-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _C = 25 °C, unless oth	erwise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	60	N/	
Gate-Source Voltage		V _{GS}	± 20	- V	
Continuous Drain Current (T ₁ = 175 °C)	T _C = 25 °C	L	90 ^d	А	
Continuous Drain Current $(1j = 175 C)$	T _C = 70 °C	I _D	90 ^d		
Pulsed Drain Current		I _{DM}	240	A	
Avalanche Current	I _{AS}	50	1		
Single Avalanche Energy ^a L = 0.1 mH		E _{AS}	125	mJ	
	T _C = 25 °C	Р	272 ^b	w	
Maximum Power Dissipation ^a	T _A = 25 °C ^c	– P _D –	3.75		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W		
Junction-to-Case (Drain)	R _{thJC}	0.55	C/ W		

Notes:

b. See SOA curve for voltage derating.c. When Mounted on 1" square PCB (FR-4 material).

d. Package limited.

a. Duty cycle \leq 1 %.

SUP90N06-6m0P

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 V, I_{D} = 250 \mu A$	60			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	2.5		4.5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 250	nA	
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			50	μΑ	
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$			250		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10$ V, $V_{GS} = 10$ V	70			А	
	B	V _{GS} = 10 V, I _D = 20 A		0.005	0.006	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V_{GS} = 10 V, I _D = 20 A, T _J = 125 °C		0.008	0.010		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 20 A		58		S	
Dynamic ^b							
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 30 V, f = 1 MHz		4700		pF	
Output Capacitance	C _{oss}			620			
Reverse Transfer Capacitance	C _{rss}			250			
Total Gate Charge ^c	Qg			78.5	120	nC	
Gate-Source Charge ^c	Q _{gs}	V_{DS} = 30 V, V_{GS} = 10 V, I_D = 50 A		28			
Gate-Drain Charge ^c	Q _{gd}			20.6			
Gate Resistance	Rg	f = 1 MHz		1.2	2.4	Ω	
Turn-On Delay Time ^c	t _{d(on)}			16	30		
Rise Time ^c	t _r	V_{DD} = 30 V, R_L = 0.6 Ω		10	20	ns	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ 50 A, V_{GEN} = 10 V, R_g = 1 Ω		25	40		
Fall Time ^c	t _f			8	15		
Source-Drain Diode Ratings and Cha	racteristics	$T_{\rm C} = 25 \ {}^{\circ}{\rm C}^{\rm b}$					
Continuous Current	ا _S				85	٨	
Pulsed Current	I _{SM}				240	A	
Forward Voltage ^a	V _{SD}	$I_{F} = 20 \text{ A}, V_{GS} = 0 \text{ V}$		0.83	1.5	V	
Reverse Recovery Time	t _{rr}			62	100	ns	
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 75 A, dl/dt = 100 A/μs		3.8	5.7	А	
Reverse Recovery Charge	Q _{rr}			118	180	nC	

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

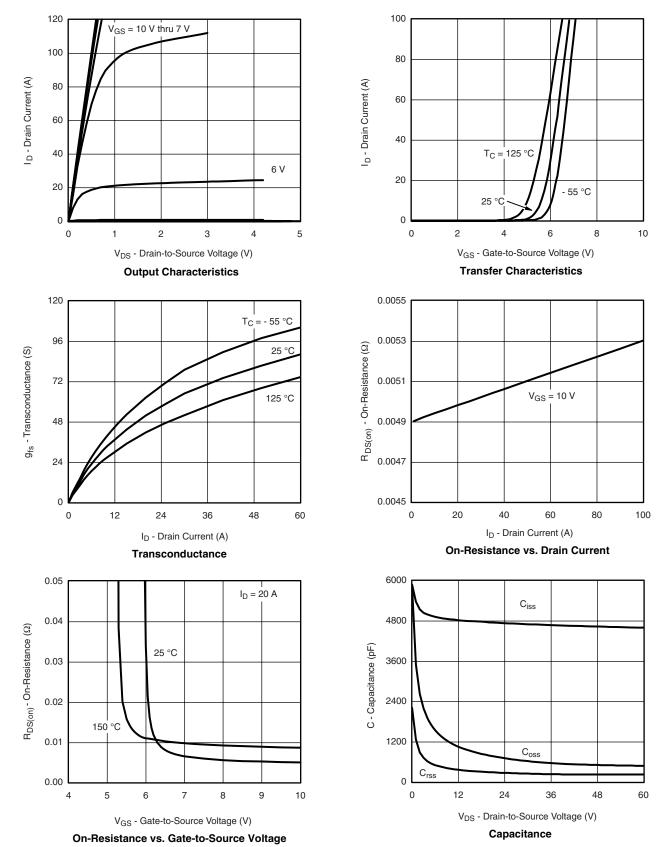
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



SUP90N06-6m0P

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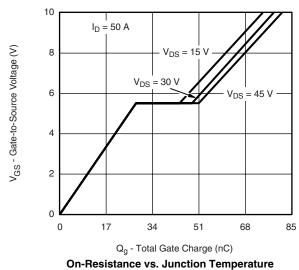


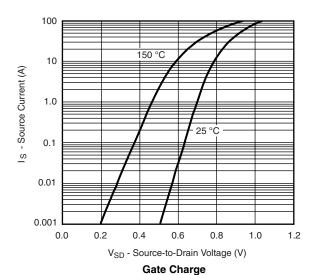
Document Number: 69536 S09-0703-Rev. B, 27-Apr-09

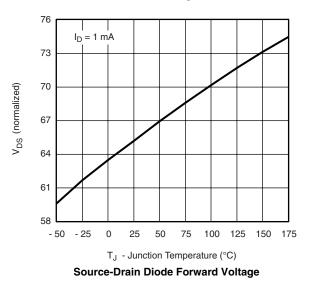


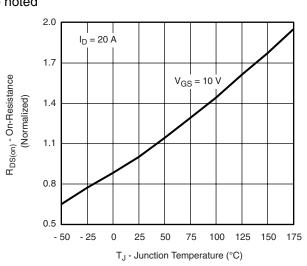
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

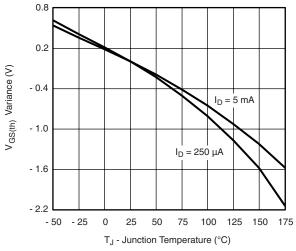




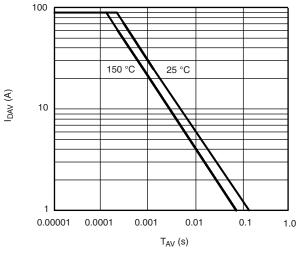




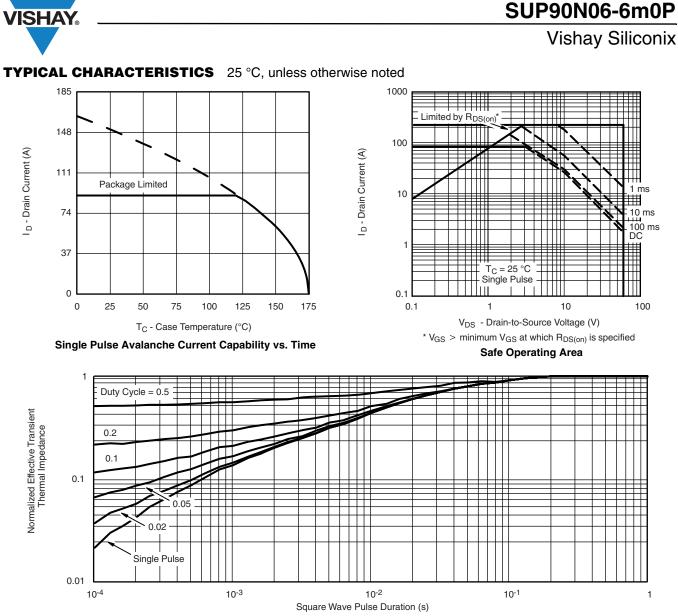
Threshold Voltage



On-Resistance vs. Junction Temperature



Maximum Drain Current vs. Case Temperature



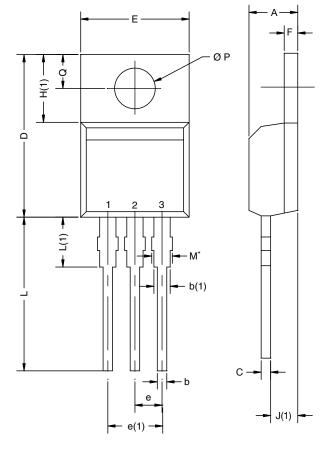
Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg269536.



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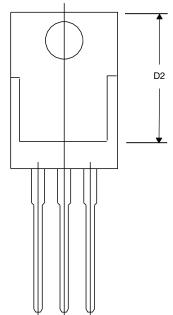
TO-220AB



	MILLIMETERS		INCHES	
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØР	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: T14-0 DWG: 5471	0413-Rev. P, 1	16-Jun-14	•	•

Note

 * M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



Revison: 16-Jun-14

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