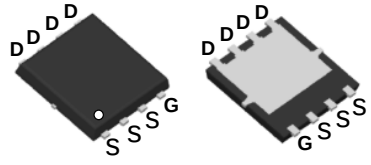
WMB082N06LG2

60V N-Channel Enhancement Mode Power MOSFET

Description

WMB082N06LG2 uses Wayon's 2nd generation power trench MOSFET technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance. This device is well suited for high efficiency fast switching applications.

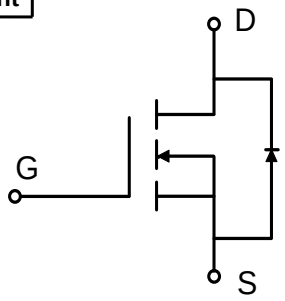


PDFN5060-8L

Features

- V_{DS} = 60V, I_D = 52A
  $R_{DS(on)}$ < 8.2mΩ @ V_{GS} = 10V
  $R_{DS(on)}$ < 12mΩ @ V_{GS} = 4.5V
- Green Device Available
- 100% EAS Guaranteed
- Low $R_{DS(ON)}$

RoHS compliant



Applications

- Industrial and Motor Drive Application
- DC/DC Converters
- Power Management Functions

Absolute Maximum Ratings

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current[1]	T_C=25℃	I_D	52	A
	T_C=100℃		34	
Pulsed Drain Current[2]		I_{DM}	160	A
Single Pulse Avalanche Energy[3]		EAS	196	mJ
Avalanche Current		I_{AS}	28	A
Total Power Dissipation[4]	T_C=25℃	P_D	42	W
	T_C=100℃		18	
Operating Junction and Storage Temperature Range		T_J , T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Case[1]	$R_{\theta JC}$	3	°C/W

Electrical Characteristics T_c = 25°C, unless otherwise noted

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	60	-	-	V
Gate-body Leakage Current		I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T_J=25℃	I_{DSS}	V_{DS} = 60V, V_{GS} = 0V	-	-	1	μA
	T_J=55℃			-	-	5	
Gate-Threshold Voltage		$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.2	1.7	2.5	V
Drain-Source On-Resistance[2]		$R_{DS(on)}$	V_{GS} = 10V, I_D = 20A	-	5.9	8.2	mΩ
			V_{GS} = 4.5V, I_D = 10A		7.7	12	
Dynamic Characteristics							
Input Capacitance		C_{iss}	V_{DS} = 30V, V_{GS} =0V, f =1MHz	-	1935	-	pF
Output Capacitance		C_{oss}		-	455	-	
Reverse Transfer Capacitance		C_{rss}		-	13.5	-	
Switching Characteristics							
Gate Resistance		R_G	V_{DS} = 0V, V_{GS} =0V, f =1MHz	-	1.6	-	Ω
Total Gate Charge		Q_g	V_{GS} = 10V, V_{DD} = 30V, I_D= 20A	-	30	-	nC
Gate-Source Charge		Q_{gs}		-	5.8	-	
Gate-Drain Charge		Q_{gd}		-	5.9	-	
Turn-On Delay Time		$t_{d(on)}$	V_{GS} =10V, V_{DD} = 30V,R_G = 3Ω, R_L = 2.5Ω	-	9.7	-	nS
Rise Time		t_r		-	4.8	-	
Turn-Off Delay Time		$t_{d(off)}$		-	29	-	
Fall Time		t_f		-	7.8	-	
Drain-Source Body Diode Characteristics							
Diode Forward Voltage[2]		V_{SD}	I_S = 20A, V_{GS} = 0V	-	-	1.2	V
Reverse Recovery Time		t_{rr}	I_F=20A, dI_F/dt=500A/μs	-	58	-	ns
Reverse Recovery Charge		Q_{rr}		-	17	-	nC

Notes:

1.The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%

3.The EAS data shows Max. rating . The test condition is V_{DD}=30V,V_{GS}=10V,L=0.5mH,I_{AS}=28A,,R_G=25Ω

4.The power dissipation is limited by 150°C junction temperature

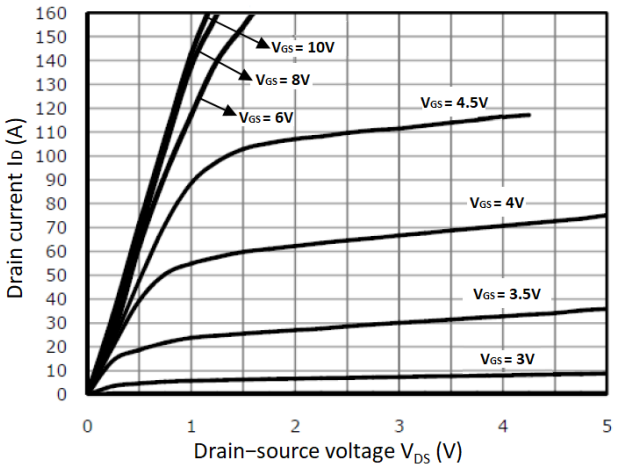


Figure 1. Output Characteristics

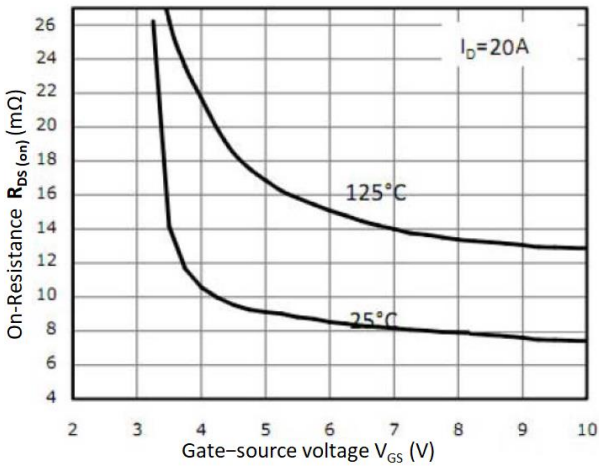


Figure 2. $R_{DS(on)}$ vs. V_{GS}

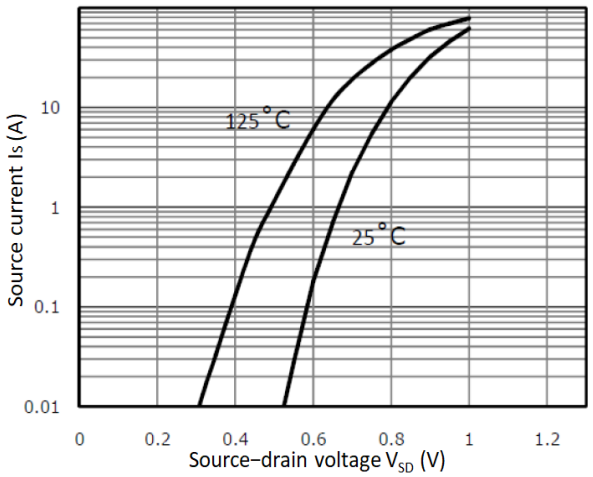


Figure3. Forward Characteristics of Reverse

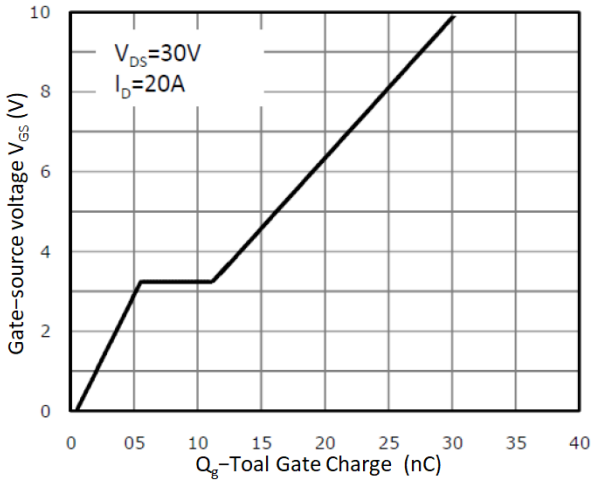


Figure4.Gate Charge Characteristics

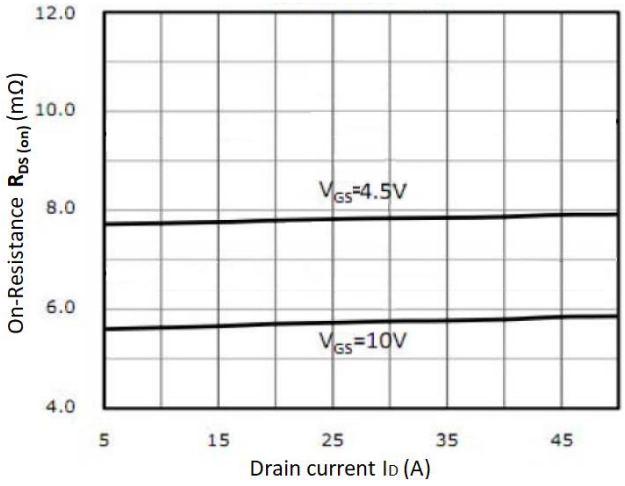


Figure 5. $R_{DS(ON)}$ Vs I_D

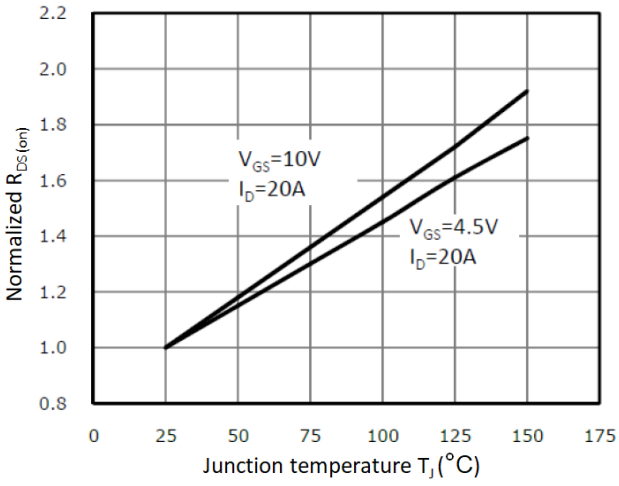


Figure 6. Normalized $R_{DS(on)}$ vs. T_J

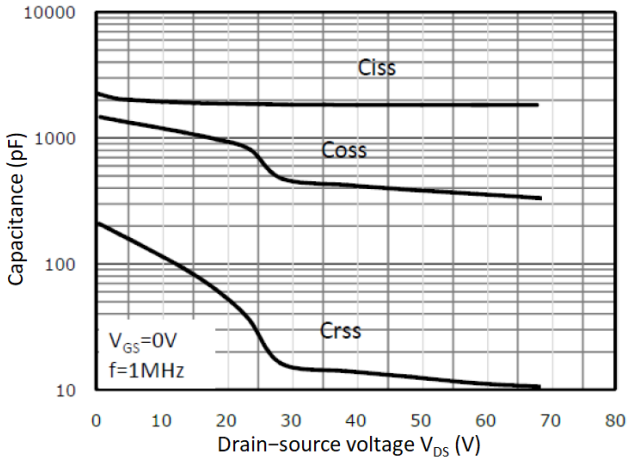


Figure 7. Capacitance Characteristics

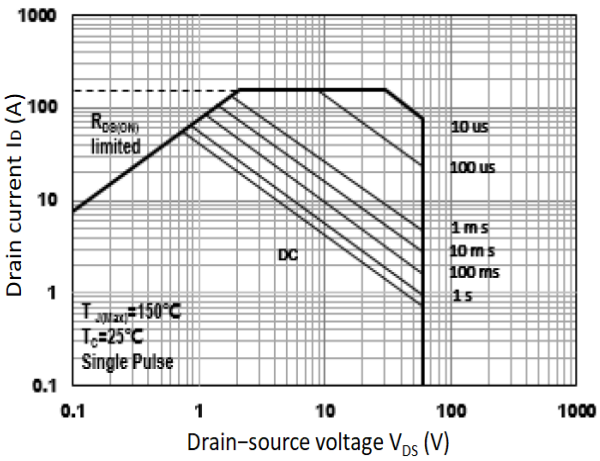


Figure 8. Safe Operating Area

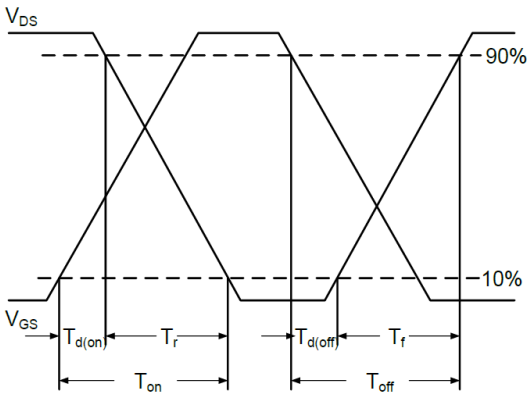


Figure 9.Switching Time Waveform

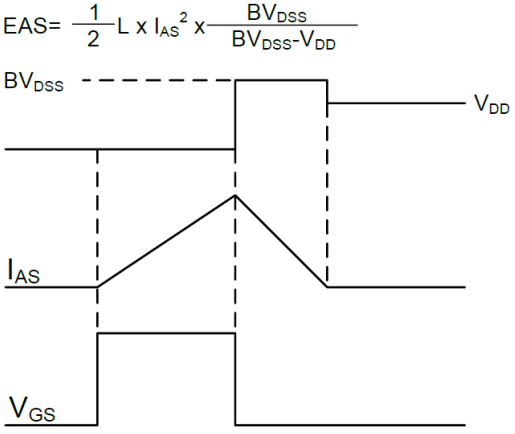
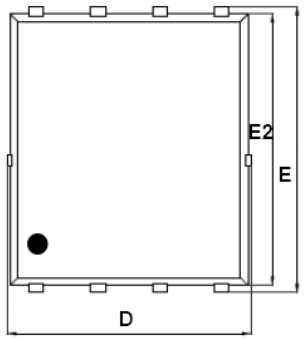
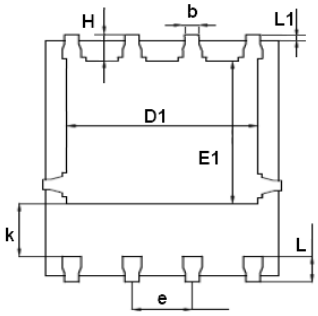
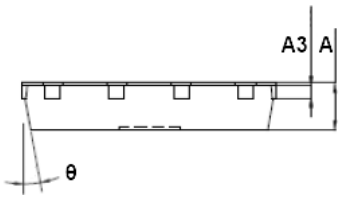


Figure 10.Unclamped Inductive Switching Waveform

Mechanical Dimensions for PDFN5060-8L







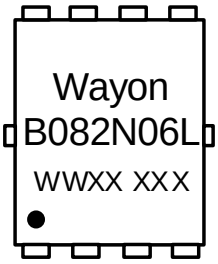
COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	0.90	1.17
A3	0.20	0.35
D	4.80	5.40
E	5.90	6.15
D1	3.61	4.31
E1	3.30	3.78
E2	5.65	5.85
k	1.10	-
b	0.30	0.51
e	1.27BSC	
L	0.38	0.71
L1	0.05	0.36
H	0.38	0.61
θ	0°	12°

Ordering Information

Part	Package	Marking	Packing method
WMB082N06LG2	PDFN5060-8L	B082N06L	Tape and Reel

Marking Information



B082N06L = Device code

WWXX XXX= Date code

Contact Information

No.1001, Shiwan(7) Road, Pudong District, Shanghai, P.R.China.201207
Tel: 86-21-50310888 Fax: 86-21-50757680 Email: market@way-on.com
WAYON website: **http://www.way-on.com**
For additional information, please contact your local Sales Representative.

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