

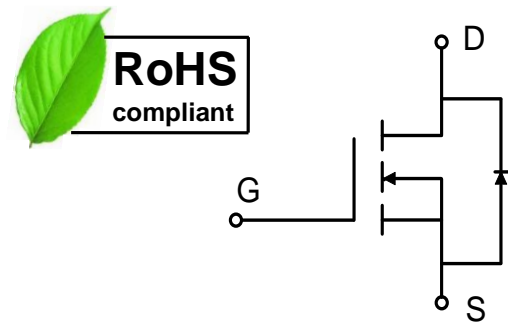
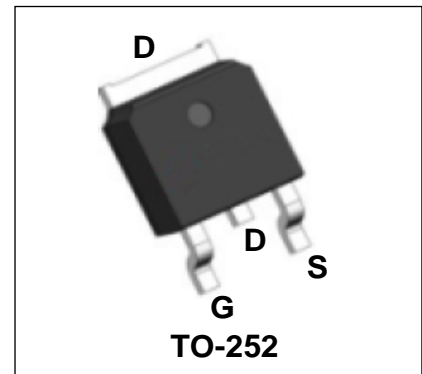
60V N-Channel Enhancement Mode Power MOSFET

Description

WMO60N10T2 uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Features

- $V_{DS} = 100V$, $I_D = 60A$
 $R_{DS(on)} < 11.5m\Omega @ V_{GS} = 10V$
 $R_{DS(on)} < 15m\Omega @ V_{GS} = 4.5V$
- Green Device Available
- Low Gate Charge
- 100% EAS Guaranteed



Applications

- Power Management Switches
- Synchronous Rectification for AC/DC Quick Charger

Absolute Maximum Ratings

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ¹	$T_C = 25^\circ C$	I_D	60	A
	$T_C = 100^\circ C$		38	
Pulsed Drain Current ²		I_{DM}	270	A
Single Pulse Avalanche Energy ³		EAS	33	mJ
Avalanche Current		I_{AS}	15	A
Total Power Dissipation ⁴	$T_C = 25^\circ C$	P_D	104	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to +150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ¹	R_{θJA}	60	$^\circ C/W$
Thermal Resistance from Junction-to-Case ¹	R_{θJC}	1.2	$^\circ C/W$

Electrical Characteristics $T_c = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100	-	-	V
Gate-body Leakage current	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	± 100	nA
Zero Gate Voltage Drain Current	$T_J=25^\circ\text{C}$	$V_{DS} = 80V, V_{GS} = 0V$	-	-	1	μA
	$T_J=55^\circ\text{C}$		-	-	5	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.2	1.7	2.3	V
Drain-Source On-Resistance ²	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 20A$	-	7.7	11.5	m Ω
		$V_{GS} = 4.5V, I_D = 10A$	-	9.7	15	
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS} = 50V, V_{GS} = 0V, f = 1\text{MHz}$	-	2120	-	pF
Output Capacitance	C_{oss}		-	330	-	
Reverse Transfer Capacitance	C_{rss}		-	7.2	-	
Switching Characteristics						
Total Gate Charge	Q_g	$V_{GS} = 4.5V, V_{DS} = 50V, I_D = 20A$	-	18.5	-	nC
Total Gate Charge	Q_g	$V_{GS} = 10V, V_{DS} = 50V, I_D = 20A$	-	43	-	
Gate-Source Charge	Q_{gs}		-	8.5	-	
Gate-Drain Charge	Q_{gd}		-	10.3	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10V, V_{DD} = 50V, R_G = 3.3\Omega, I_D = 20A$	-	10	-	nS
Rise Time	t_r		-	7	-	
Turn-Off Delay Time	$t_{d(off)}$		-	50	-	
Fall Time	t_f		-	11	-	
Drain-Source Body Diode Characteristics						
Diode Forward Voltage ²	V_{SD}	$I_S = 1A, V_{GS} = 0V$	-	-	1.2	V
Continuous Source Current ^{1,5}	I_S	$V_G = V_D = 0V$, Force Current	-	-	36	A
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 20A, di/dt = 100A/\mu s$	-	45	-	nS
Body Diode Reverse Recovery Charge	Q_{rr}		-	165	-	nC

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The EAS data shows Max. rating. The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.3mH, I_{AS}=15A$
- The power dissipation is limited by 150°C junction temperature
- The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

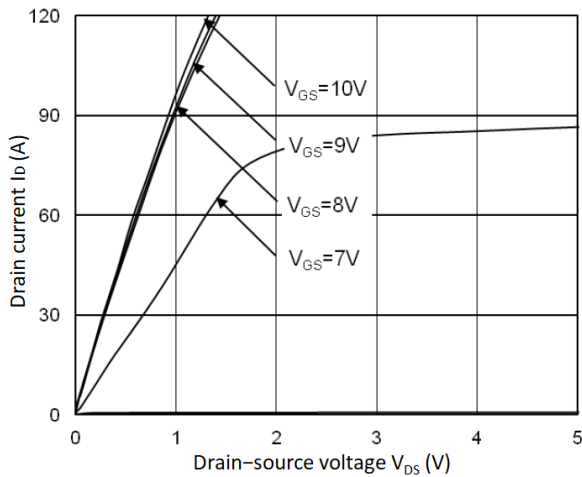


Figure 1. Output Characteristics

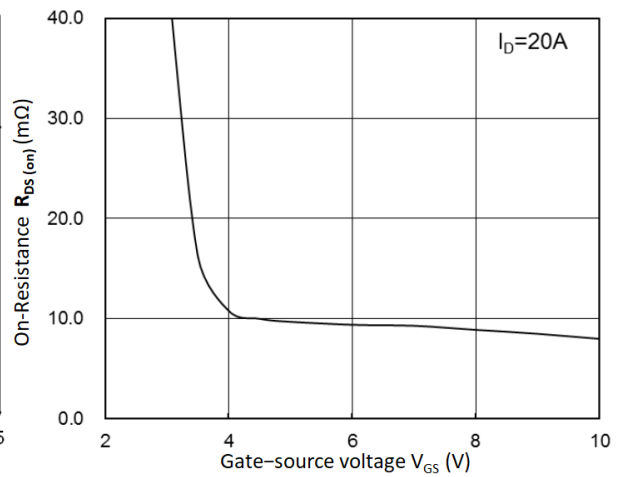


Figure 2. $R_{DS(on)}$ vs. V_{GS}

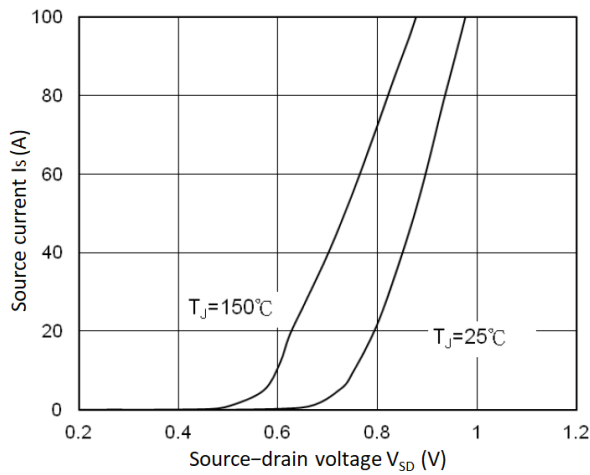


Figure 3. Forward Characteristics of Reverse

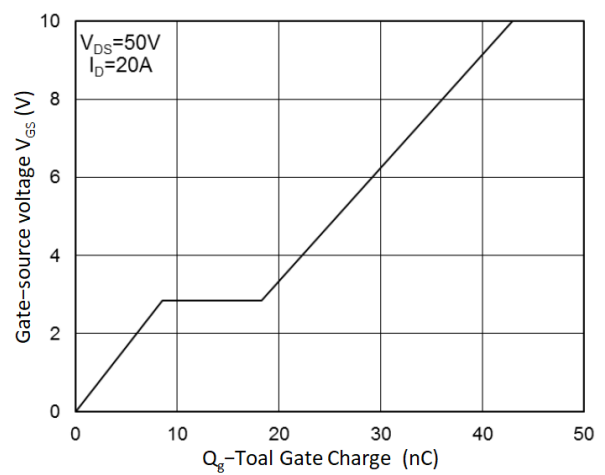


Figure 4. Gate Charge Characteristics

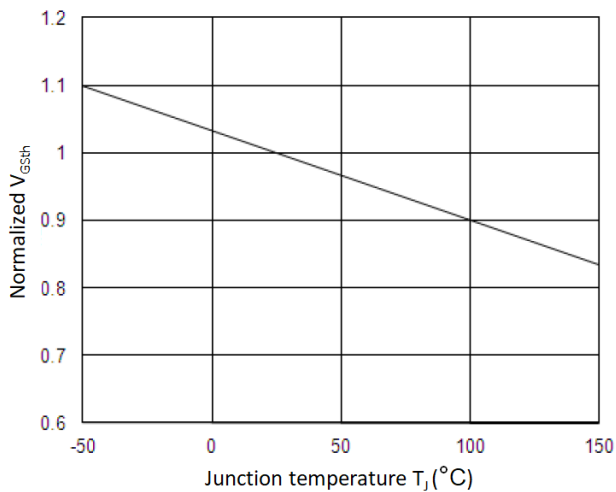


Figure 5. Normalized $V_{GS(th)}$ vs. T_J

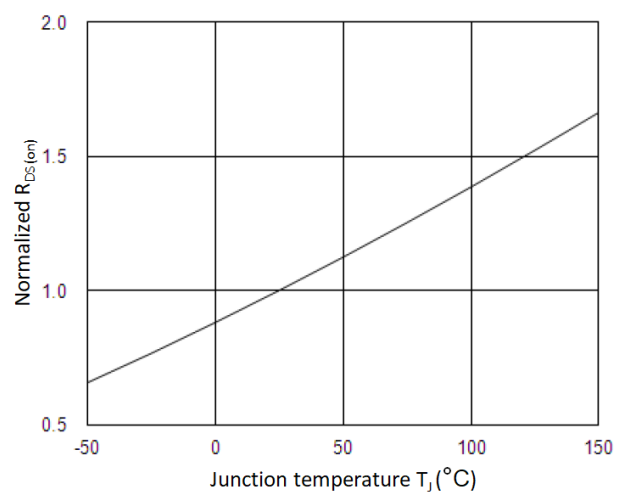


Figure 6. Normalized $R_{DS(on)}$ vs. T_J

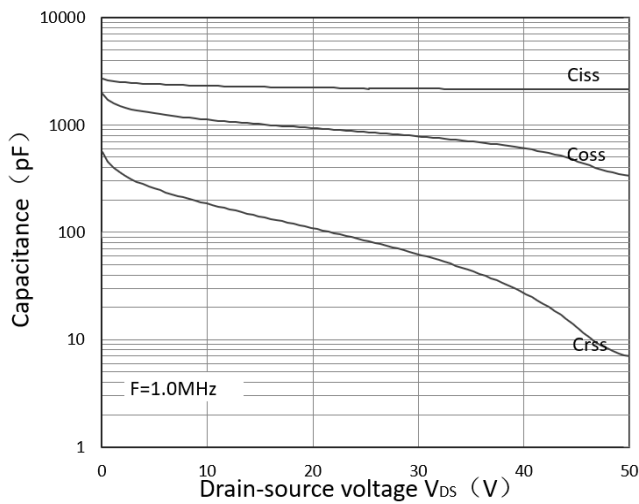


Figure 7. Capacitance Characteristics

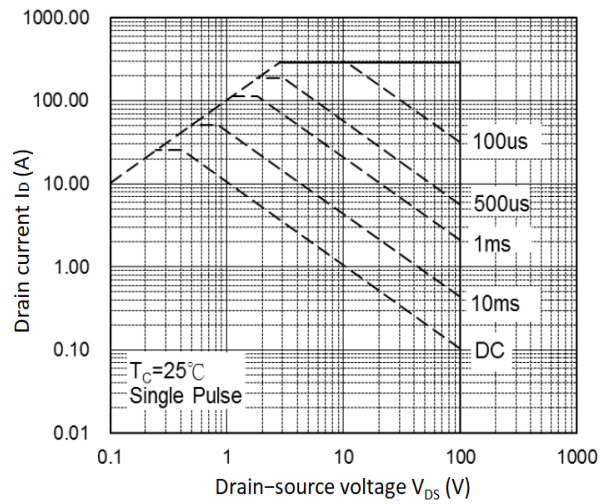


Figure 8. Safe Operating Area

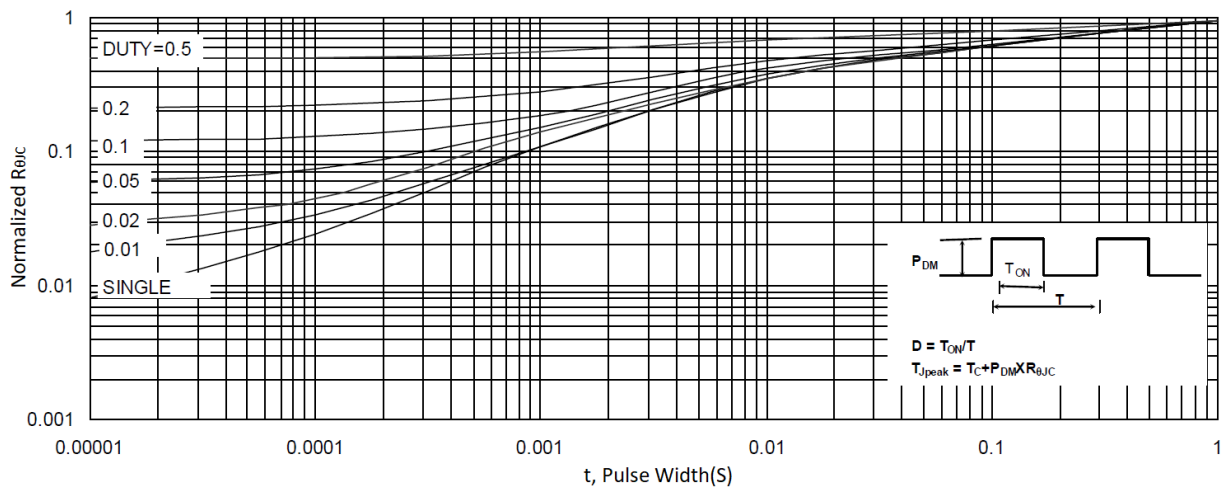


Figure 9. Normalized Maximum Transient Thermal Impedance

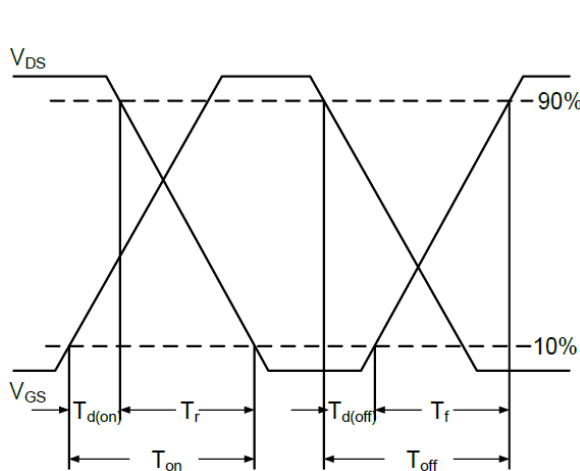


Figure 10. Switching Time Waveform

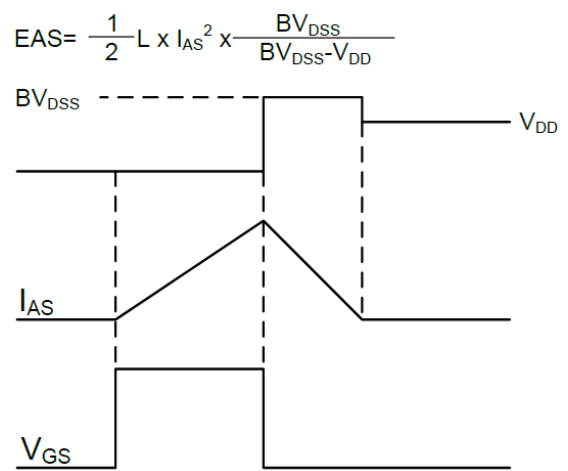
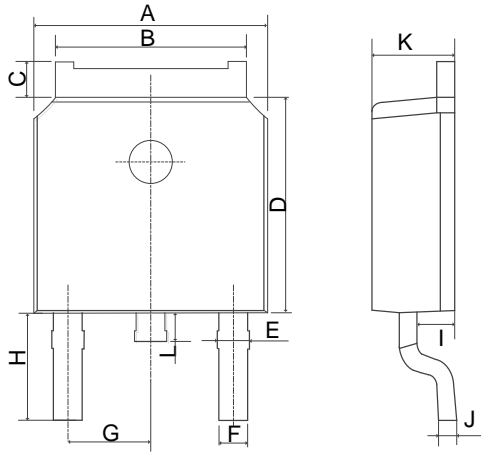


Figure 11. Unclamped Inductive Switching Waveform

$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

Mechanical Dimensions for TO-252



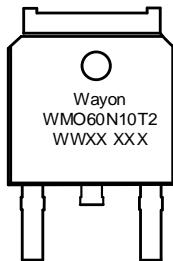
COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	6.40	6.80
B	5.13	5.50
C	0.88	1.28
D	5.90	6.22
E	0.68	1.10
F	0.68	0.91
G	2.29REF	
H	2.90REF	
I	0.85	1.17
J	0.51REF	
K	2.10	2.50
L	0.40	1.00

Ordering Information

Part	Package	Marking	Packing method
WMO60N10T2	TO-252	WMO60N10T2	Tape and Reel

Marking Information



WMO60N10T2 = Device code

WWXX XXX= Date Code


Contact Information

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