

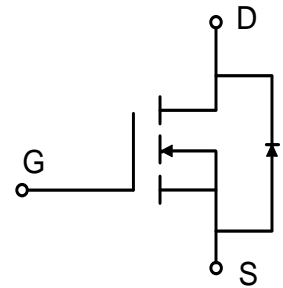
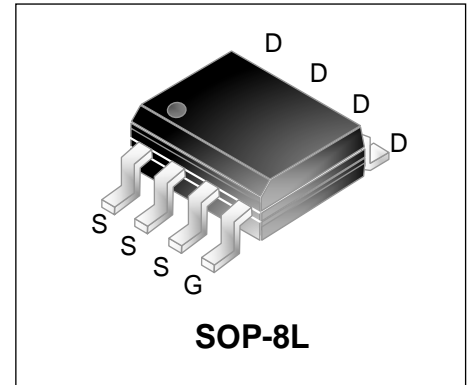
## 100V N-Channel Enhancement Mode Power MOSFET

### Description

WMS08N10T1 uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

### Features

- $V_{DS} = 100V$ ,  $I_D = 8A$   
 $R_{DS(on)} < 24m\Omega$  @  $V_{GS} = 10V$   
 $R_{DS(on)} < 28m\Omega$  @  $V_{GS} = 4.5V$
- Low  $R_{DS(on)}$
- Low Gate Charge
- 100% EAS Guaranteed



### Applications

LED Lighting, Charger, Adapter, PC, LCD TV, Server

### Absolute Maximum Ratings

Parameter		Symbol	Value	Unit
Drain-Source voltage		$V_{DS}$	100	V
Gate-Source voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current@10V <sup>1</sup>	$T_A = 25^\circ C$	$I_D$	8	A
	$T_A = 100^\circ C$		6.6	
Pulsed Drain Current <sup>2</sup>		$I_{DM}$	32	A
Single Pulse Avalanche Energy <sup>3</sup>		<b>EAS</b>	29	mJ
Avalanche Current		$I_{AS}$	24	A
Total Power Dissipation <sup>4</sup>	$T_A = 25^\circ C$	$P_D$	2.7	W
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55 to +150	$^\circ C$

### Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient <sup>1</sup> ( $t \leq 10S$ )	$R_{\theta JA}$	45	$^\circ C/W$
Thermal Resistance from Junction-to-Ambient <sup>1</sup>	$R_{\theta JA}$	80	$^\circ C/W$

**Electrical Characteristics**  $T_c = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100	-	-	V
Gate-body Leakage current	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	$\pm 100$	nA
Zero Gate Voltage Drain Current	$T_J=25^\circ\text{C}$	$V_{DS} = 80V, V_{GS} = 0V$	-	-	1	$\mu A$
	$T_J=55^\circ\text{C}$		-	-	5	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.2	-	2.5	V
Drain-Source On-Resistance <sup>2</sup>	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 8A$	-	20	24	m $\Omega$
		$V_{GS} = 4.5V, I_D = 4A$	-	23	28	
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 25V, V_{GS} = 0V, f = 1\text{MHz}$	-	3350	-	pF
Output Capacitance	$C_{oss}$		-	150	-	
Reverse Transfer Capacitance	$C_{rss}$		-	110	-	
<b>Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{GS} = 10V, V_{DS} = 30V, I_D = 8A$	-	57	-	nC
Gate-Source Charge	$Q_{gs}$		-	8.7	-	
Gate-Drain Charge	$Q_{gd}$		-	14	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10V, V_{DD} = 30V, R_G = 3.3\Omega, I_D = 1A$	-	16.2	-	nS
Rise Time	$t_r$		-	41.2	-	
Turn-Off Delay Time	$t_{d(off)}$		-	56.4	-	
Fall Time	$t_f$		-	16.2	-	
<b>Drain-source body diode Characteristics</b>						
Diode Forward Voltage <sup>2</sup>	$V_{SD}$	$I_S = 1A, V_{GS} = 0V$	-	-	1.2	V
Continuous Source Current <sup>1,5</sup>	$I_S$	$V_G = V_D = 0V$ , Force Current	-	-	8	A
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 8A, di/dt = 100A/\mu s$	-	44	-	nS
Body Diode Reverse Recovery Charge	$Q_{rr}$		-	25	-	nC

## Notes:

- The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$
- The EAS data shows Max. rating. The test condition is  $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=24A$
- The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.

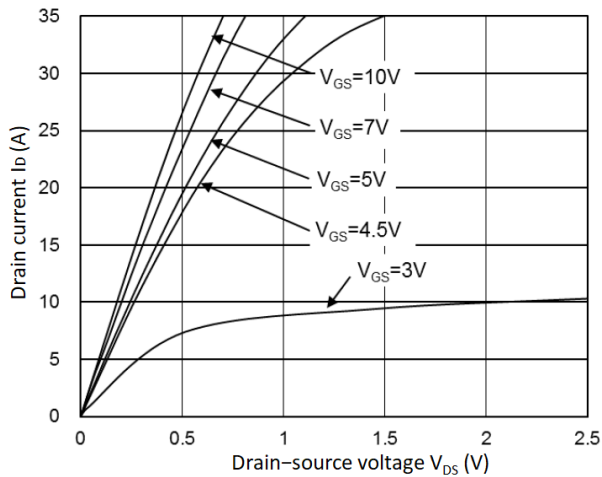


Figure 1. Typical Output Characteristics

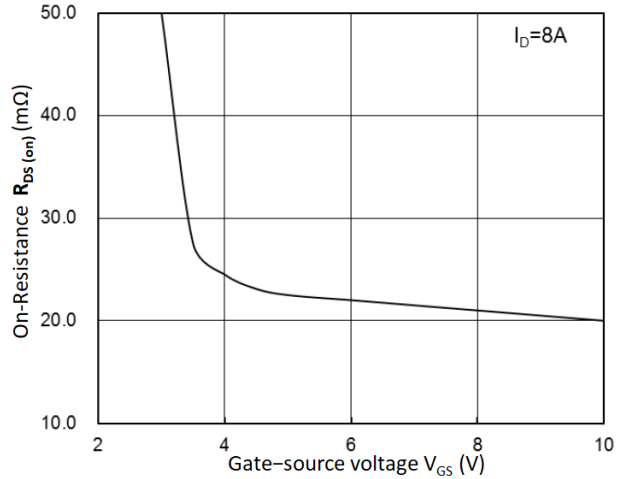


Figure 2.  $R_{DS(on)}$  vs.  $V_{GS}$

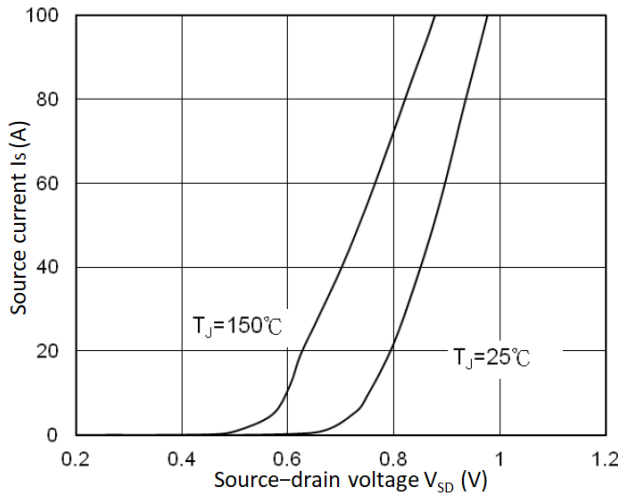


Figure 3. Forward Characteristics Of Reverse

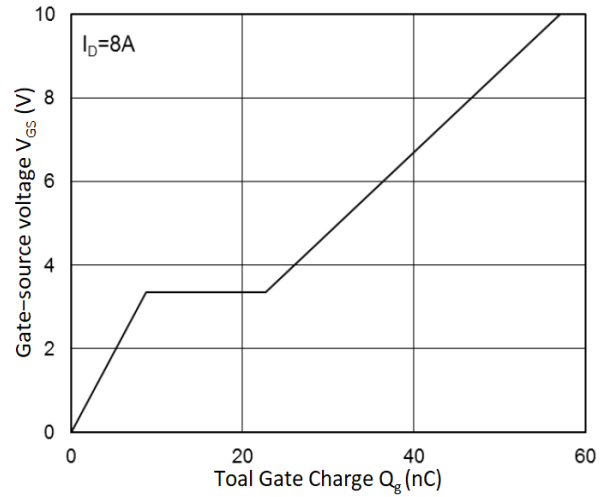


Figure 4. Gate Charge Characteristics

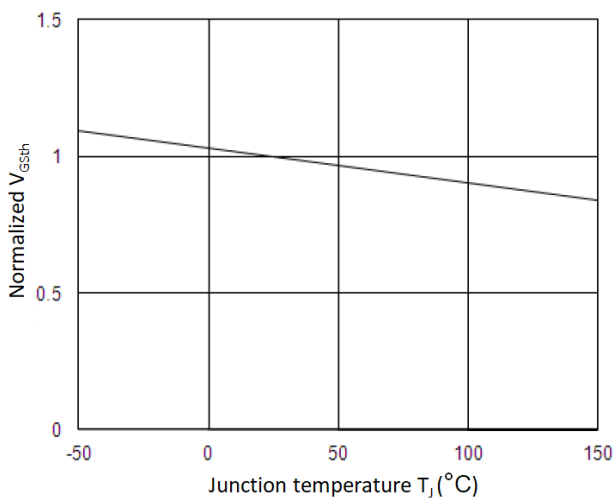


Figure 5. Normalized  $V_{GS(th)}$  vs.  $T_J$

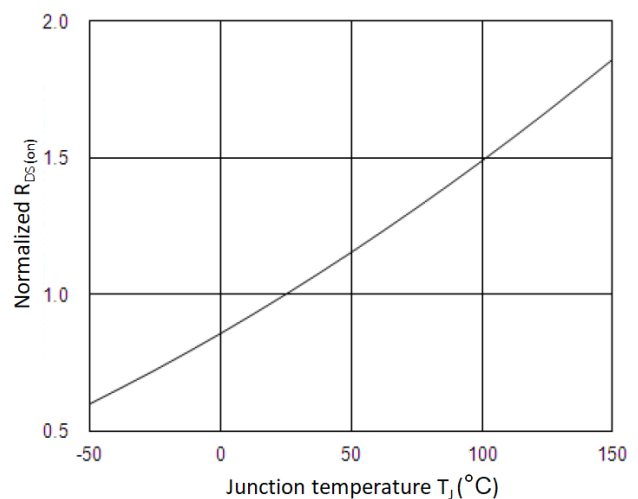


Figure 6. Normalized  $R_{DS(on)}$  vs.  $T_J$

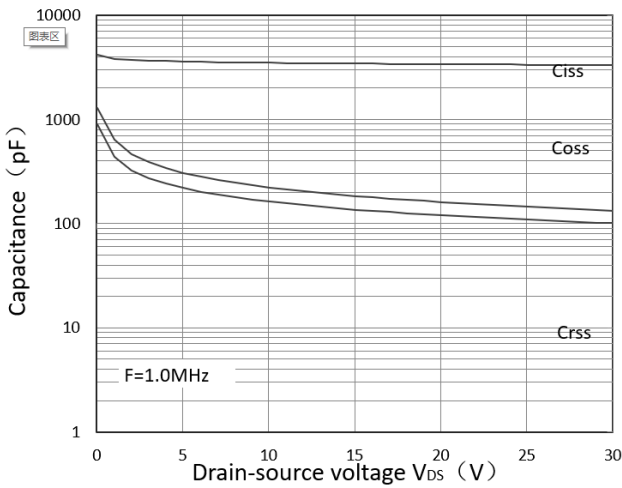


Figure7. Capacitance Characteristics

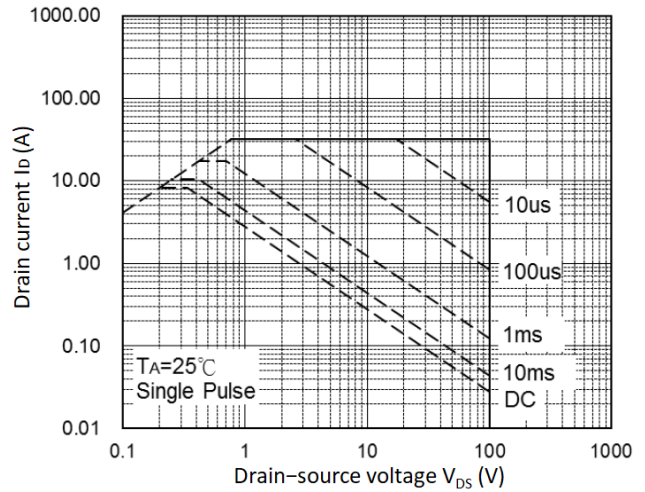


Figure 8. Safe Operating Area

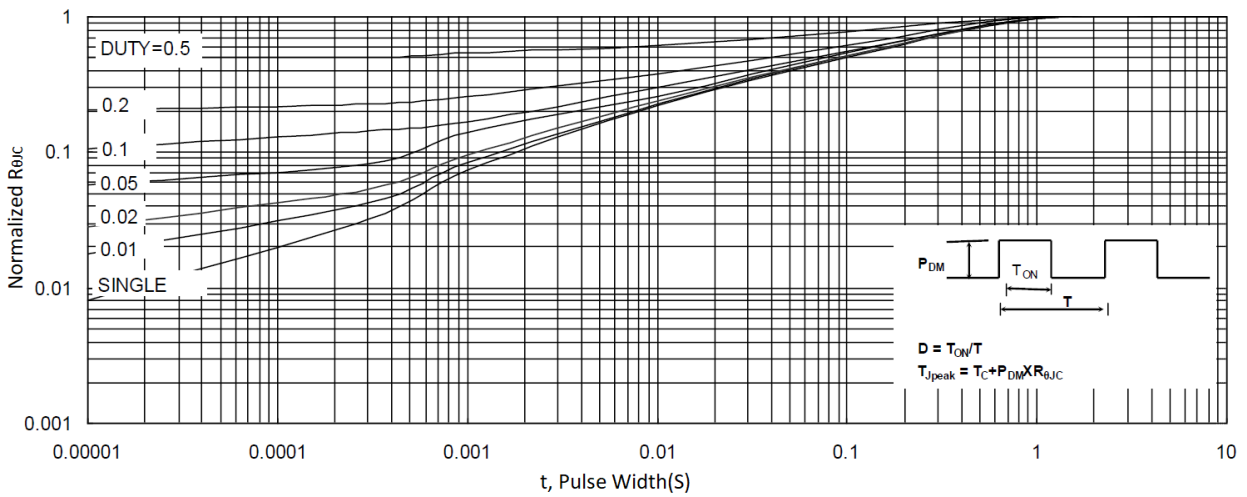


Figure 9. Normalized Maximum Transient Thermal Impedance

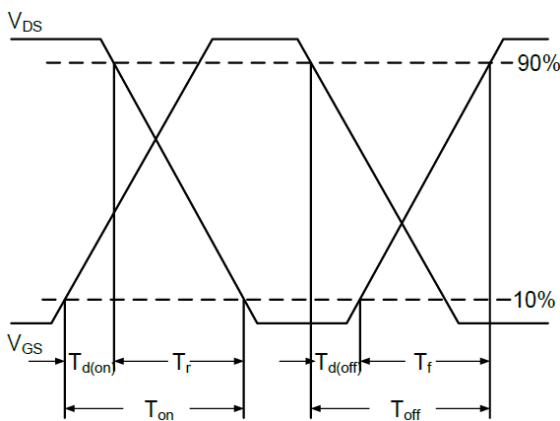


Figure 10. Switching Time Waveform

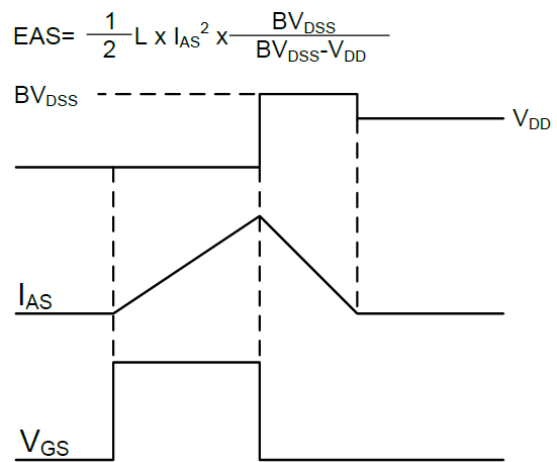
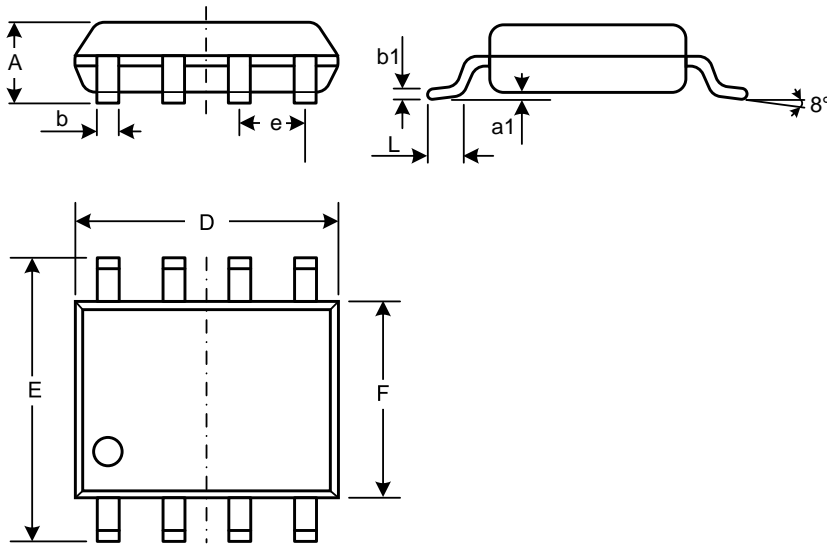


Figure 11. Unclamped Inductive Switching Waveform

$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

Mechanical Dimensions for SOP-8L

COMMON DIMENSIONS

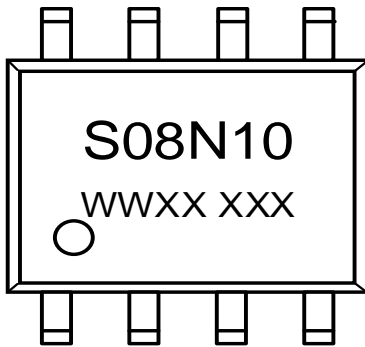


SYMBOL	MM	
	MIN	MAX
A	1.23	1.75
a1	0.05	0.25
b	0.31	0.51
b1	0.16	0.25
D	4.70	5.15
E	5.75	6.25
e	1.07	1.47
F	3.70	4.10
L	0.4	1.27

## Ordering Information

Part	Package	Marking	Packing method
WMS08N10T1	SOP-8L	S08N10	Tape and Reel

## Marking Information



S08N10 = Device code

WWXX XXX= Date code


## Contact Information

No.1001, Shiwan(7) Road, Pudong District, Shanghai, P.R.China.201207

Tel: 86-21-50310888 Fax: 86-21-50757680 Email: market@way-on.com

WAYON website: <http://www.way-on.com>

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