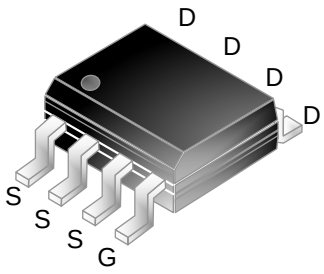
WMS12N10T2

100V N-Channel Enhancement Mode Power MOSFET

Description

WMS12N10T2 uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

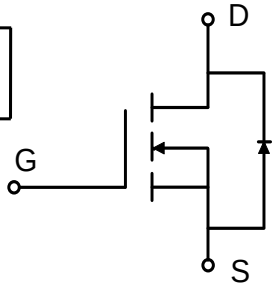


SOP-8L

Features

- V_{DS} =100V, I_D = 11.5A
  $R_{DS(on)}$ < 12.0mΩ @ V_{GS} = 10V
  $R_{DS(on)}$ < 15.5mΩ @ V_{GS} = 4.5V
- Low $R_{DS(on)}$
- Low Gate Charge
- 100% EAS Guaranteed

RoHS compliant



Applications

- Power Management Switches
- Synchronous Rectification for AC/DC Quick Charger

Absolute Maximum Ratings

Parameter		Symbol	Value	Unit
Drain-Source voltage		V_{DS}	100	V
Gate-Source voltage		V_{GS}	±20	V
Continuous Drain Current@10V[1]	T_A=25℃	I_D	11.5	A
	T_A=70℃		9	
Pulsed Drain Current[2]		I_{DM}	46	A
Single Pulse Avalanche Energy[3]		EAS	12	mJ
Avalanche Current		I_{AS}	9	A
Total Power Dissipation[4]	T_A=25℃	P_D	3.1	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to+150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient[1]	$R_{\theta JA}$	75	°C/W
Thermal Resistance from Junction-to-Case[1]	$R_{\theta JC}$	24	°C/W

Electrical Characteristics T_c = 25°C, unless otherwise noted

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	100	-	-	V
Gate-body Leakage current		I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T_J=25℃	I_{DSS}	V_{DS} = 80V, V_{GS} = 0V	-	-	1	μA
	T_J=55℃			-	-	5	
Gate-Threshold Voltage		$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.2	1.7	2.3	V
Drain-Source On-Resistance[2]		$R_{DS(on)}$	V_{GS} = 10V, I_D = 11.5A	-	7.9	12	mΩ
			V_{GS} = 4.5V, I_D = 9.5A	-	9.9	15.5	
Forward Transconductance		g_{fs}	V_{DS} = 5V, I_D = 11.5A	-	45	-	S
Dynamic Characteristics S							
Input Capacitance		C_{iss}	V_{DS} = 50V, V_{GS} =0V, f =1MHz	-	2220	-	pF
Output Capacitance		C_{oss}		-	350	-	
Reverse Transfer Capacitance		C_{rss}		-	12	-	
Switching Characteristics							
Total Gate Charge(10V）		Q_g	V_{GS} = 10V,V_{DS} = 50V, I_D= 11.5A		35		nC
Total Gate Charge(4.5V)		Q_g		-	16	-	
Gate-Source Charge		Q_{gs}		-	8	-	
Gate-Drain Charge		Q_{gd}		-	4	-	
Turn-On Delay Time		$t_{d(on)}$	V_{GS} =10V, V_{DD} = 50V,R_G = 3Ω, I_D= 11.5A	-	9	-	nS
Rise Time		t_r		-	4.5	-	
Turn-Off Delay Time		$t_{d(off)}$		-	35	-	
Fall Time		t_f		-	5.5	-	
Drain-source body diode Characteristics							
Diode Forward Voltage[2]		V_{SD}	I_S = 1A, V_{GS} = 0V	-	-	1.1	V
Continuous Source Current[1,5]		I_S	V_G=V_D=0V , Force Current	-	-	4	A
Body Diode Reverse Recovery Time		t_{rr}	I_F = 11.5A, dI/dt = 100A/μs	-	28	-	nS
Body Diode Reverse Recovery Charge		Q_{rr}		-	120	-	nC

Notes:

1.The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%

3. The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.3mH,I_{AS}=9A

4.The power dissipation is limited by 150℃ junction temperature

5. The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

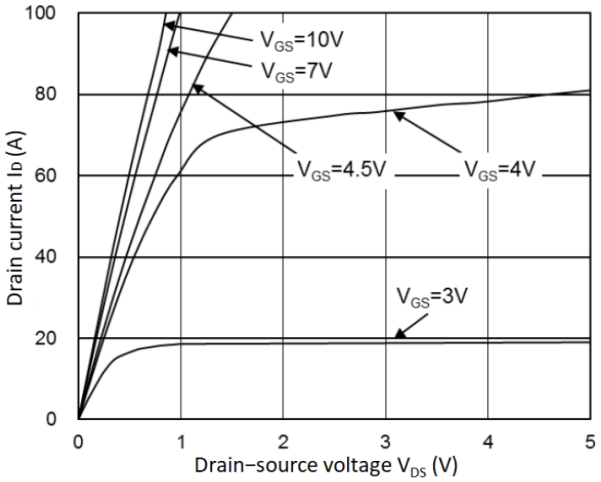


Figure 1. Typical Output Characteristics

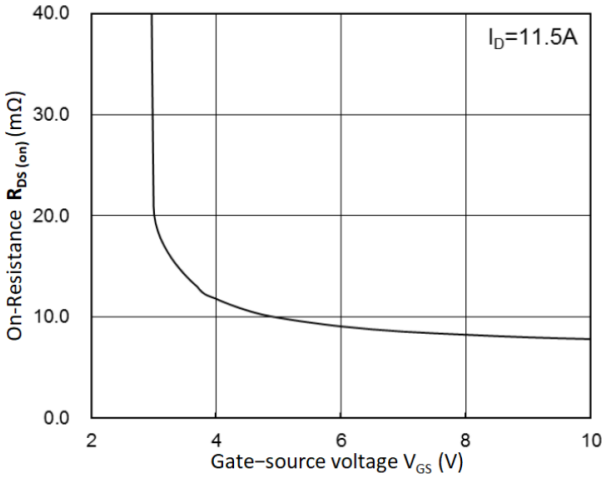


Figure 2. $R_{DS(on)}$ vs. V_{GS}

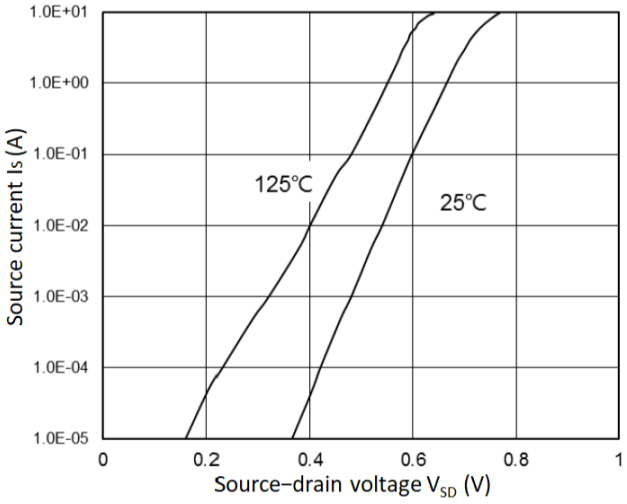


Figure 3. Forward Characteristics of Reverse

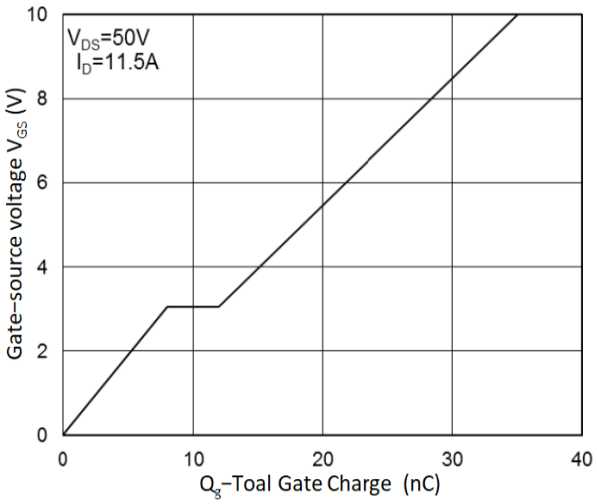


Figure 4. Gate Charge Characteristics

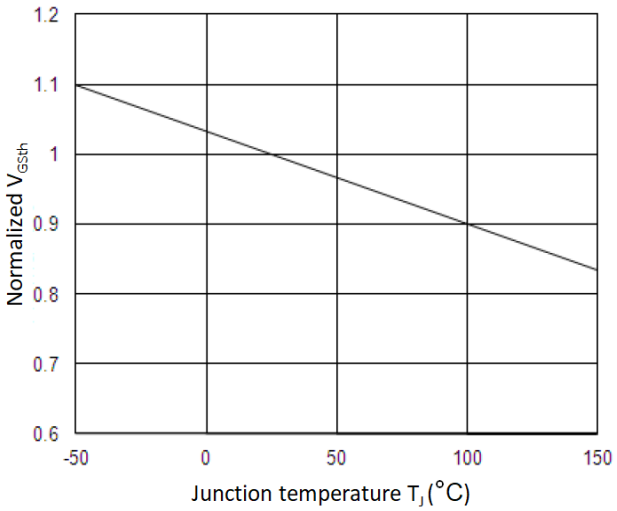


Figure 5. Normalized $V_{GS(th)}$ vs. T_J

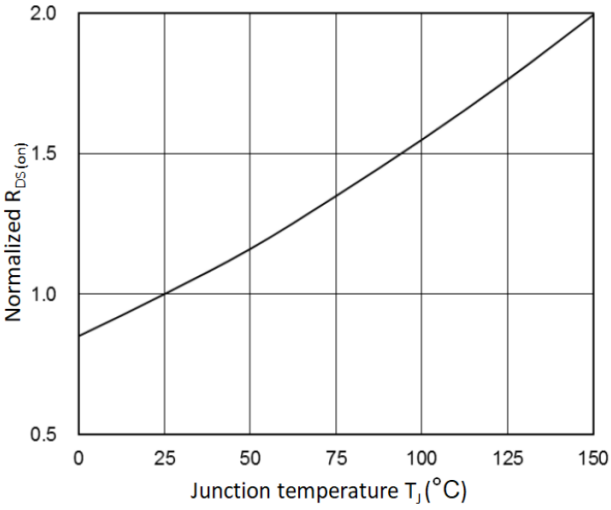


Figure 6. Normalized $R_{DS(ON)}$ vs. T_J

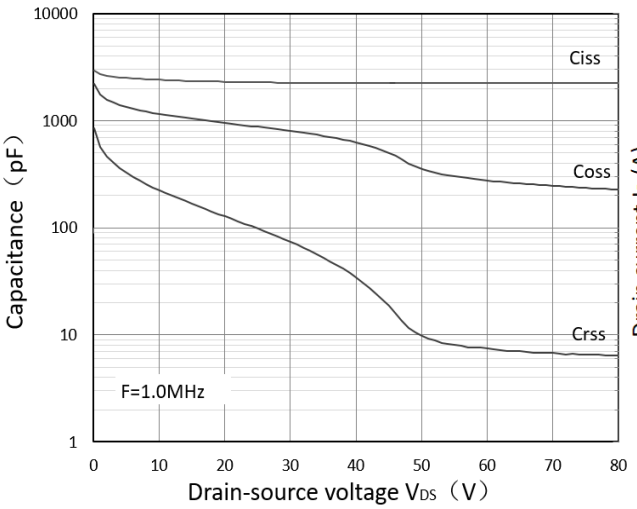


Figure 7. Capacitance Characteristics

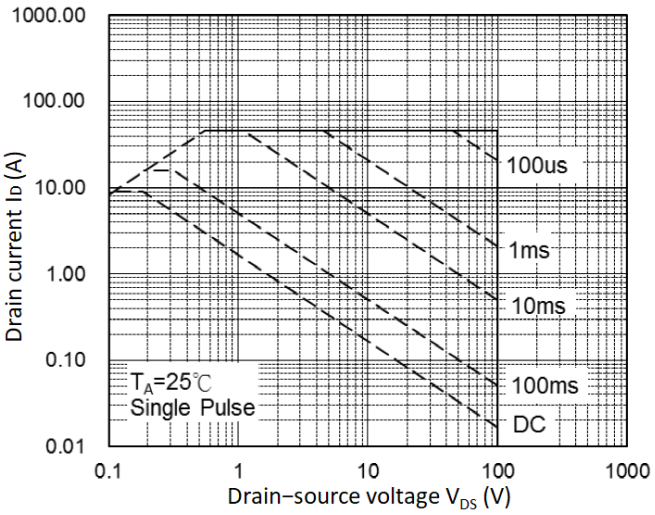


Figure 8. Safe Operating Area

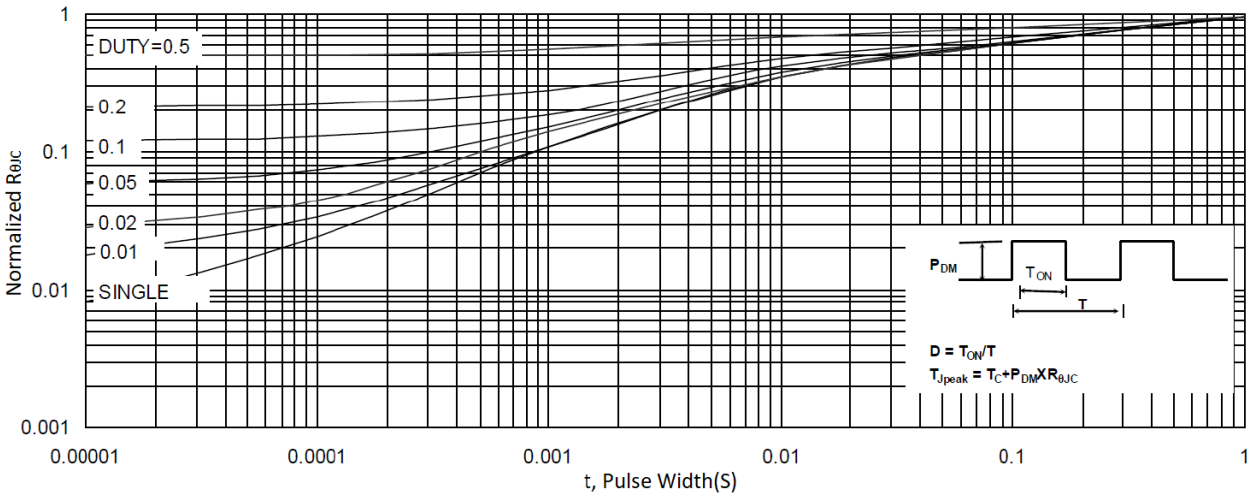


Figure 9. Normalized Maximum Transient Thermal Impedance

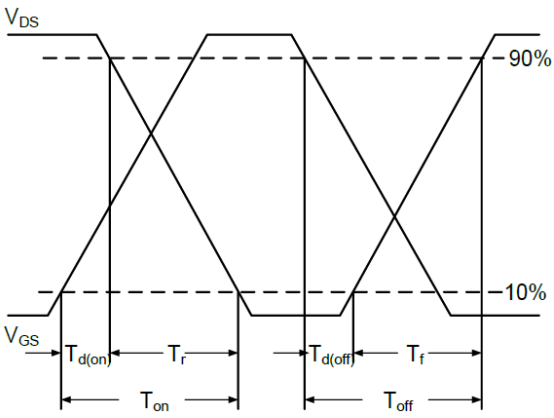


Figure 10. Switching Time Waveform

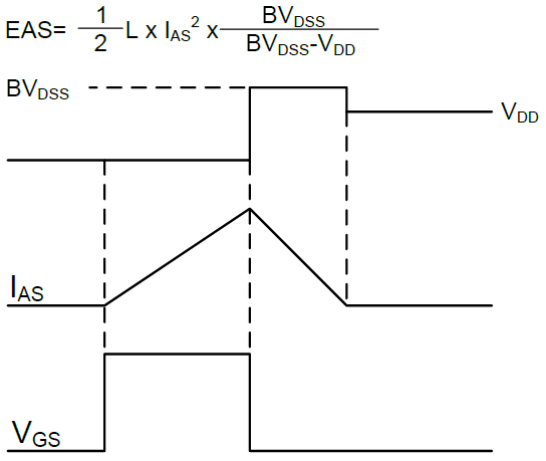
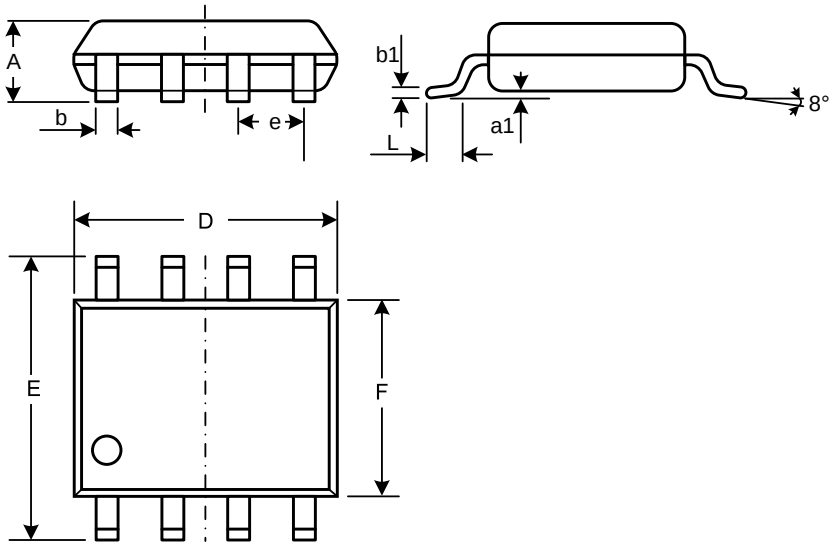


Figure 11. Unclamped Inductive Switching Waveform

Mechanical Dimensions for SOP-8L



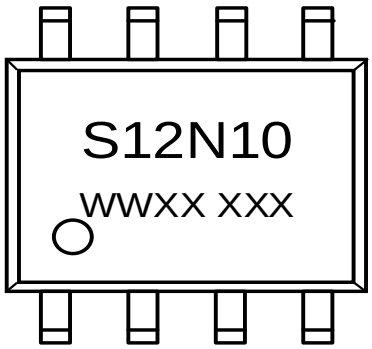
COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	1.23	1.75
a1	0.05	0.25
b	0.31	0.51
b1	0.16	0.25
D	4.70	5.15
E	5.75	6.25
e	1.07	1.47
F	3.70	4.10
L	0.4	1.27

Ordering Information

Part	Package	Marking	Packing method
WMS12N10T2	SOP-8L	S12N10	Tape and Reel

Marking Information



S12N10 = Device code

WWXX XXX= Date code

Contact Information

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