

# 30V P-Channel Enhancement Mode Power MOSFET

# **Description**

WMS14P03T1 uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

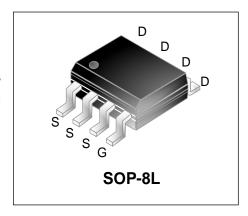
### **Features**

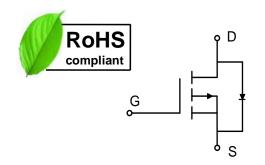
- $V_{DS}$ = -30V,  $I_{D}$  = -14A  $R_{DS(on)}$  < 9.2m $\Omega$  @  $V_{GS}$  = -10V  $R_{DS(on)}$  < 14m $\Omega$  @  $V_{GS}$  = -4.5V
- High Power and Current Handing Capability
- Low Gate Charge

## **Applications**

- Power Management Switches
- Battery Protection Applications







Parameter		Symbol	Value	Unit	
Drain-Source voltage		V <sub>DS</sub>	-30	V	
Gate-Source voltage		V <sub>GS</sub>	±20	V	
Continuous Proin Current® 40V1	T <sub>A</sub> =25°C		-14	А	
Continuous Drain Current@-10V <sup>1</sup>	T <sub>A</sub> =70°C	l <sub>D</sub>	-11		
Pulsed Drain Current <sup>2</sup>		I <sub>DM</sub>	-56	А	
Single Pulse Avalanche Energy <sup>3</sup>		EAS	151	mJ	
Avalanche Current		I <sub>AS</sub>	-55	А	
Total Power Dissipation <sup>4</sup>	T <sub>A</sub> =25°C	P <sub>D</sub>	1.5	W	
Operating Junction and Storage Temperature Range		Тл, Тата	-55 to+150	°C	

#### **Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient <sup>1</sup>	Reja	75	°C/W
Thermal Resistance from Junction-to-Case <sup>1</sup>	R <sub>0</sub> JC	24	°C/W



#### Electrical Characteristics T<sub>c</sub> = 25°C, unless otherwise noted

Parameter		Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static Characteristics				•	•		
Drain-Source Breakdown Voltage		V <sub>(BR)DSS</sub>	$V_{GS} = 0V, I_D = -250\mu A$	-30	-	-	V
Gate-body Leakage current		Igss	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
Zero Gate Voltage Drain Current	T <sub>J</sub> =25°C	- I <sub>DSS</sub>	$V_{DS} = -24V, V_{GS} = 0V$	-	-	-1	μΑ
	T <sub>J</sub> =55°C			-	-	-5	
Gate-Threshold Voltage		V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1.0	-	-2.5	V
Drain-Source On-Resistance <sup>2</sup>		_	V <sub>GS</sub> = -10V, I <sub>D</sub> = -12A	-	7.6	9.2	mΩ
		R <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -10A	-	11	14	
Forward Transconductance		<b>G</b> fs	V <sub>DS</sub> = -5V, I <sub>D</sub> = -12A	-	25	-	S
Dynamic Characteristics							
Input Capacitance		Ciss		-	3448	-	
Output Capacitance		Coss	$V_{DS} = -15V$ , $V_{GS} = 0V$ , $f = 1MHz$	-	445	-	pF
Reverse Transfer Capacitano	е	C <sub>rss</sub>		-	325	-	
Switching Characteristic	s						
Total Gate Charge		Qg			30	-	nC
Gate-Source Charge		Q <sub>gs</sub>	$V_{GS} = -4.5V, V_{DS} = -15V$ $I_{D} = -12A$	-	10	-	
Gate-Drain Charge		$\mathbf{Q}_{gd}$		-	10.4	-	
Turn-On Delay Time		t <sub>d(on)</sub>		-	9.4	-	
Rise Time Turn-Off Delay Time		tr	$V_{GS}$ =-10V, $V_{DD}$ = -15V $R_{G}$ = 3.3 $\Omega$ , $I_{D}$ = -1A	-	10.2	-	nS
		t <sub>d(off)</sub>		-	117	-	
Fall Time		t <sub>f</sub>		-	24	-	
Drain-source body diode	Character	istics					
Diode Forward Voltage <sup>2</sup>		V <sub>SD</sub>	I <sub>S</sub> = -1A, V <sub>GS</sub> = 0V	-	-	-1.2	V
Continuous Source Current <sup>1,5</sup>		Is	Vg=VD=0V , Force Current	-	-	-14	Α
Reverse Recovery Time t <sub>rr</sub>		t <sub>rr</sub>		-	19.4	-	nS
Reverse Recovery Charge		Qrr	I <sub>F</sub> = -10A, dI/dt = 100A/μs	-	9.1	-	nC

#### Notes:

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%
- 3. The EAS data shows Max. rating . The test condition is  $V_{DD}$ = -25V,  $V_{GS}$ = -10V, L=0.1mH,  $I_{AS}$ = -55A
- 4.The power dissipation is limited by 150°C junction temperature
- 5. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



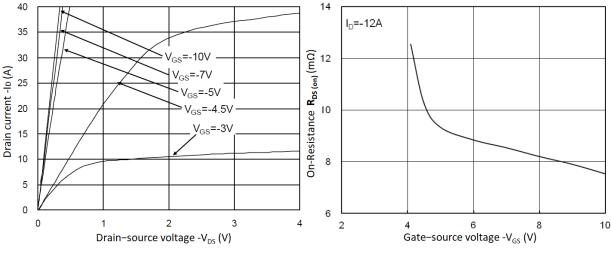


Figure 1. Typical Output Characteristics

Figure 2.  $R_{DS(on)}$  vs.  $V_{GS}$ 

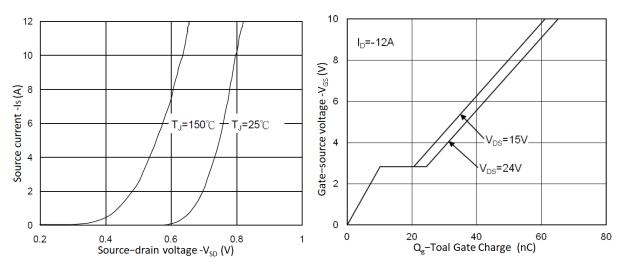


Figure 3. Forward Characteristics of Reverse

Figure 4. Gate Charge Characteristics

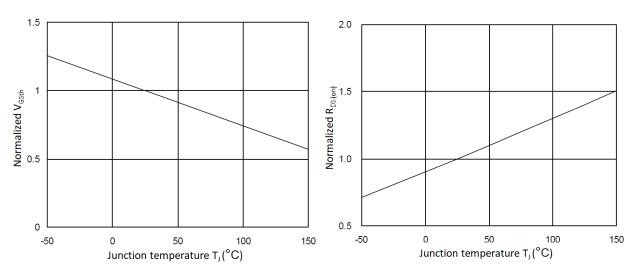


Figure 5. Normalized  $V_{\text{GS(th)}}$  vs.  $T_{\text{J}}$ 

Figure 6. Normalized R<sub>DS(ON)</sub> vs. T<sub>J</sub>



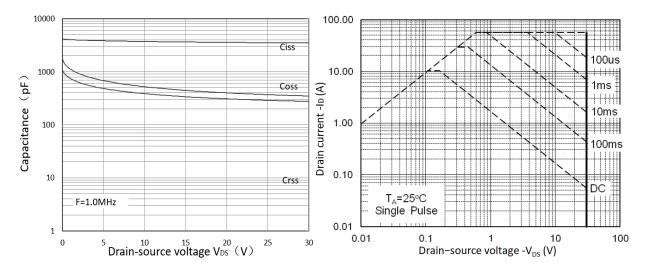


Figure 7. Capacitance Characteristics

Figure8. Safe Operating Area

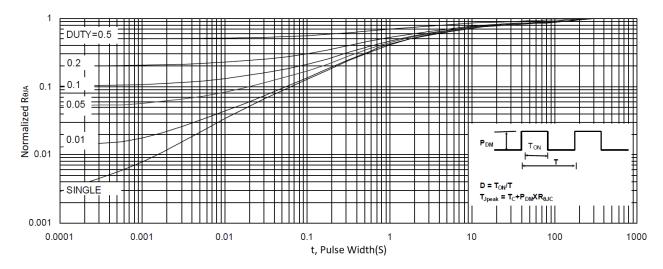


Figure 9. Normalized Maximum Transient Thermal Impedance

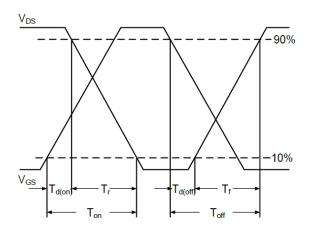


Figure 10. Switching Time Waveform

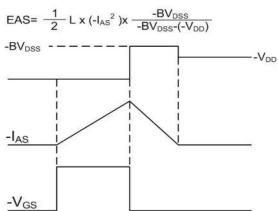
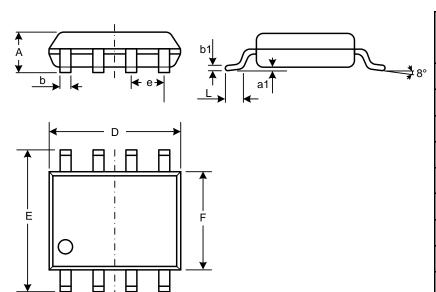


Figure 11. Unclamped Inductive Switching

Waveform



#### **Mechanical Dimensions for SOP-8L**



#### **COMMON DIMENSIONS**

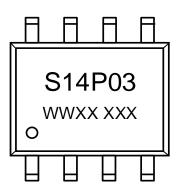
	MM			
SYMBOL	MIN	MAX		
А	1.23	1.75		
a1	0.05	0.25		
b	0.31	0.51		
b1	0.16	0.25		
D	4.70	5.15		
E	5.75	6.25		
е	1.07	1.47		
F	3.70	4.10		
L	0.4	1.27		



### **Ordering Information**

Part	Package	Marking	Packing method	
WMS14P03T1	SOP-8L	S14P03	Tape and Reel	

#### **Marking Information**



S14P03 = Device code WWXX XXX= Date code

### **Contact Information**

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