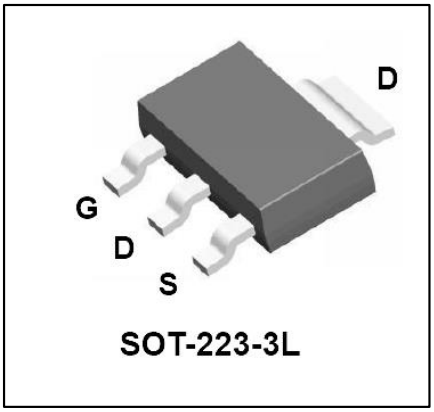
WMT04N15T1

150V N-Channel Enhancement Mode Power MOSFET



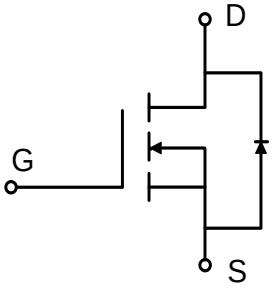
Description

WMT04N15T1 uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Features

- V_{DS}= 150V, I_D = 4A
  $R_{DS(on)}$ < 160mΩ @ V_{GS} = 10V
- High Density Cell Design for Ultra Low Rdson
- Fully Characterized Avalanche Voltage and Current
- Excellent Package for Good Heat Dissipation

RoHS compliant



Applications

- Power Switching Application
- Hard Switched and High Frequency Circuits

Absolute Maximum Ratings

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	150	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current[1]	T_C=25℃	I_D	4	A
Pulsed Drain Current[2]		I_{DM}	16	A
Single Pulse Avalanche Energy[3]		EAS	8	mJ
Total Power Dissipation[4]	T_C=25℃	P_D	2.9	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to+150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient[1]	$R_{\theta JA}$	80	°C/W

Electrical Characteristics T_c = 25°C, unless otherwise noted

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	150	-	-	V
Gate-body Leakage current		I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T_J=25℃	I_{DSS}	V_{DS} = 150V, V_{GS} = 0V	-	-	1	μA
Gate-Threshold Voltage		$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.5	2.0	2.5	V
Drain-Source On-Resistance[2]		$R_{DS(on)}$	V_{GS} = 10V, I_D = 4A	-	125	160	mΩ
Dynamic Characteristics							
Input Capacitance		C_{iss}	V_{DS} = 25V, V_{GS} =0V, f =1MHz	-	883	-	pF
Output Capacitance		C_{oss}		-	53	-	
Reverse Transfer Capacitance		C_{rss}		-	37	-	
Switching Characteristics							
Gate Resistance		R_g	V_{DS} =0V, V_{GS} =0V, f =1MHz	-	1.1	-	Ω
Total Gate Charge		Q_g	V_{GS} = 10V,V_{DS} = 75V, I_D= 1.5A	-	18.5	-	nC
Gate-Source Charge		Q_{gs}		-	5.3	-	
Gate-Drain Charge		Q_{gd}		-	6.9	-	
Turn-On Delay Time		$t_{d(on)}$	V_{GS} =10V, V_{DS} = 75V R_G = 6Ω, I_D= 1A,R_L= 75Ω	-	7.9	-	nS
Rise Time		t_r		-	9.8	-	
Turn-Off Delay Time		$t_{d(off)}$		-	19.5	-	
Fall Time		t_f		-	14.7	-	
Drain-Source Body Diode Characteristics							
Diode Forward Voltage[2]		V_{SD}	I_S = 2A, V_{GS} = 0V	-	-	1.2	V
Continuous Source Current[1,5]		I_S	V_G=V_D=0V , Force Current	-	-	4	A

Notes:

1.The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%

3.The EAS data shows Max. rating . The test condition is V_{DD}=25V, V_{GS}=10V, L=1mH. I_{AS}=4A

4.The power dissipation is limited by 150℃ junction temperature

5. The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

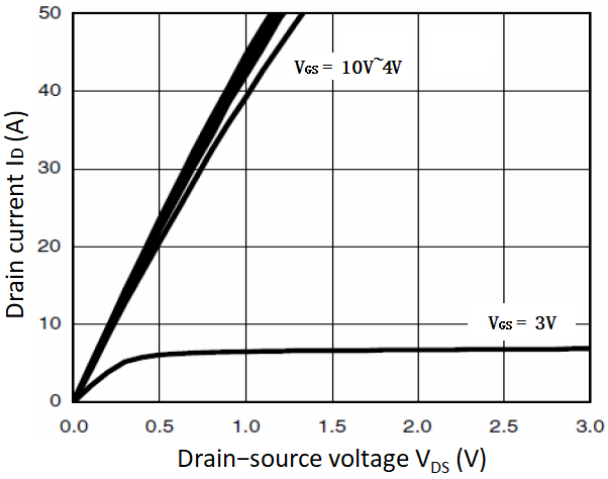


Figure 1. Typical Output Characteristics

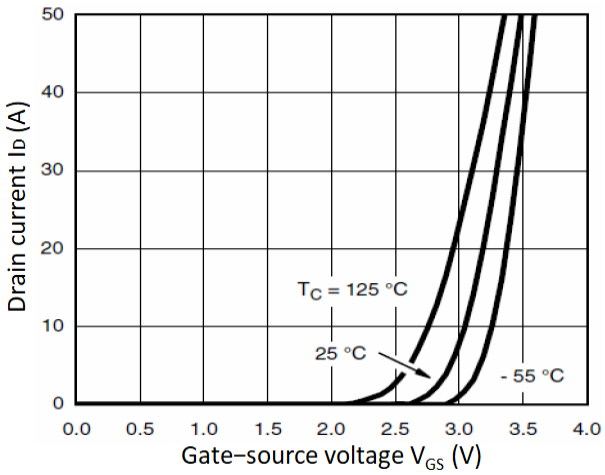


Figure 2. Transfer Characteristics

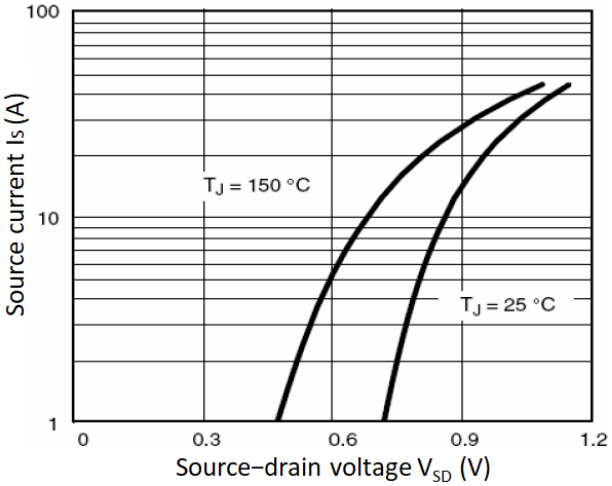


Figure 3. Forward Characteristics of Reverse

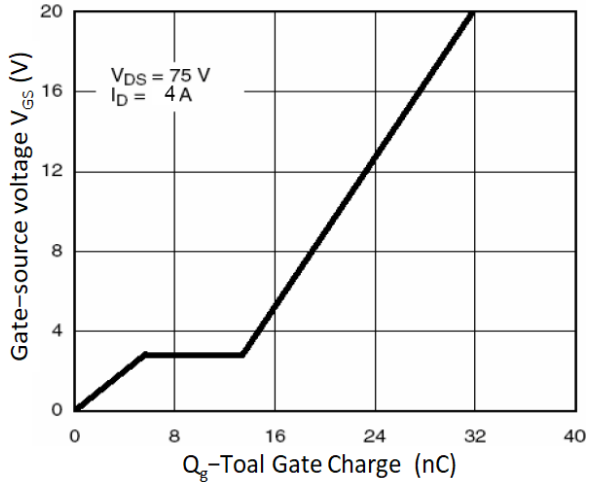


Figure 4. Gate Charge Characteristics

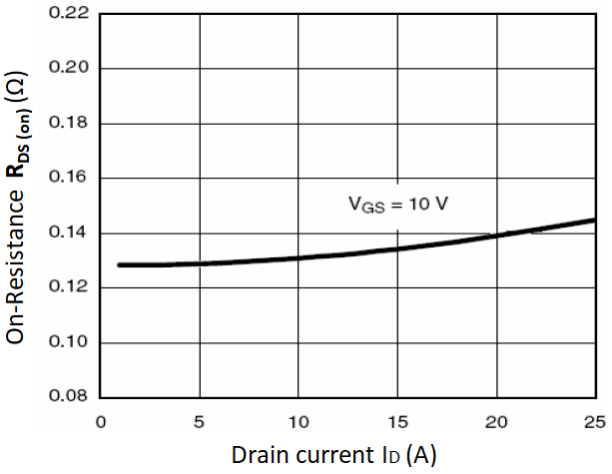


Figure 5. $R_{DS(on)}$ vs. I_D

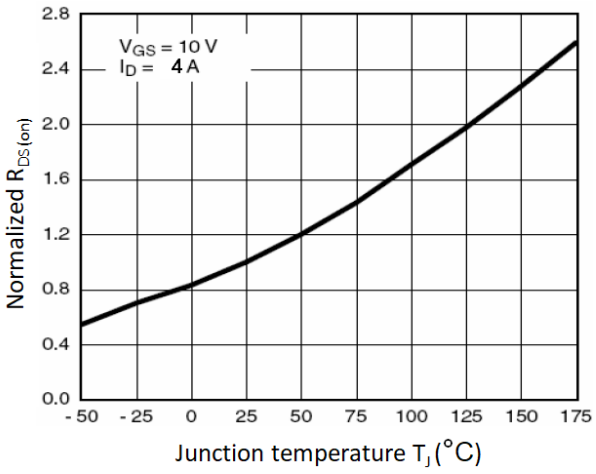


Figure 6. Normalized $R_{DS(ON)}$ vs. T_J

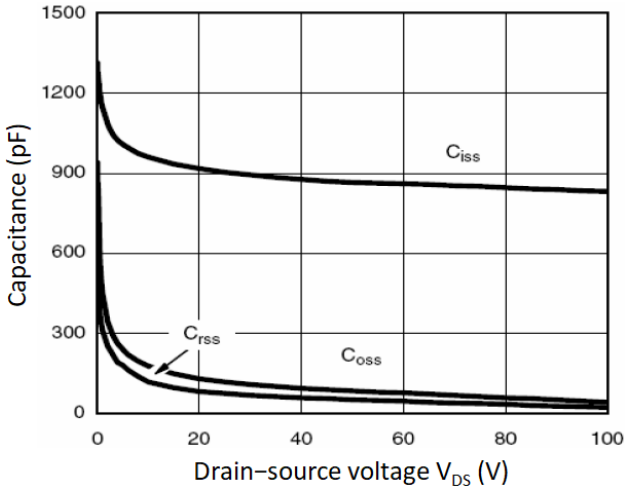


Figure 7. Capacitance Characteristics

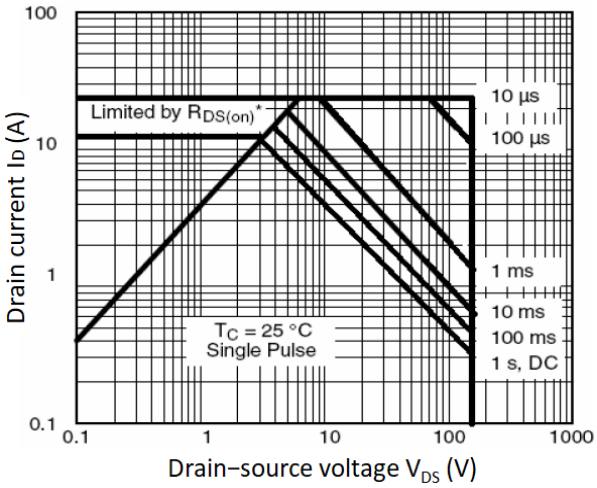


Figure 8. Safe Operating Area

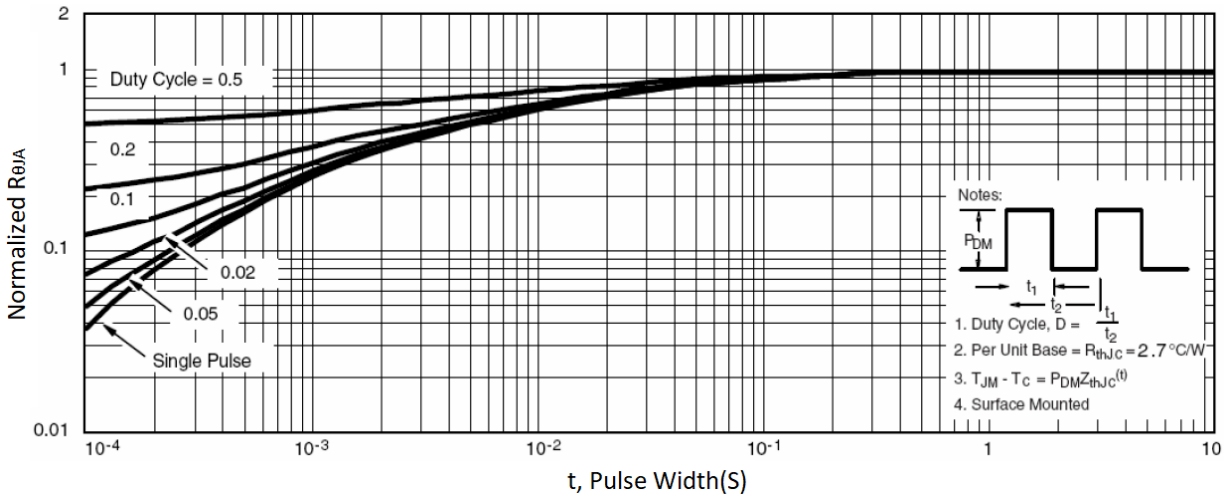


Figure 9. Normalized Maximum Transient Thermal Impedance

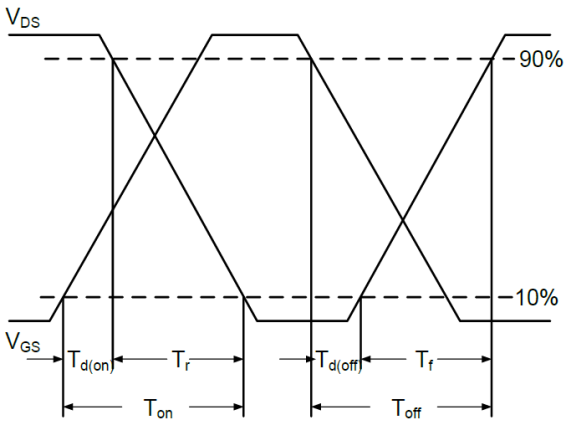


Figure 10. Switching Time Waveform

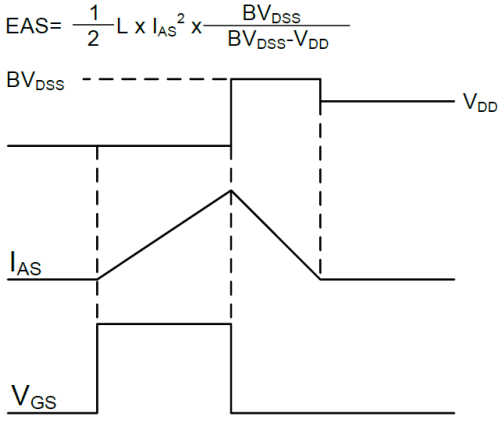
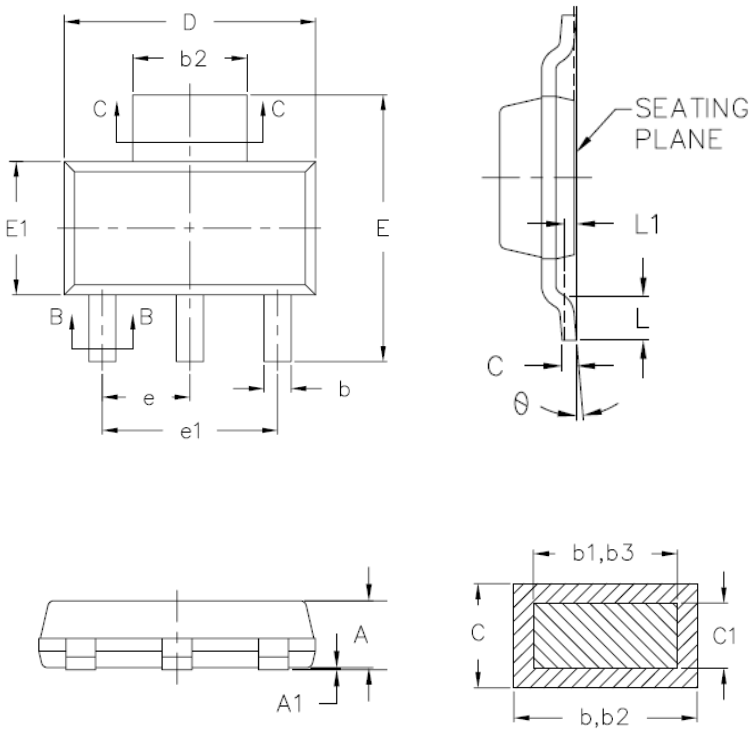


Figure 11. Unclamped Inductive Switching Waveform

Mechanical Dimensions for SOT-223-3L



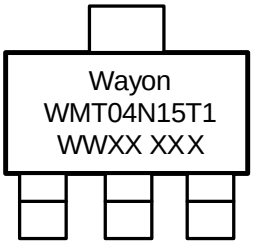
COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	-	1.80
A1	0.02	0.10
b	0.66	0.84
b1	0.60	0.79
b2	2.90	3.10
b3	2.84	3.05
c	0.23	0.35
c1	0.23	0.33
D	6.20	6.70
E	6.70	7.30
E1	3.30	3.70
e	2.30BSC	
e1	4.60BSC	
L	0.80	-
L1	0.25BSC	
θ	0°	10°

Ordering Information

Part	Package	Marking	Packing method
WMT04N15T1	SOT-223-3L	WMT04N15T1	Tape and Reel

Marking Information



WMT04N15T1 = Device code

WWXX XXX= Date code

Contact Information

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